

FIG. 1

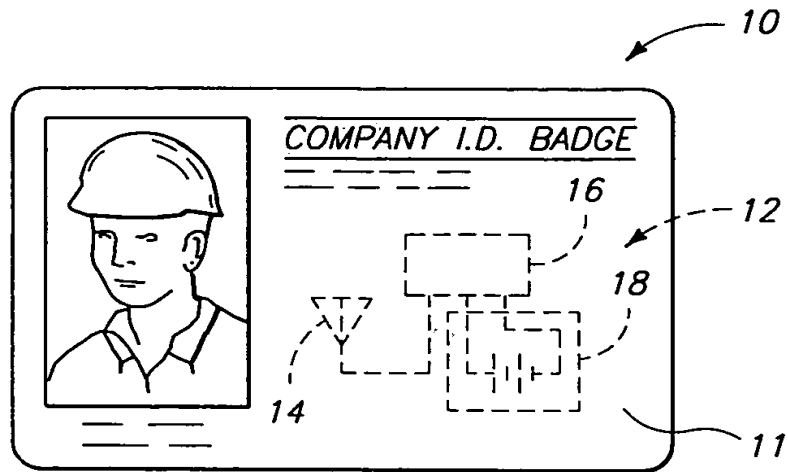
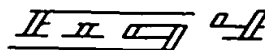
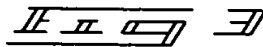


FIG. 2



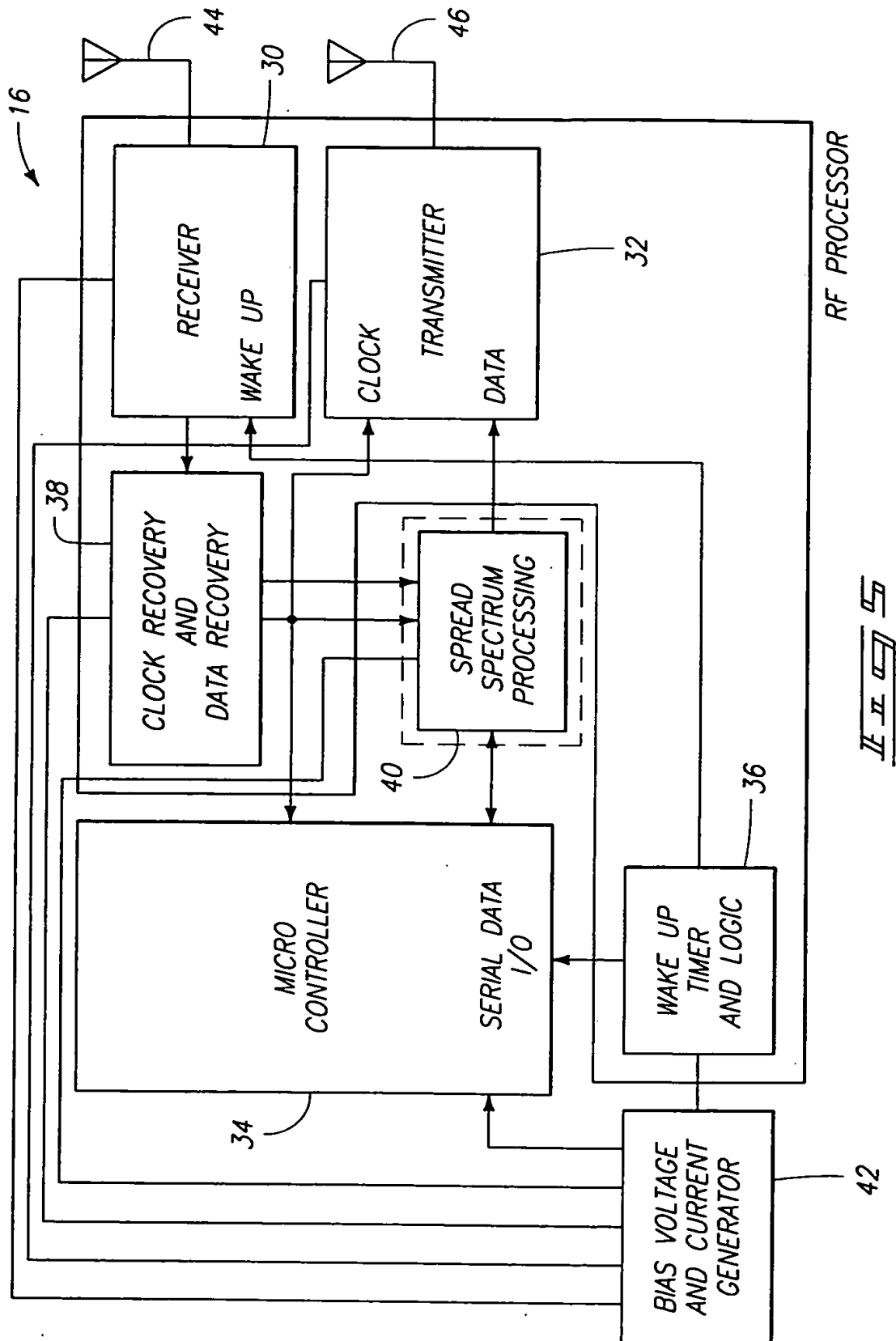


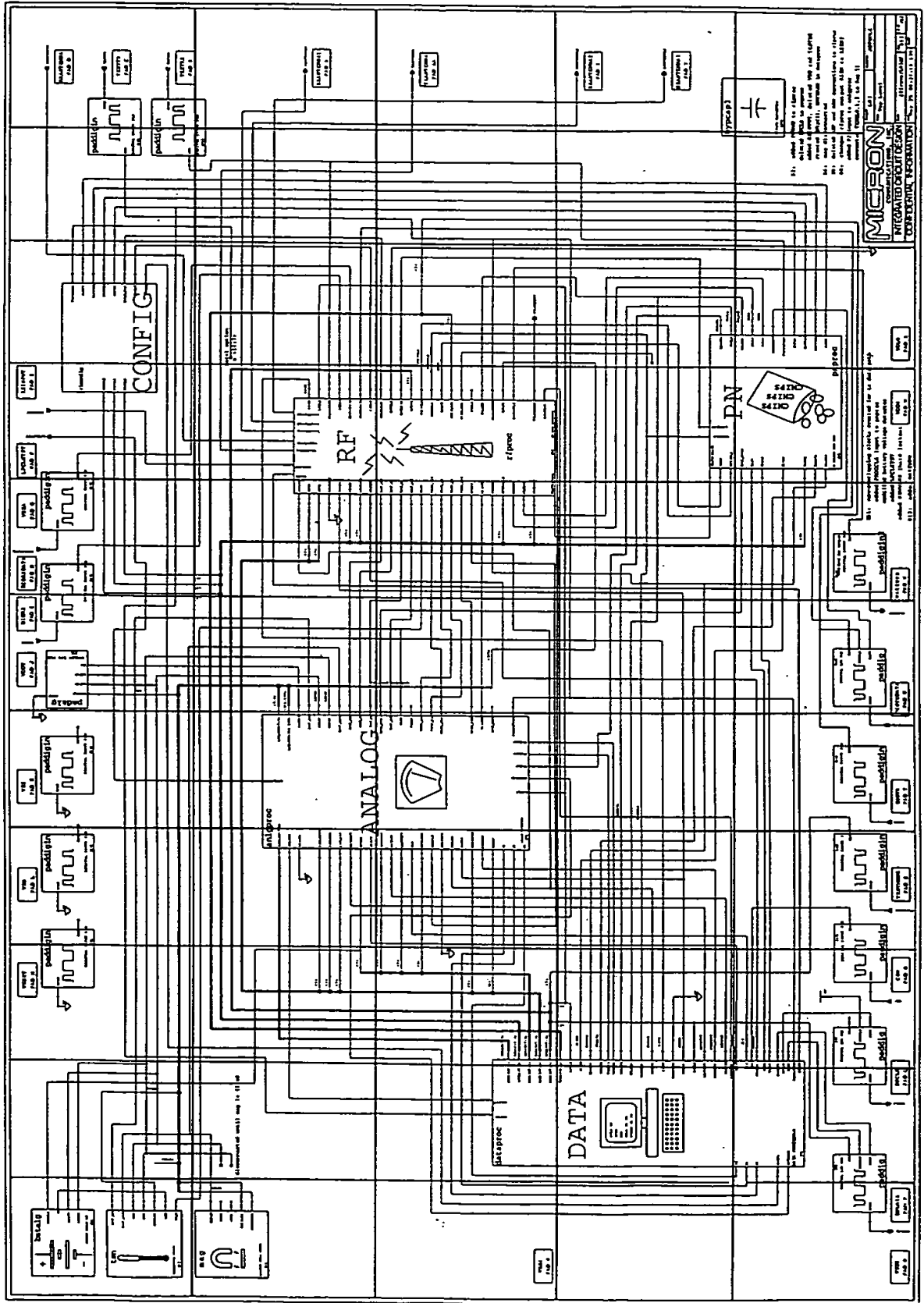
FIG. 1

6AA	6AB	6AC	6AD	6AE	6AF	6AG	6AH	6AI	6AJ	6AK
6BA	6BB	6BC	6BD	6BE	6BF	6BG	6BH	6BI	6BJ	6BK
6CA	6CB	6CC	6CD	6CE	6CF	6CG	6CH	6CI	6CJ	6CK
6DA	6DB	6DC	6DD	6DE	6DF	6DG	6DH	6DI	6DJ	6DK
6EA	6EB	6EC	6ED	6EE	6EF	6EG	6EH	6EI	6EJ	6EK

11 11 11

CONFIDENTIAL

FIG. 6AA-EK



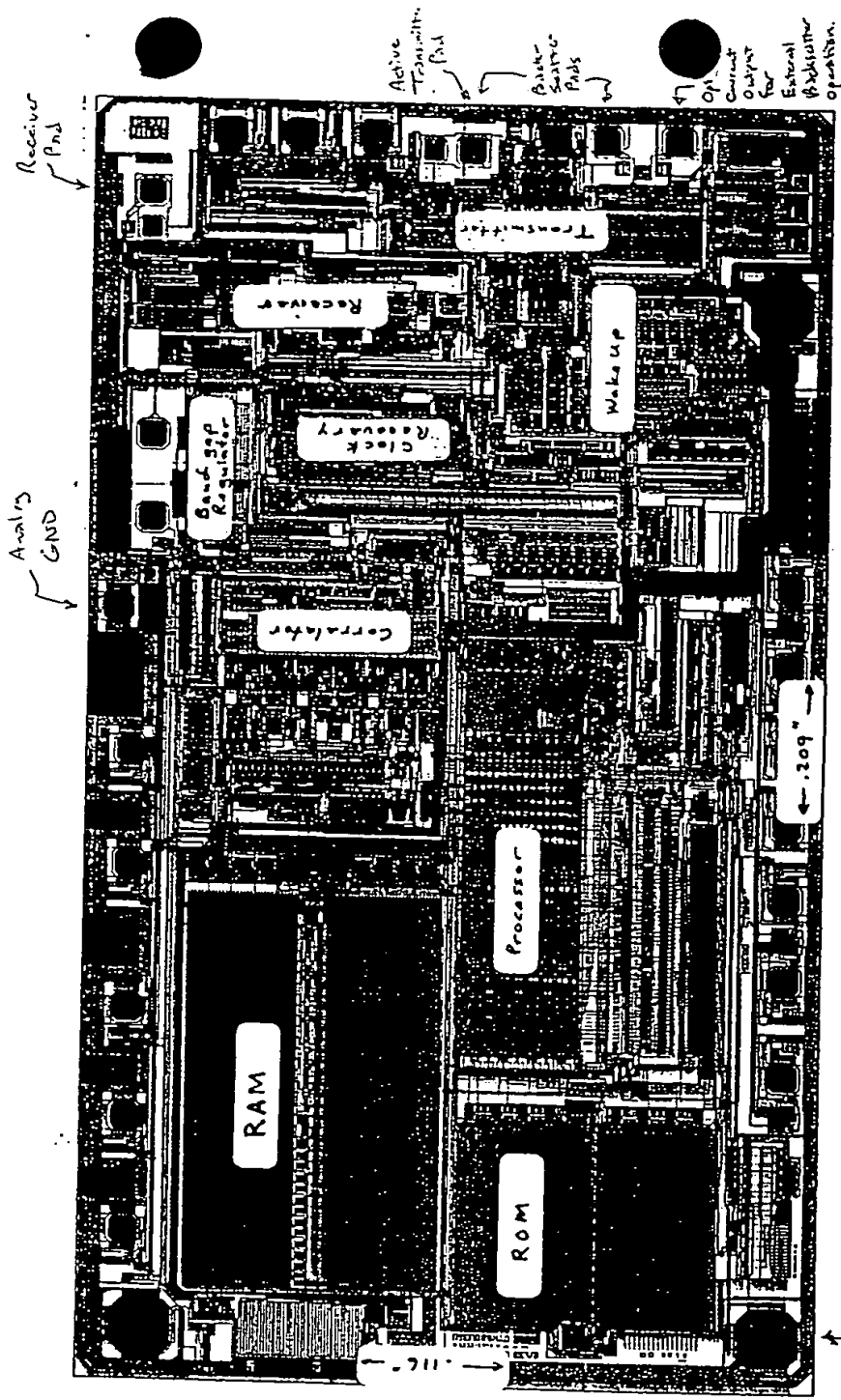
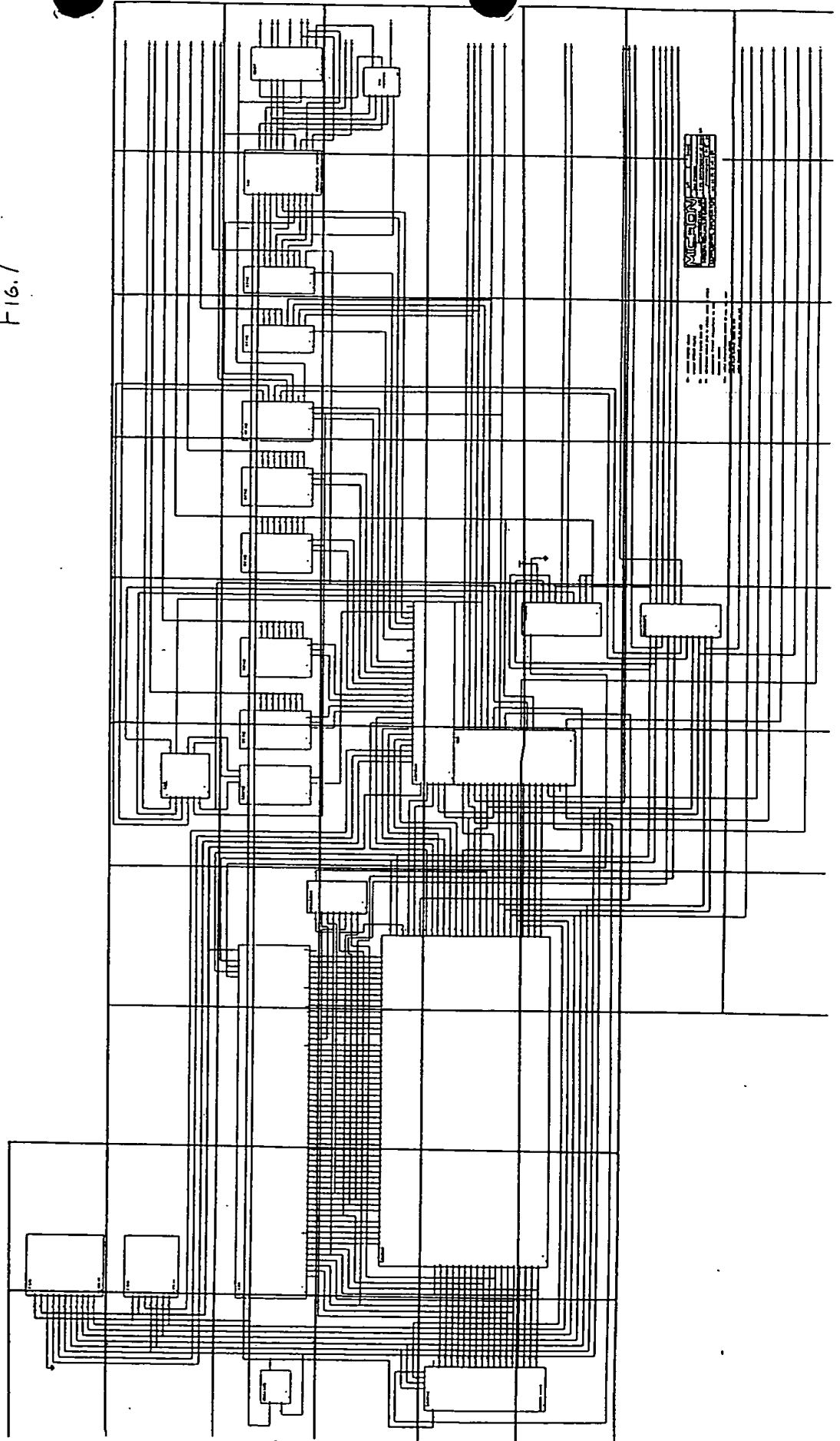


FIG. 6.01

BEST AVAILABLE COPY

[illegible]
$$\frac{\pi}{\pi + \pi}$$

Fig. 7



004420 20920900

MI40-030

7.01AA	7.01AB
7.01BA	7.01BB

II II 7.011

[illegible]

MICRON		CONFIDENTIAL	
COMMUNICATIONS, INC.		CONFIDENTIAL INFORMATION	
INTEGRATED CIRCUIT DESIGN			
Processor Clock Generator			
2-Phase/4-State/8-Cycle			
part no.	103rev0/cbk	rev	011
date	Apr 13 09:58:52 1996		
CONFIDENTIAL INFORMATION			
revision	L03	part number	Rotcoll

B2: created ENUp11 signal
added ENVREG logic

B11: added hard lockout to c1kst

[illegible]

MI40-030

7.0101AA	7.0101AB
7.0101BA	7.0101BB

Ихъ и въ П.М.П.

004420 20900000

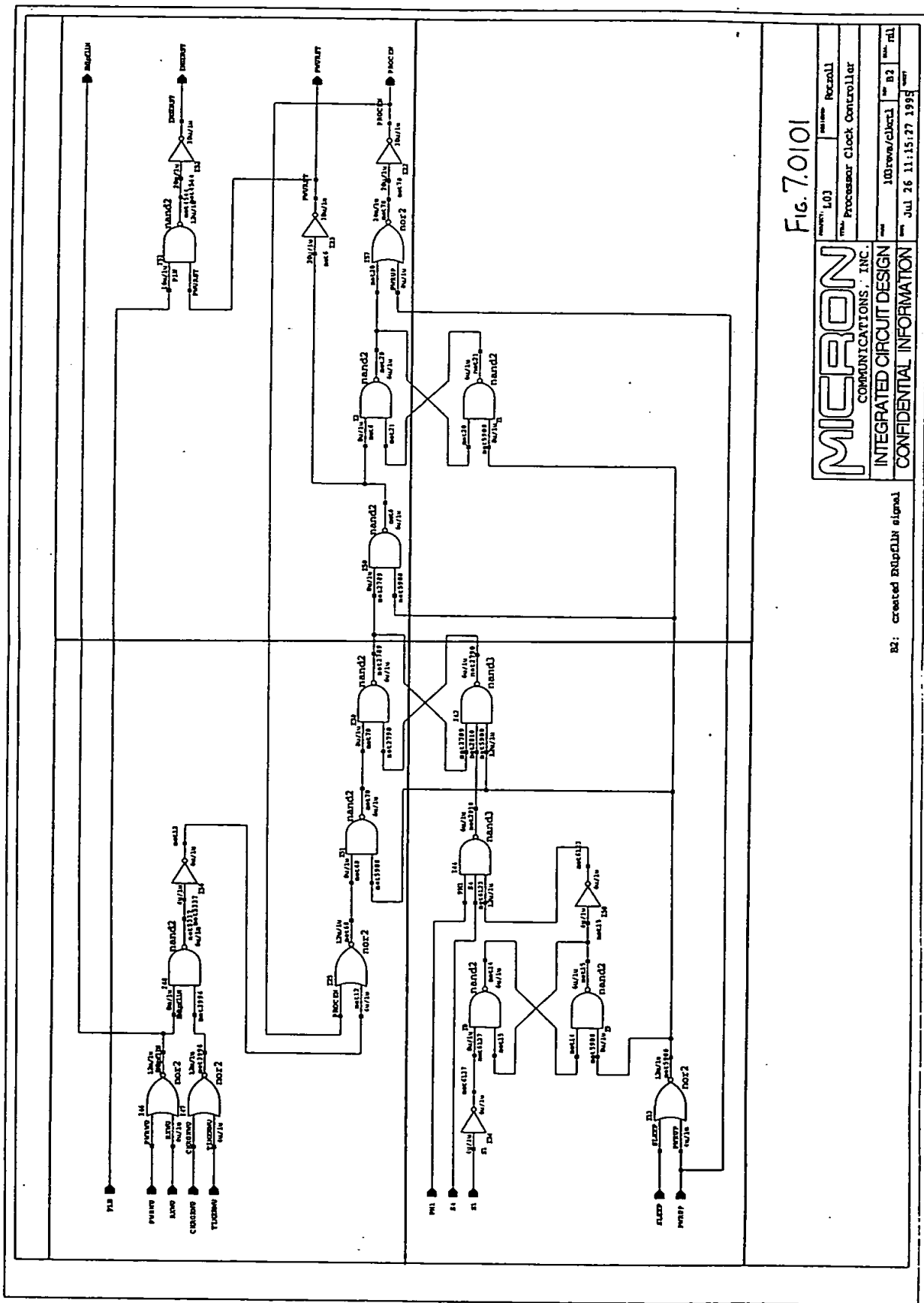


Fig. 7.0101

MICRON		COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN		CONFIDENTIAL INFORMATION	
PROJECT: L03	REVISION: Rev001	DATE: 10/19/90	BY: B2
Processor Clock Controller		DATE: Jul 26 11:15:27 1999	BY: B2

B2: created EXDCLIN signal

7.0102BA	7.0102BB	7.0102BC	7.0102BD	7.0102BE	7.0102BF	7.0102AG	7.0102AH	7.0102AI	7.0102AJ
7.0102CA	7.0102CB	7.0102CC	7.0102CD	7.0102CE	7.0102CF	7.0102CG	7.0102CH	7.0102CI	7.0102CJ
7.0102DA	7.0102DB	7.0102DC	7.0102DD	7.0102DE	7.0102DF	7.0102DG	7.0102DH	7.0102DI	7.0102DJ

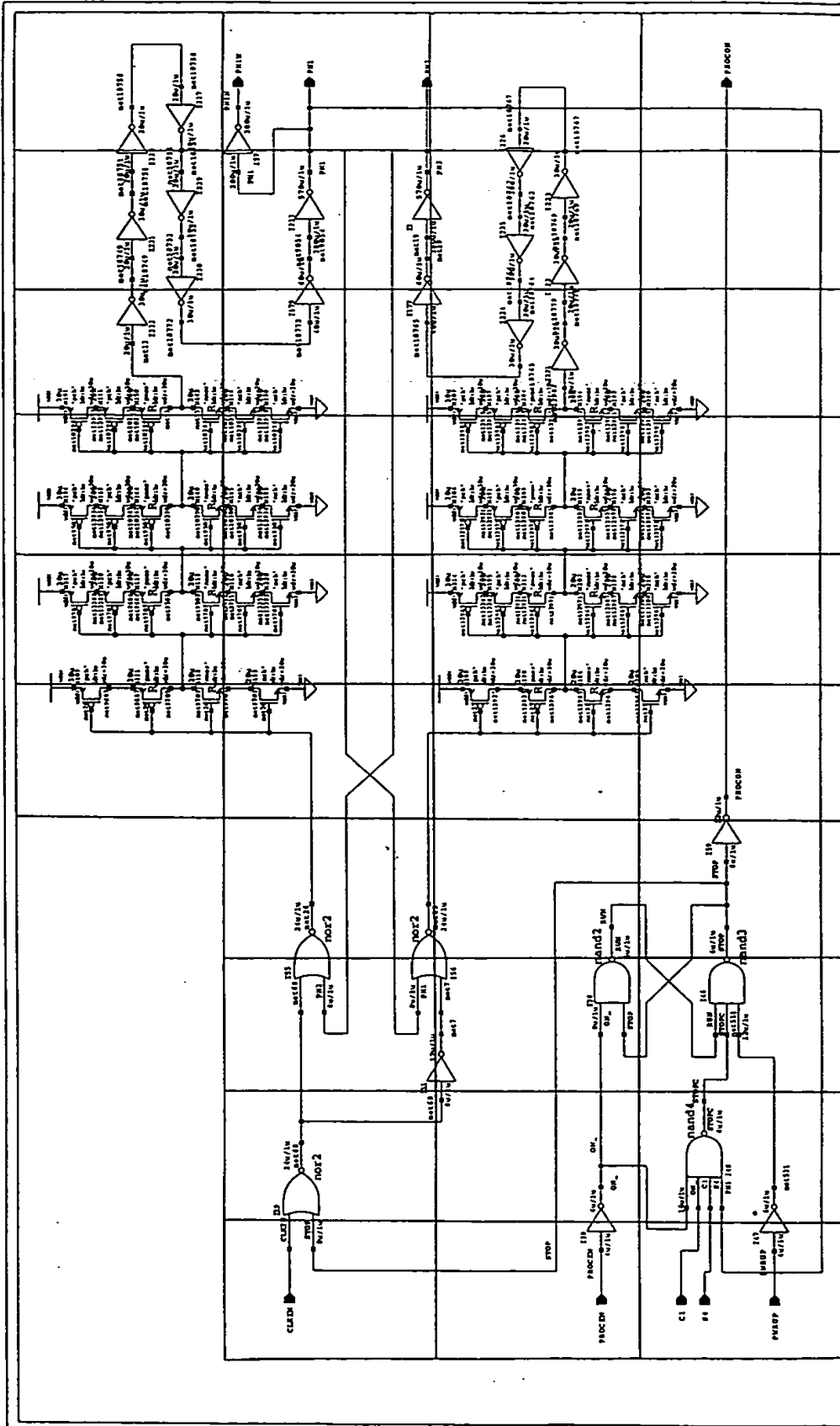


Fig. 7.0102

MICRON		PRODUCT: L03	REVISION: 1000000
COMMUNICATIONS, INC.		Phase Processor Phase Generator	
INTEGRATED CIRCUIT DESIGN		2-Phase Non-overlapping	
CONFIDENTIAL INFORMATION		DATE: 10/28/88	BY: B8
		DEC 5 17:55:56 1995	REV: 1

B2: Pin name changes
 B8: Added 6 inverters to non-overlap time
 Make the number of inverters adjustable on metal

007420 20920500

MI40-030

7.0103AA	7.0103AB	7.0103AC	7.0103AD
7.0103BA	7.0103BB	7.0103BC	7.0103BD

IL 11 11 11 11

Abstract



B2: created ENUPfill signal
added ENUPREQ logic

B11: added hard lockout enable logic

```

B2: created ENUPFLL signal
    added ENTRMREQ logic
B11: added hard lockout en

```


007420 20920500

MI40-030

7.010301AA	7.010301AB
7.010301BA	7.010301BB

ILX 11111111

001420 2020300

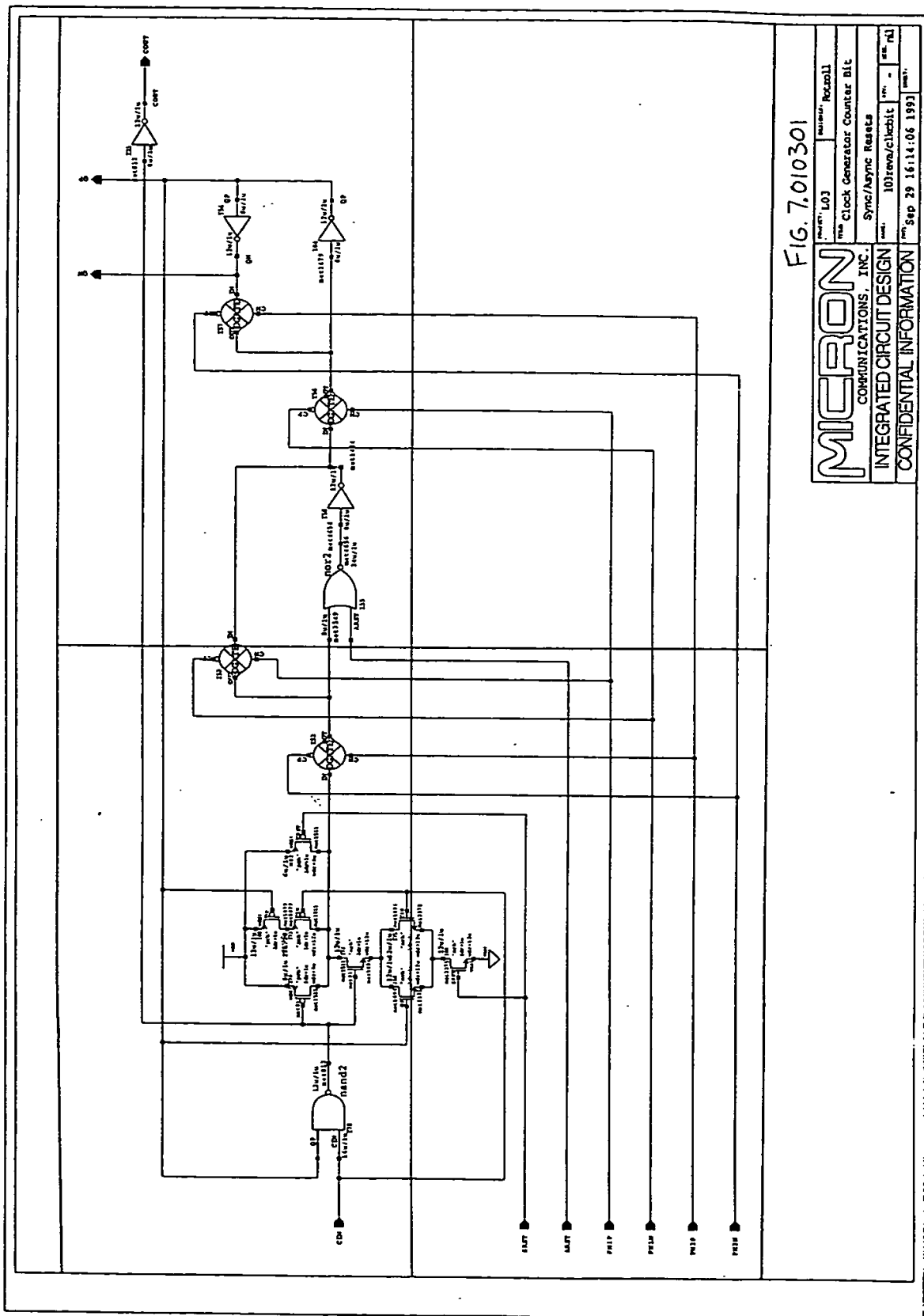


FIG. 7.010301

MICRON		Part No. L03	Revision	Roll
COMMUNICATIONS, INC.		Clock Generator Counter Bit		
INTEGRATED CIRCUIT DESIGN		Sync/Async Resets		
CONFIDENTIAL INFORMATION		Rev.	101revs/clockbit	Rev. - n1
		Date	Sep 29 16:14:06 1993	

001120 20320540

MI40-030

7.02AA	7.02AB	7.02AC	7.02AD	7.02AE	7.02AF
7.02BA	7.02BB	7.02BC	7.02BD	7.02BE	7.02BF

11.09 11.02



•

14-00000

00000000000000000000

MI40-030

7.03AA	7.03AB	7.03AC	7.03AD	7.03AE	7.03AF	7.03AG	7.03AH
7.03BA	7.03BB	7.03BC	7.03BD	7.03BE	7.03BF	7.03BG	7.03BH
7.03CA	7.03CB	7.03CC	7.03CD	7.03CE	7.03CF	7.03CG	7.03CH
7.03DA	7.03DB	7.03DC	7.03DD	7.03DE	7.03DF	7.03DG	7.03DH
7.03EA	7.03EB	7.03EC	7.03ED	7.03EE	7.03EF	7.03EG	7.03EH

И. П. М.

This technical drawing illustrates a complex electronic circuit board, possibly a multi-layer printed circuit board (PCB). The layout is highly organized, featuring a dense grid of components and intricate interconnecting traces. The board is oriented vertically, with a series of pins or connectors visible along the top edge. The central area is dominated by a large, dense array of components, likely integrated circuits (ICs) and resistors, which are interconnected by a complex network of traces. The drawing is divided into several horizontal sections, with a central area containing a large, dense array of components. The overall design suggests a high-performance, multi-functional electronic system.

McGraw-Hill

00420-00000000

MI40-030

7.0301AA	7.0301AB
7.0301BA	7.0301BB

MEMO

SECRET

RAM CONTROL

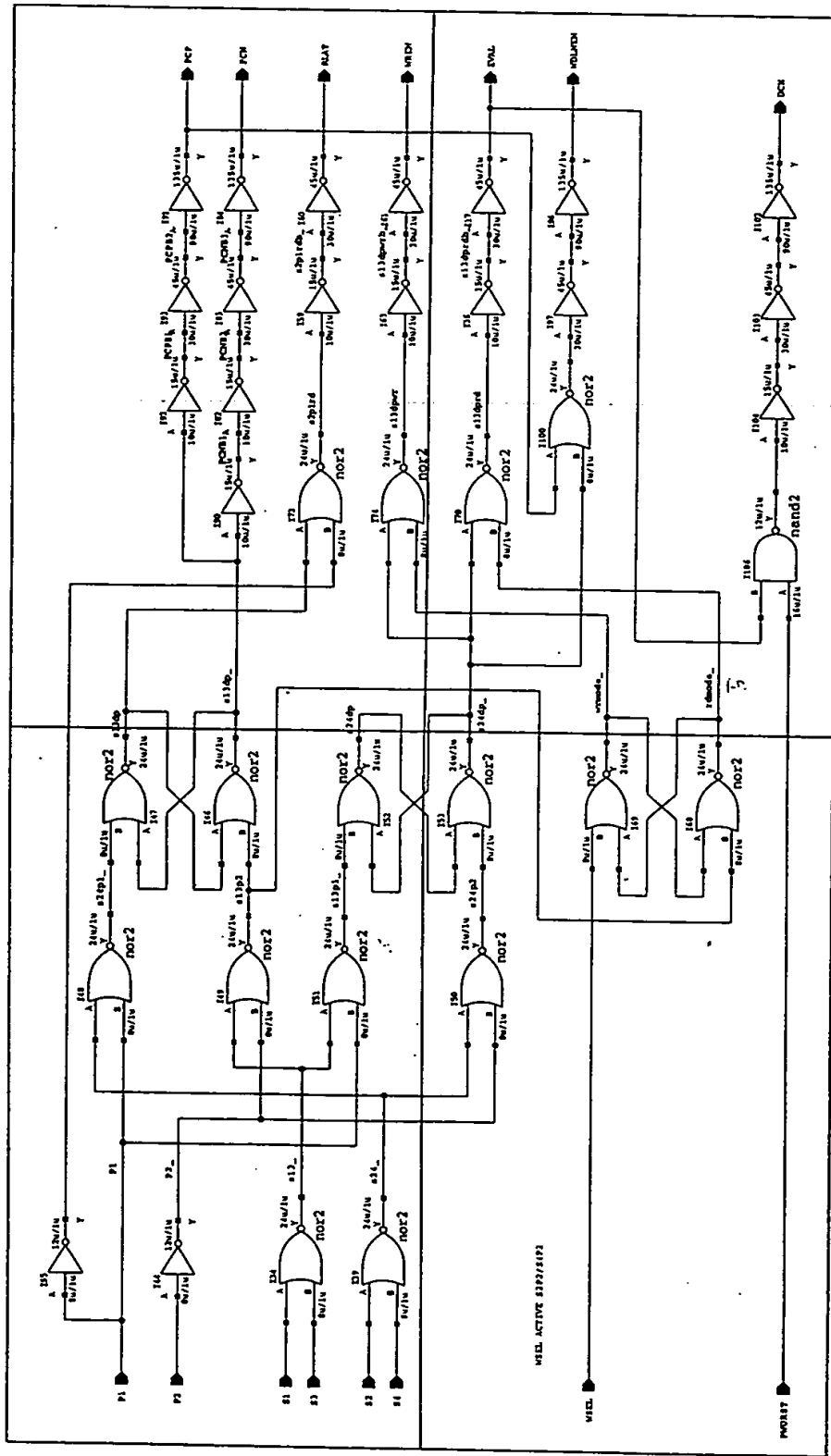


Fig. 7.0301

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
PROJECT: 103 AFJ	REVISION: 103001
TITLE: RAM CONTROL	
DATE: 1031000/10001	BY: 1031000/10001
DATE: Feb 11 16:47:36 1996	

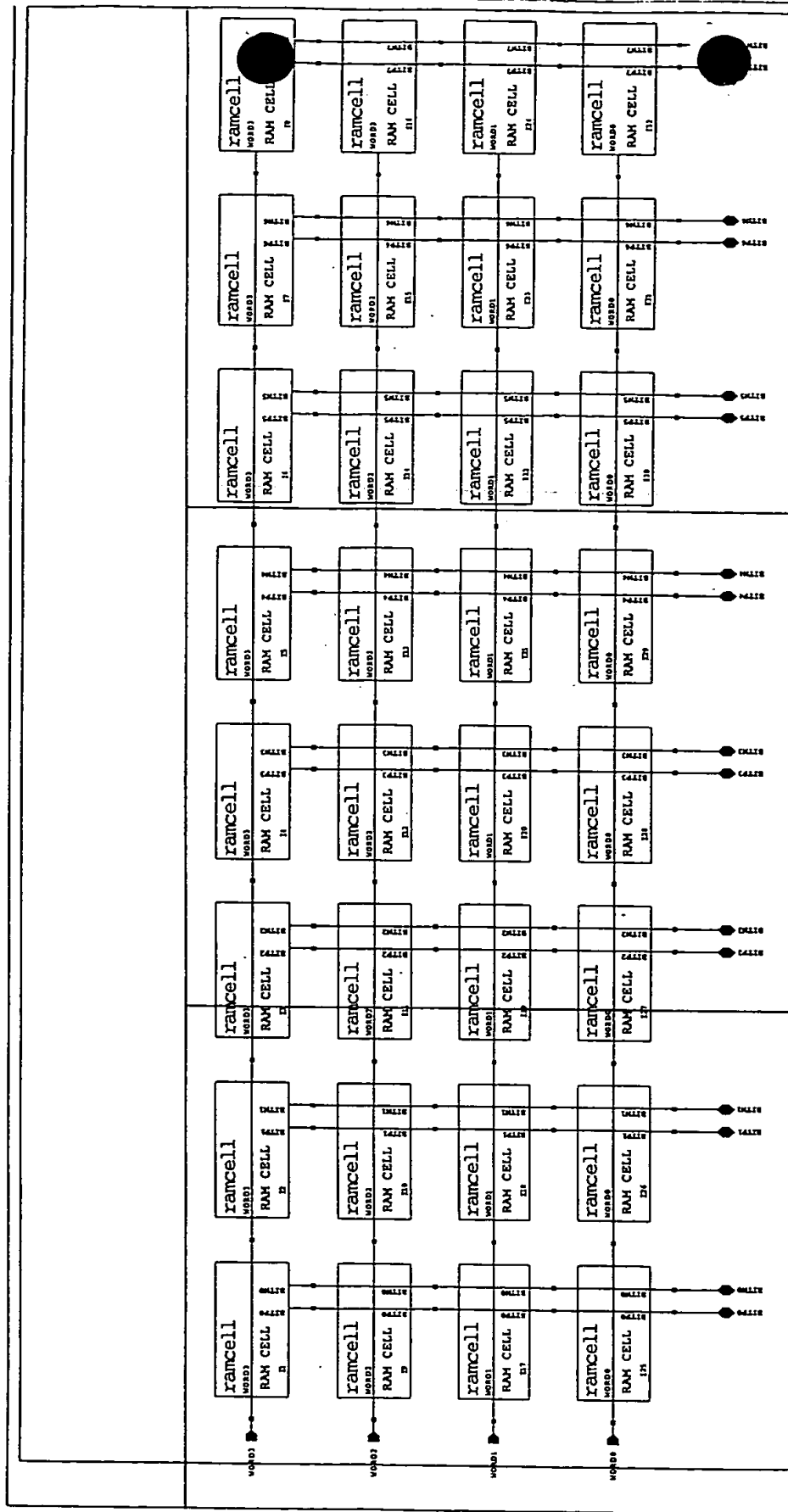


FIG. 7.0302

21CIPROZ
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN
CORPORATIONS, INC.

CONFIDENTIAL INFORMATION

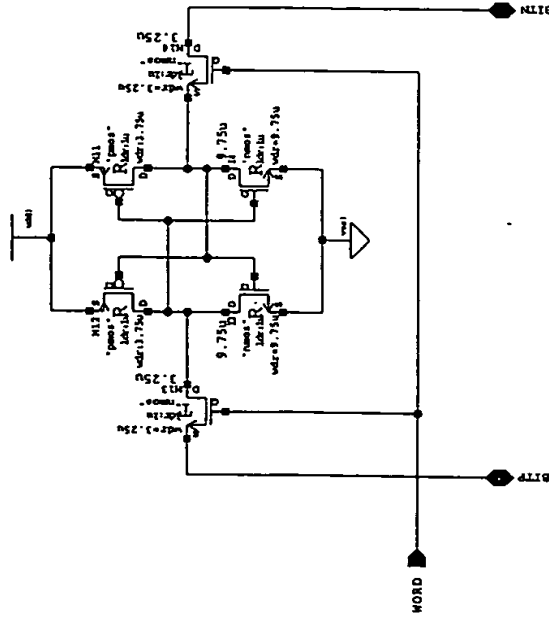
L03	683 HALL, ROZELL
-----	------------------

8x4 RAM Array	
---------------	--

model:	103reva/ram8x4	year	-	file	rd1
--------	----------------	------	---	------	-----

Nov 6 11:34:37 1993

Fig. 7.030201



MICRON		PROJECT: L03	DESTROYED: Rotzoll
COMMUNICATIONS, INC.		TITLE: 6T RAM Cell	
INTEGRATED CIRCUIT DESIGN		MADE: 103eva/ramcell	REV: -
CONFIDENTIAL INFORMATION		DATE: Nov 6 11:34:48 1993	SHEET: A

001120 20500500

MI40-030

7.0303AA	7.0303AB	7.0303AC	7.0303AD
----------	----------	----------	----------

MEMO

001120 20320300

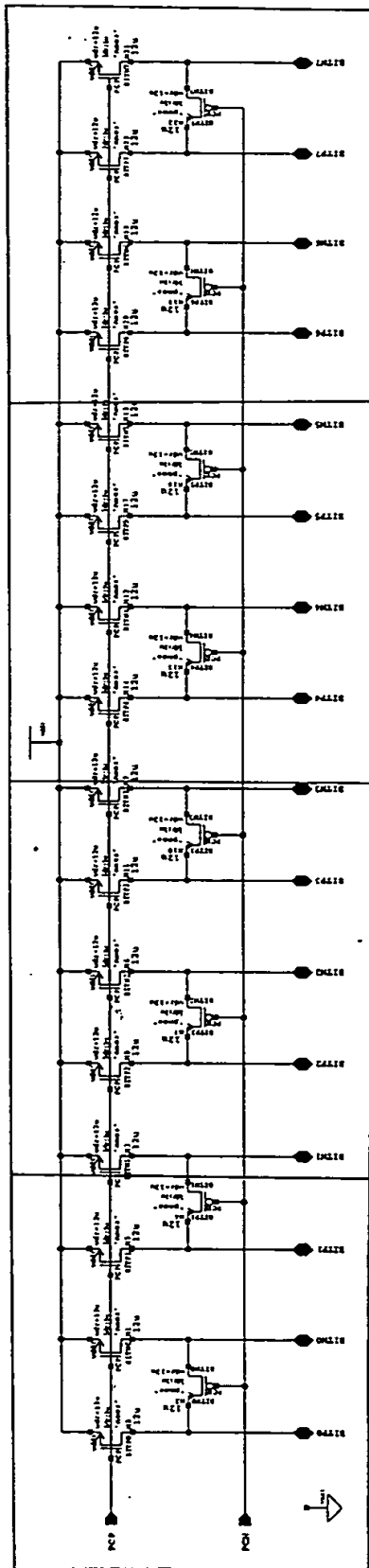


Fig. 7.0303

MICRON		PROPERTY: L03		REVISION: Rotzoll	
		NAME: RAM Precharge			
		DATE: 101revA/rampch		REV: - 100	
		DATE: Nov 12 02:58:36 1993		PAGE: 1	
MICRON		COMMUNICATIONS, INC.			
INTEGRATED CIRCUIT DESIGN					
CONFIDENTIAL INFORMATION					

00000000000000000000

MI40-030

7.0304AA	7.0304AB	7.0304AC	7.0304AD
----------	----------	----------	----------

7.0304

CONFIDENTIAL

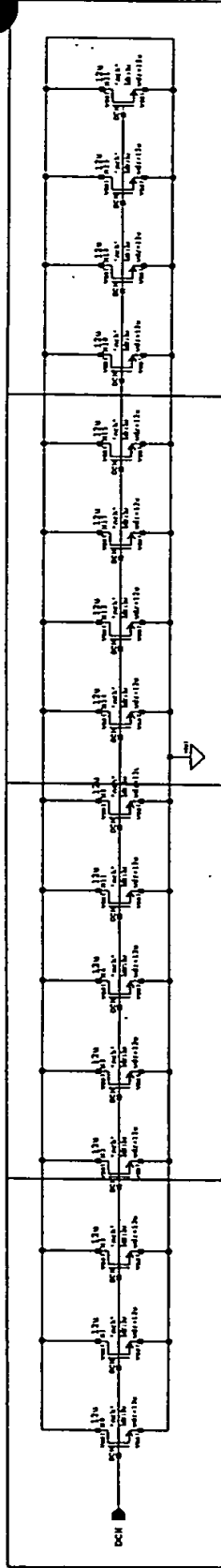
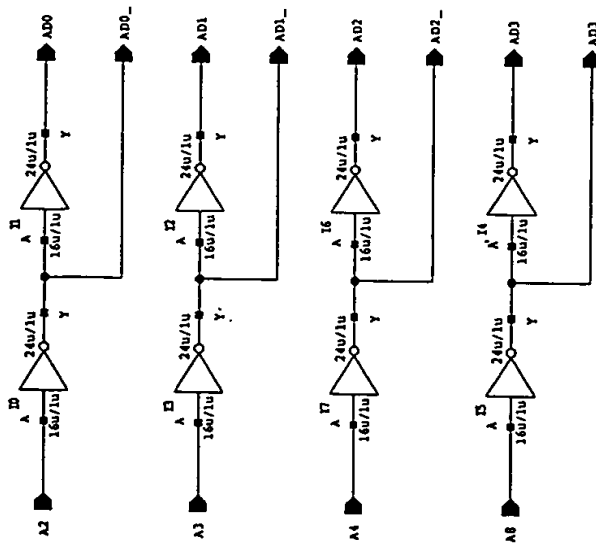


FIG. 7.0304

MICRON		PROPERTY: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		NAME: RAH Precharge	
INTEGRATED CIRCUIT DESIGN		DATE: 10/28/96	BY: B8
CONFIDENTIAL INFORMATION		DATE: Jan 28 09:51:27 1996	BY: rhl

B8: disconnected dch devices from bit lines and tied to vss

CONFIDENTIAL



PROJECT: L03		DESIGNER: Rotzoll	
TITLE: RAM Address Buffer		REV: -	
AUTH: 103reva/ramadb		DATE: Sep 29 16:04:01 1993	
MICRON		COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN		CONFIDENTIAL INFORMATION	

Fig. 7.0305

001120 2050500

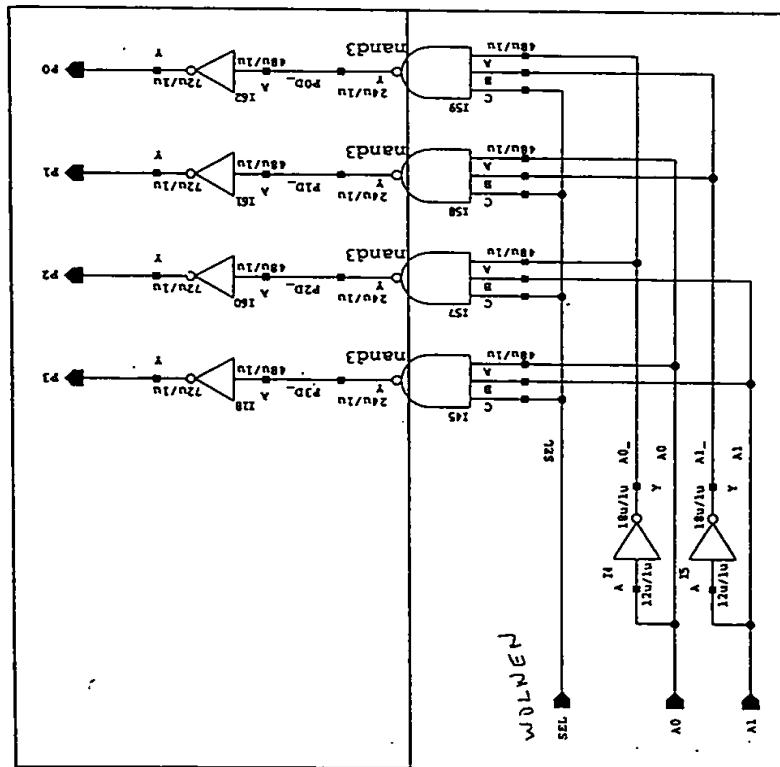
7.0306AA

7.0306BA

7.0306 7.0306

007420 00000000

FIG. 7.0306



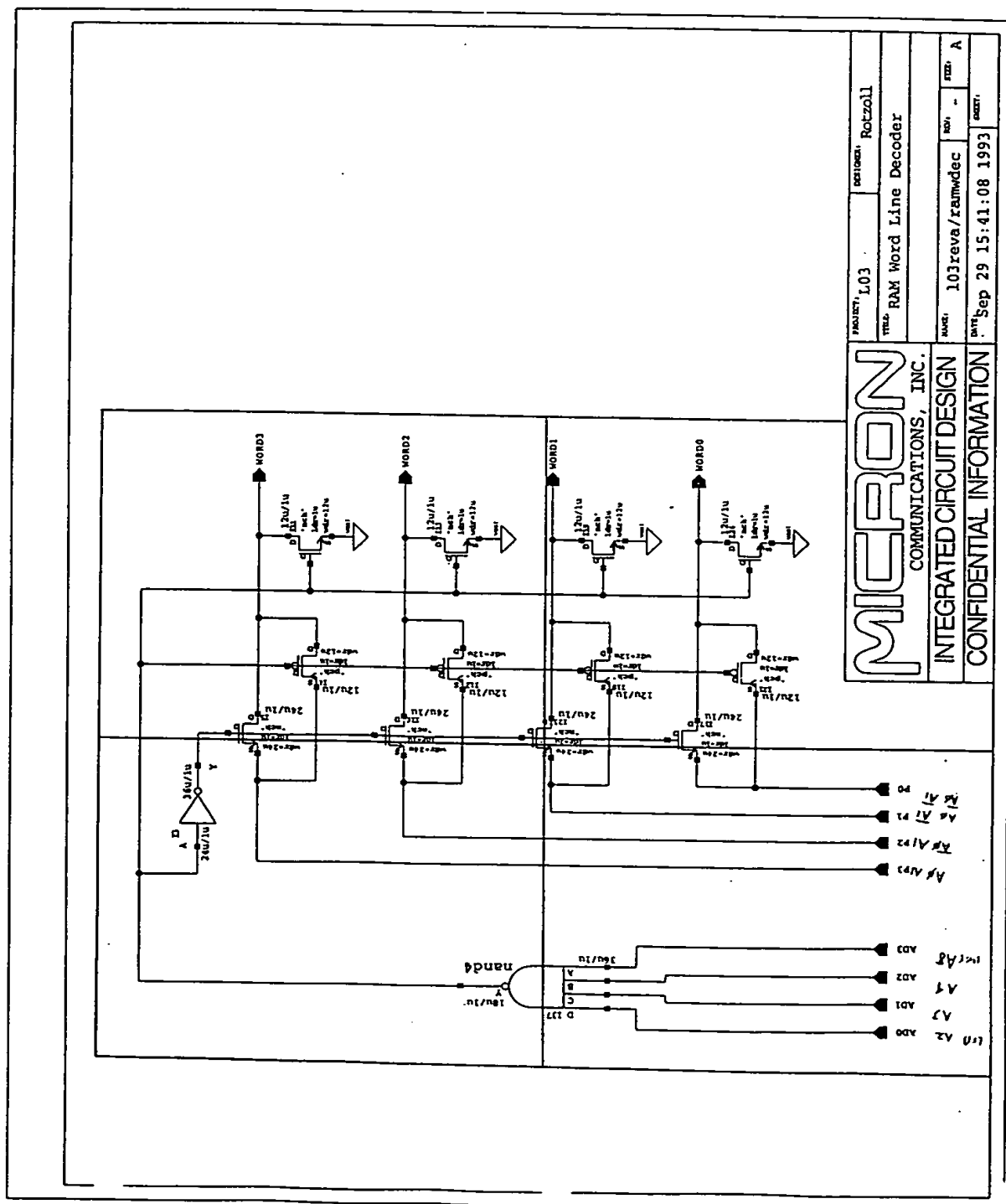
MICRON		PRODUCT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: RAM Word Line Driver	
INTEGRATED CIRCUIT DESIGN		MODEL: 103reva/ramwdr	REV: A
CONFIDENTIAL INFORMATION		DATE: Sep 29 16:04:16 1993	PAGE: 1

7.0307AB

7.0307BB

Итого 7.0307

FIG. 7.0307



7.0308BB

[illegible]

MICRON		COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN		CONFIDENTIAL INFORMATION	
NAME:	101reva/ramdec	REV:	-
DATE:	NOV 5 17:21:07 1993	REV:	11
RAM Column Select Decode		3 to 8	
FORMAT:	L03	REVISION:	ROLL



MICRON
COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION

7.0309AA	7.0309AB	7.0309AC	7.0309AD	7.0309AE	7.0309AF	7.0309AG
7.0309BA	7.0309BB	7.0309BC	7.0309BD	7.0309BE	7.0309BF	7.0309BG

001120 20920500

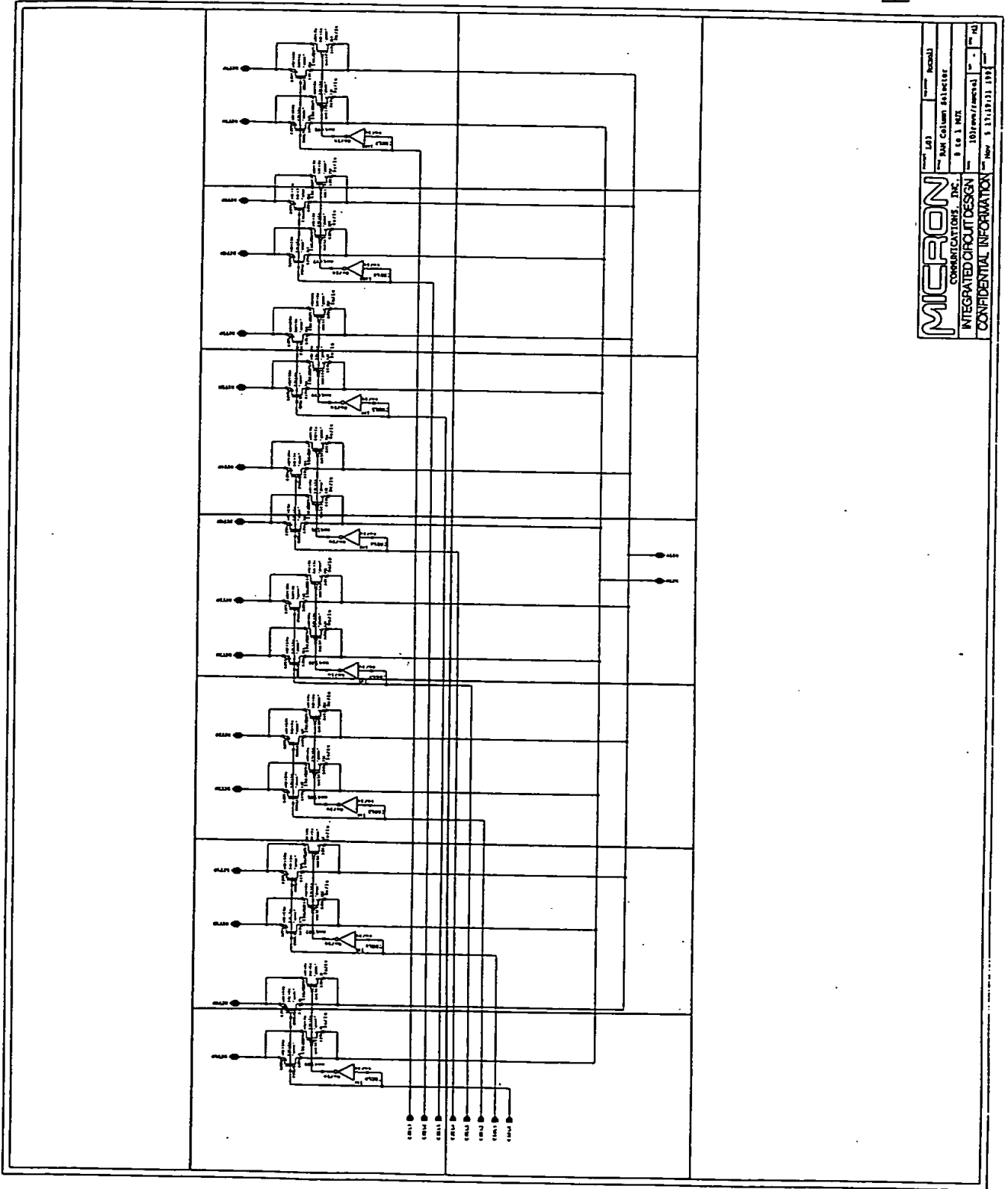


Fig. 7.0309

MICRON		Model	1024
COMMUNICATIONS, INC.		RAM Column Selector	8 to 1 MUX
INTEGRATED CIRCUIT DESIGN		1024 bits/word	10
CONFIDENTIAL INFORMATION		Rev. 3.12.12.11.1991	

007420 20920500

MI40-030

7.0310AA	7.0310AB
7.0310BA	7.0310BB

IL 11 11.11.11

MICRON		PROPERTY: 1.03		REMARKS: R020011	
COMMUNICATIONS, INC.		TITLE:		RAM Databus Interface	
INTEGRATED CIRCUIT DESIGN		DATE:		10/29/93	
CONFIDENTIAL INFORMATION		REV:		103revA/ramdb	
		DATE:		OCT 6 12:08:33 1993	

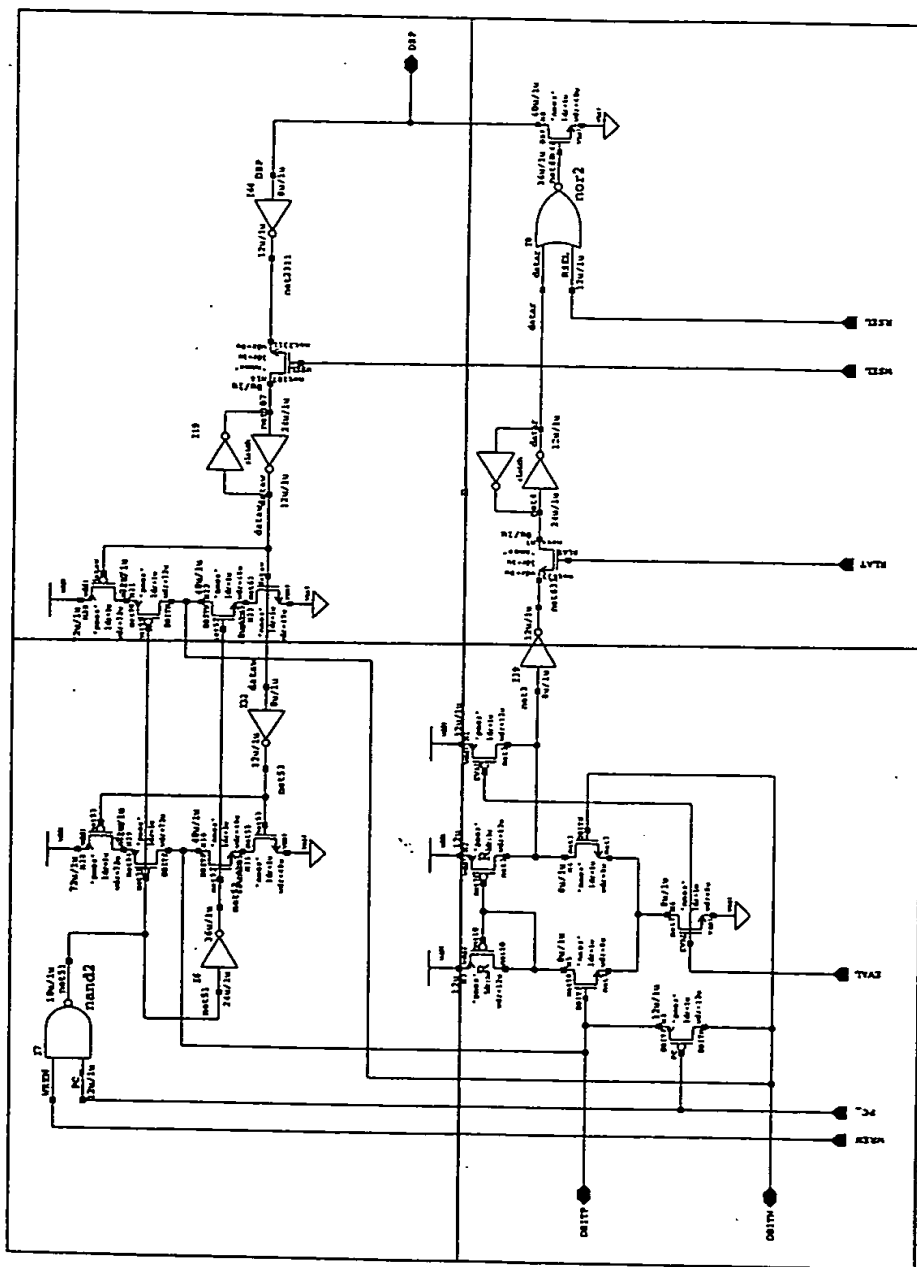


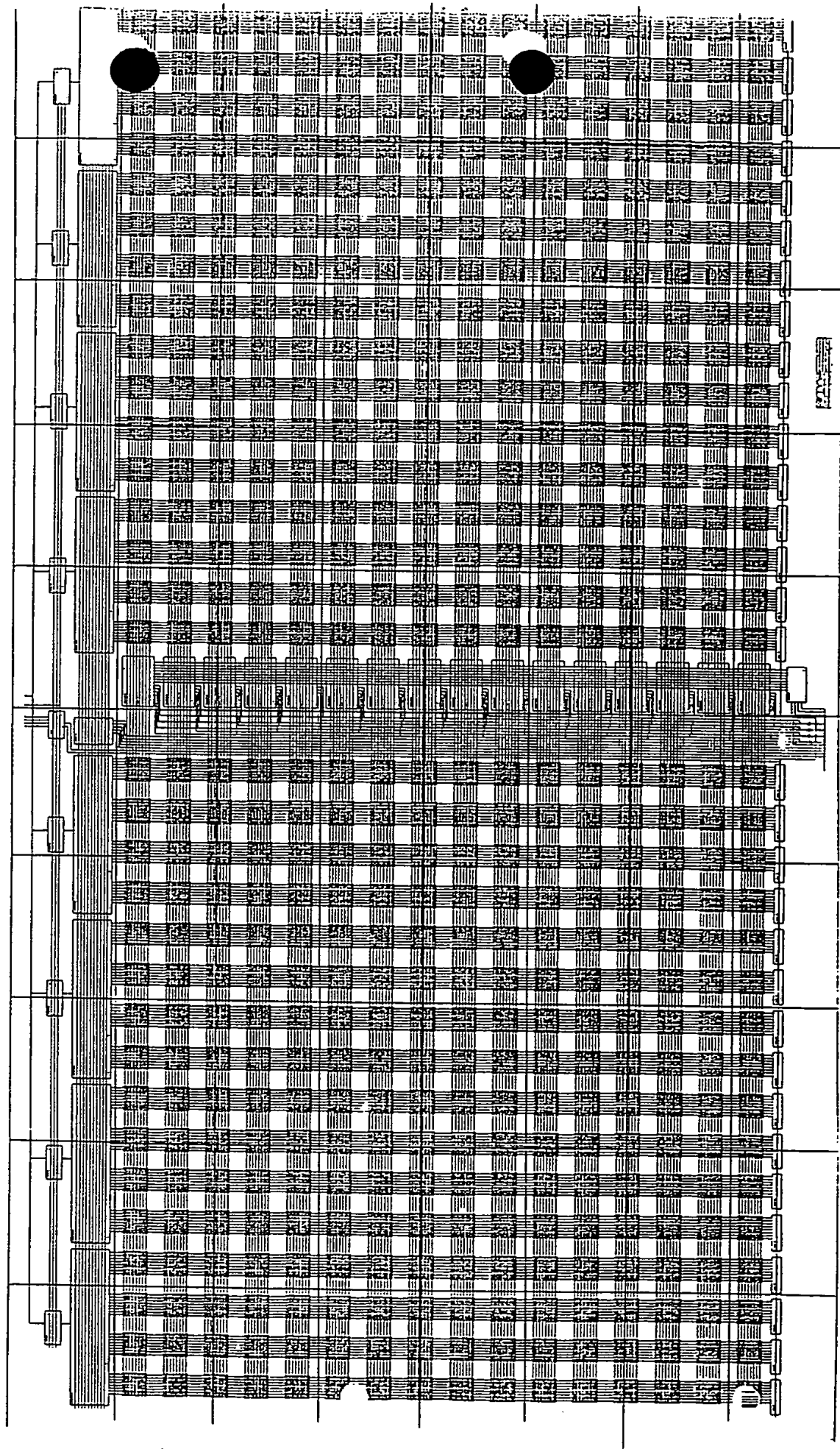
FIG. 7.0310

7.04AA	7.04AB	7.04AC	7.04AD	7.04AE	7.04AF	7.04AG	7.04AH	7.04AI	7.04AJ
7.04BA	7.04BB	7.04BC	7.04BD	7.04BE	7.04BF	7.04BG	7.04BH	7.04BI	7.04BJ
7.04CA	7.04CB	7.04CC	7.04CD	7.04CE	7.04CF	7.04CG	7.04CH	7.04CI	7.04CJ
7.04DA	7.04DB	7.04DC	7.04DD	7.04DE	7.04DF	7.04DG	7.04DH	7.04DI	7.04DJ
7.04EA	7.04EB	7.04EC	7.04ED	7.04EE	7.04EF	7.04EG	7.04EH	7.04EI	7.04EJ
7.04FA	7.04FB	7.04FC	7.04FD	7.04FE	7.04FF	7.04FG	7.04FH	7.04FI	7.04FJ
7.04GA	7.04GB	7.04GC	7.04GD	7.04GE	7.04GF	7.04GG	7.04GH	7.04GI	7.04GJ
7.04HA	7.04HB	7.04HC	7.04HD	7.04HE	7.04HF	7.04HG	7.04HH	7.04HI	7.04HJ

IL IL IL 7.040000

001420-20920500

FIG. 7.04



007120 20920300

MI40-030

7.0401AA	7.0401AB
----------	----------

IL 00 11.04.00 11

SECRET

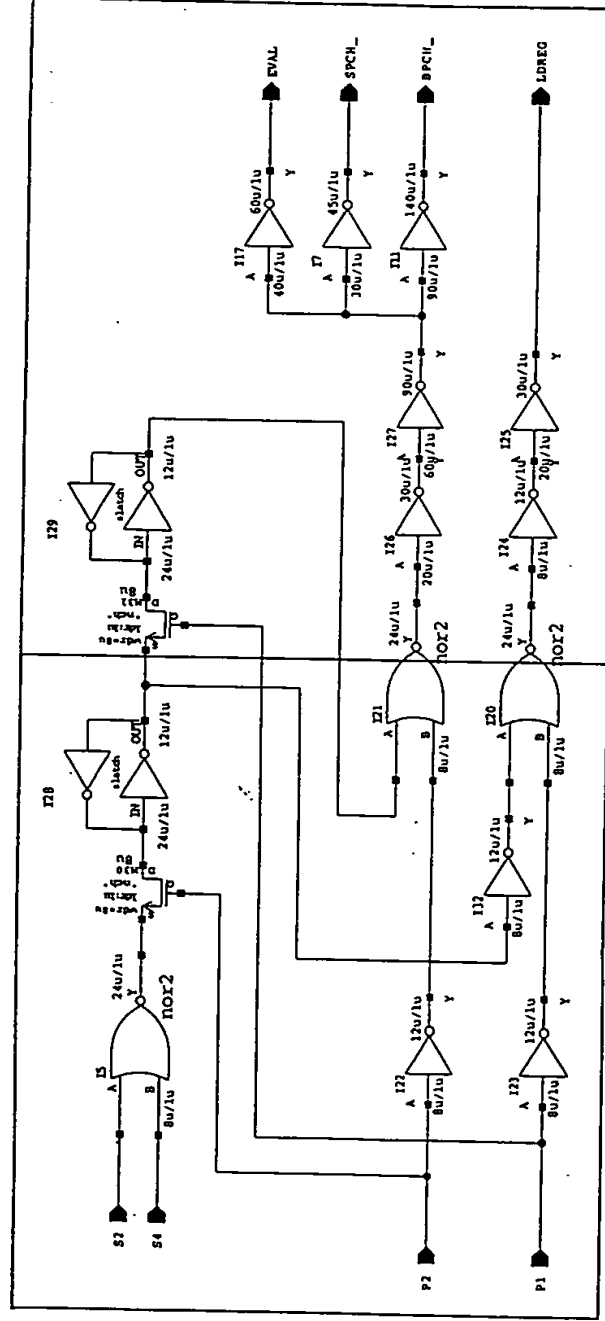


Fig. 7.0401

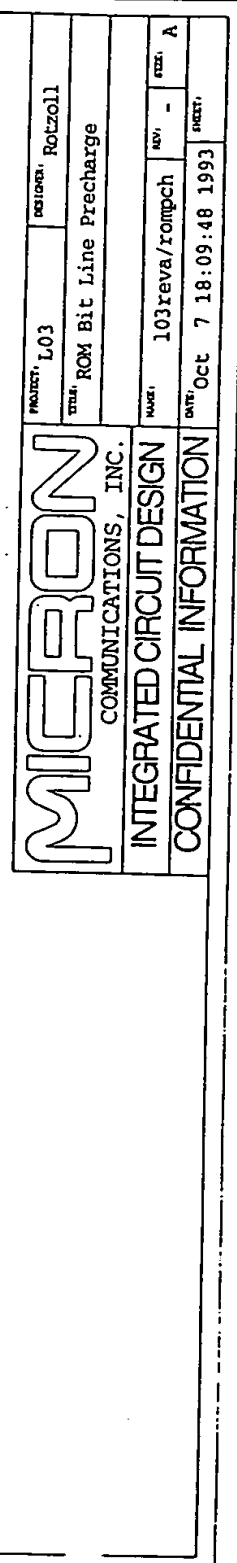
<div>MICRON</div> <div>COMMUNICATIONS, INC.</div> <div>INTEGRATED CIRCUIT DESIGN</div> <div>CONFIDENTIAL INFORMATION</div>				PROJECT: L03		DESIGNER: Rotzoll			
				TITLE: ROM Control Logic					
				NAME: 103revA/romctl		REV: -		PAGE: A	
				DATE: Oct 3 13:16:28 1993		PAGE: 1			

2025 RELEASE UNDER E.O. 14176

7.0402AA	7.0402AB
----------	----------

EX-110

FIG. 7.0402



NICRON
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

PROJECT, L03	DESIGNER, Rotzoll
--------------	-------------------

TIME	ROM Bit Line Precharge
0	0
1	0
2	0
3	0
4	0
5	0
6	0
7	0
8	0
9	0
10	0
11	0
12	0
13	0
14	0
15	0
16	0
17	0
18	0
19	0
20	0
21	0
22	0
23	0
24	0
25	0
26	0
27	0
28	0
29	0
30	0
31	0
32	0
33	0
34	0
35	0
36	0
37	0
38	0
39	0
40	0
41	0
42	0
43	0
44	0
45	0
46	0
47	0
48	0
49	0
50	0
51	0
52	0
53	0
54	0
55	0
56	0
57	0
58	0
59	0
60	0
61	0
62	0
63	0
64	0
65	0
66	0
67	0
68	0
69	0
70	0
71	0
72	0
73	0
74	0
75	0
76	0
77	0
78	0
79	0
80	0
81	0
82	0
83	0
84	0
85	0
86	0
87	0
88	0
89	0
90	0
91	0
92	0
93	0
94	0
95	0
96	0
97	0
98	0
99	0
100	0
101	0
102	0
103	0
104	0
105	0
106	0
107	0
108	0
109	0
110	0
111	0
112	0
113	0
114	0
115	0
116	0
117	0
118	0
119	0
120	0
121	0
122	0
123	0
124	0
125	0
126	0
127	0
128	0
129	0
130	0
131	0
132	0
133	0
134	0
135	0
136	0
137	0
138	0
139	0
140	0
141	0
142	0
143	0
144	0
145	0
146	0
147	0
148	0
149	0
150	0
151	0
152	0
153	0
154	0
155	0
156	0
157	0
158	0
159	0
160	0
161	0
162	0
163	0
164	0
165	0
166	0
167	0
168	0
169	0
170	0
171	0
172	0
173	0
174	0
175	0
176	0
177	0
178	0
179	0
180	0

NAME:	107 hours / month	ADV:	
-------	-------------------	------	--

DATE:	Oct 7 18:09:48 1993	SHEET:	-
-------	---------------------	--------	---

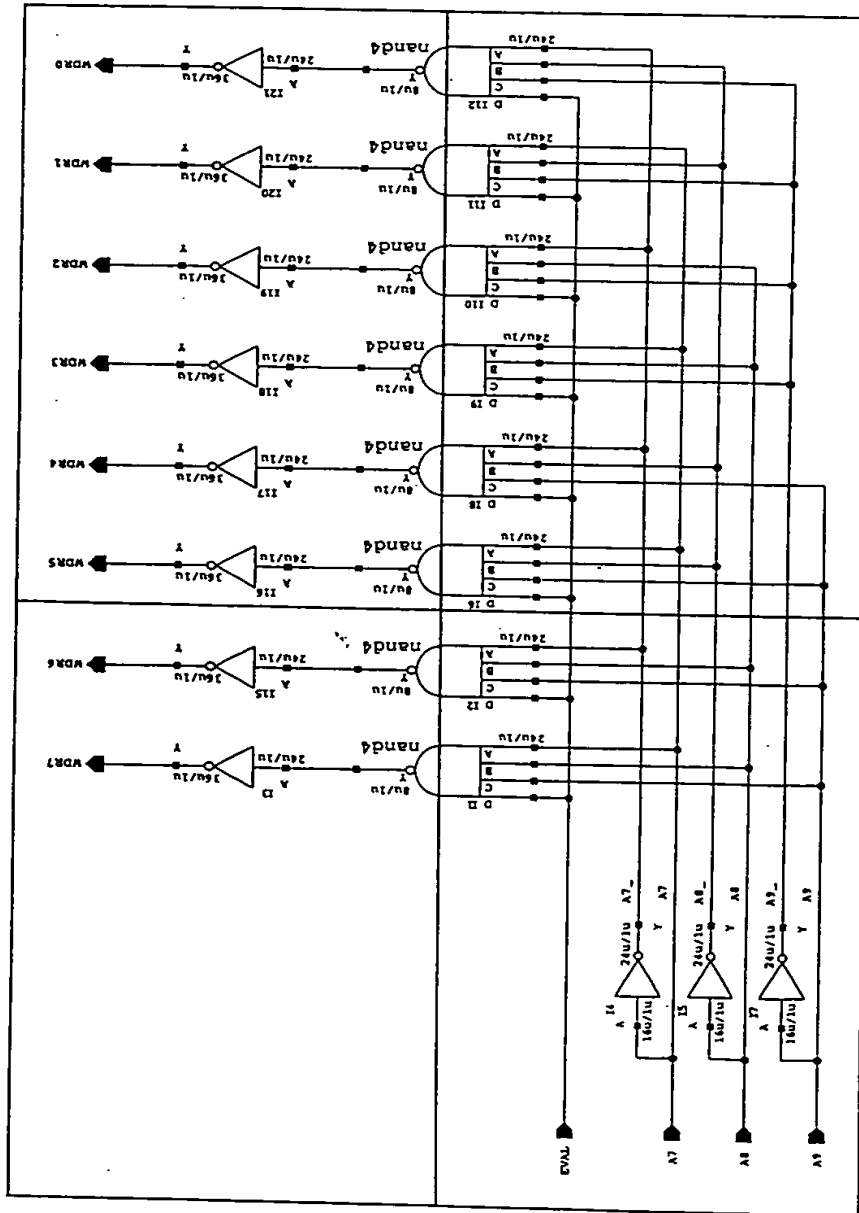
007420" 00920900

MI40-030

7.0403AA	7.0403AB
7.0403BA	7.0403BB

II II II 7.0403BB

FIG. 7.0903

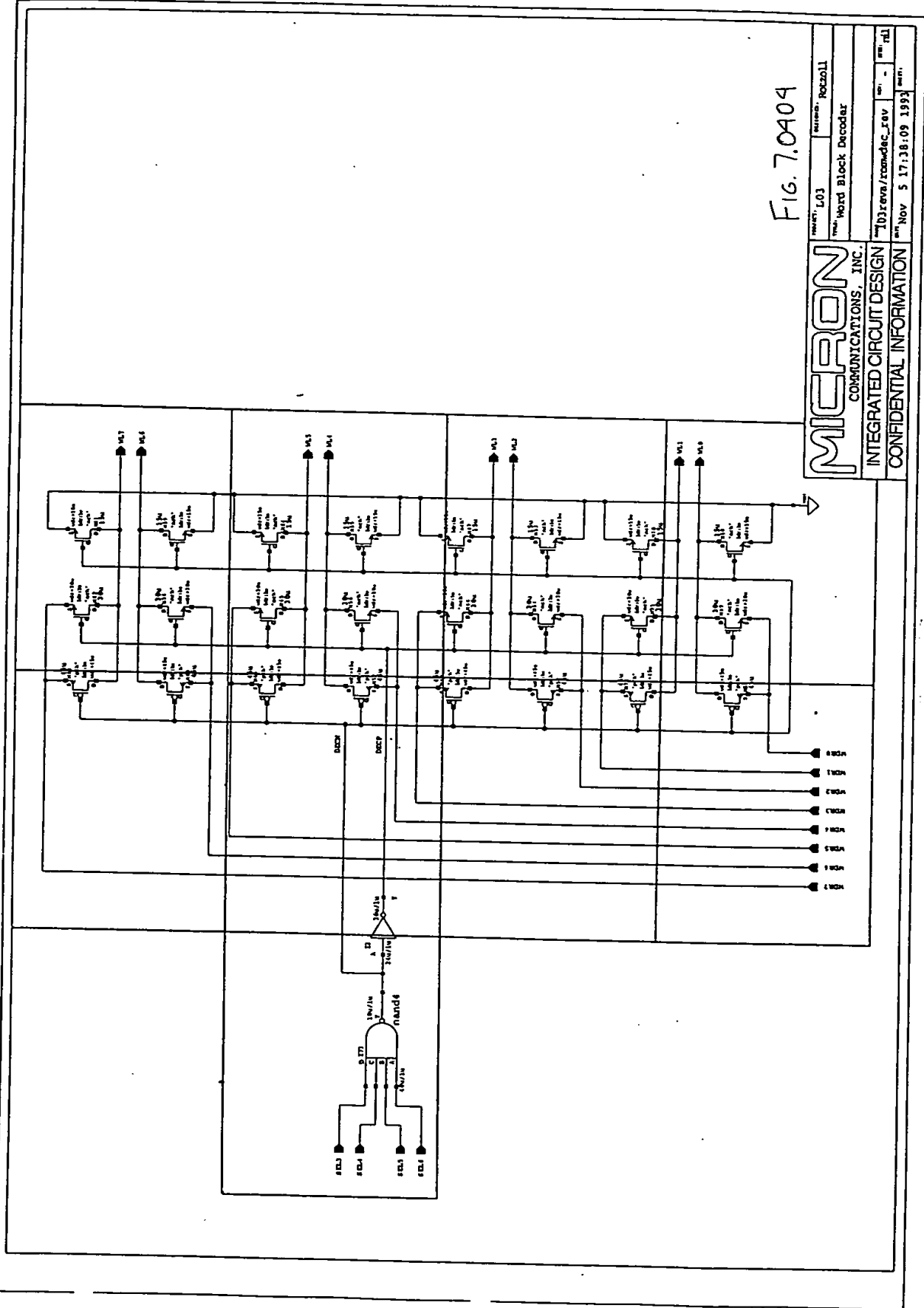


MICRON		PROJECT: L03		DESIGNED BY: Rotzoll	
		TITLE:		ROM Word Line Driver	
		NAME:		103reva/rommodr	
		REV:		-	
		DATE:		Oct 7 18:11:34 1993	
		SHEET:		1	

	7.0404AB	7.0404AC
7.0404BA	7.0404BB	7.0404BC
	7.0404CB	7.0404CC
	7.0404DB	7.0404DC

Итого 7.0404

CONFIDENTIAL



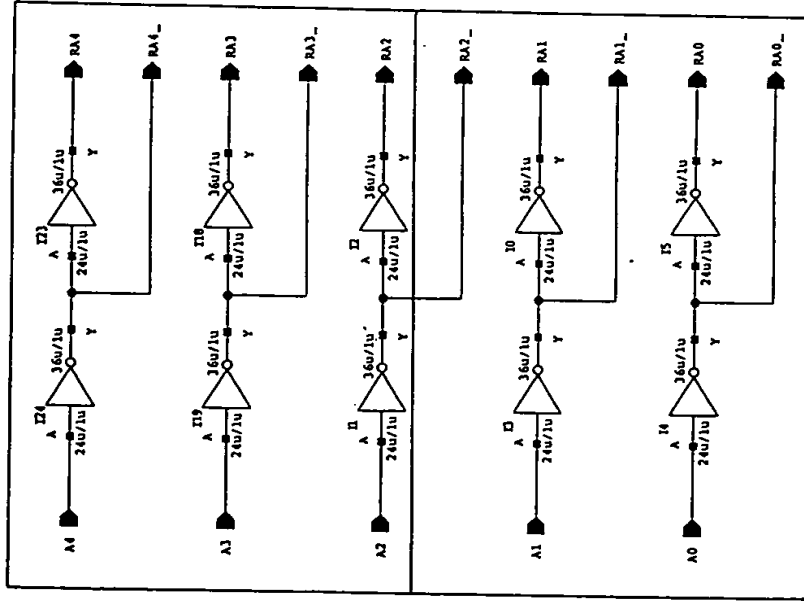
7.0405AA

7.0405BA

Fig 7.0405

0950303 00400
007120 20920500

FIG. 7.0905



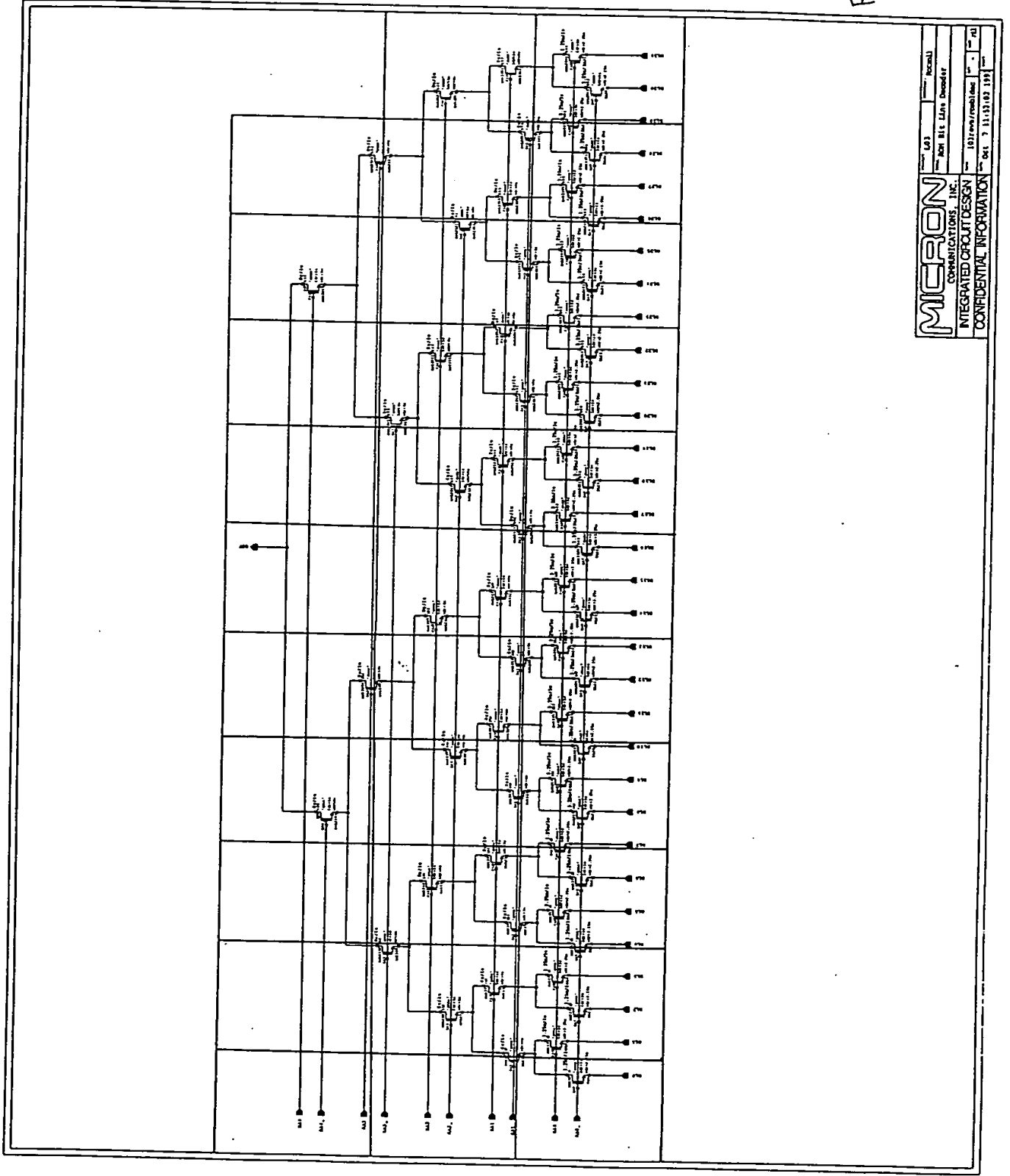
MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: ROM Bit Line Address Driver	
INTEGRATED CIRCUIT DESIGN		NUMBER: 103reva/romblldr	REV: A
CONFIDENTIAL INFORMATION		DATE: Oct 7 12:08:42 1993	

7.0406AA	7.0406AB	7.0406AC	7.0406AD	7.0406AE	7.0406AF	7.0406AG	7.0406AH	7.0406AI	7.0406AJ	
7.0406BA	7.0406BB	7.0406BC	7.0406BD	7.0406BE	7.0406BF	7.0406BG	7.0406BH	7.0406BI	7.0406BJ	7.0406BK
7.0406CA	7.0406CB	7.0406CC	7.0406CD	7.0406CE	7.0406CF	7.0406CG	7.0406CH	7.0406CI	7.0406CJ	7.0406CK

II MI40-030

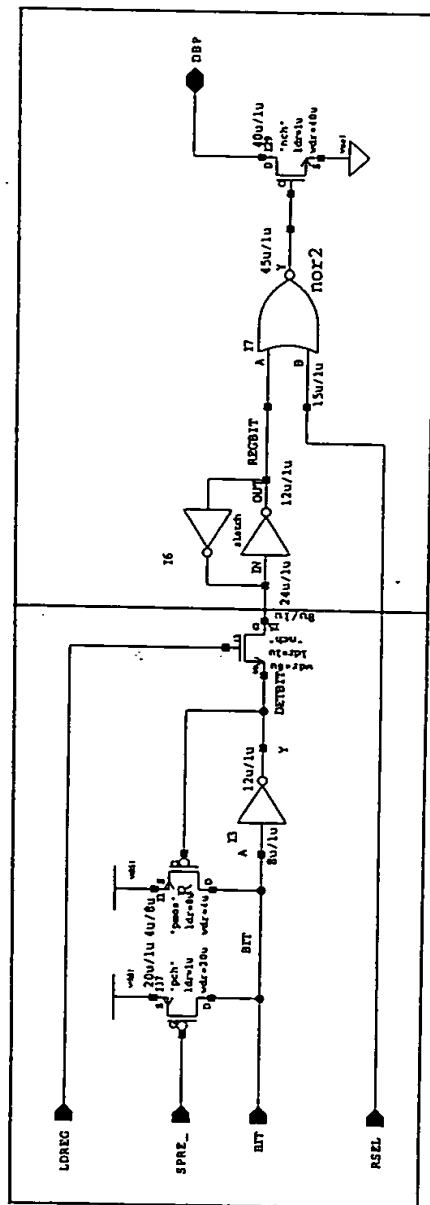
001420 20900000

Fig. 7.0406



MICRON		L03		Recall	
COMMUNICATIONS, INC.		Non Bit Latch Decoder			
INTEGRATED CIRCUIT DESIGN		101 www.micron.com		101	
CONFIDENTIAL INFORMATION		Oct 7 11:12:02 1997			

FIG. 7.0407



MICRON		PROJECT: L03		DESIGN: Rotzoll	
COMMUNICATIONS, INC.		TITLE ROM Sense Amplifier			
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/romsns		REV: -	
CONFIDENTIAL INFORMATION				SIZE: A	
		DATE: Oct 7 18:12:58 1993		SHEET:	

7.05AA	7.05AB
7.05BA	7.05BB
7.05CA	7.05CB

7.05

00110 00000000

SECRETED 00900300

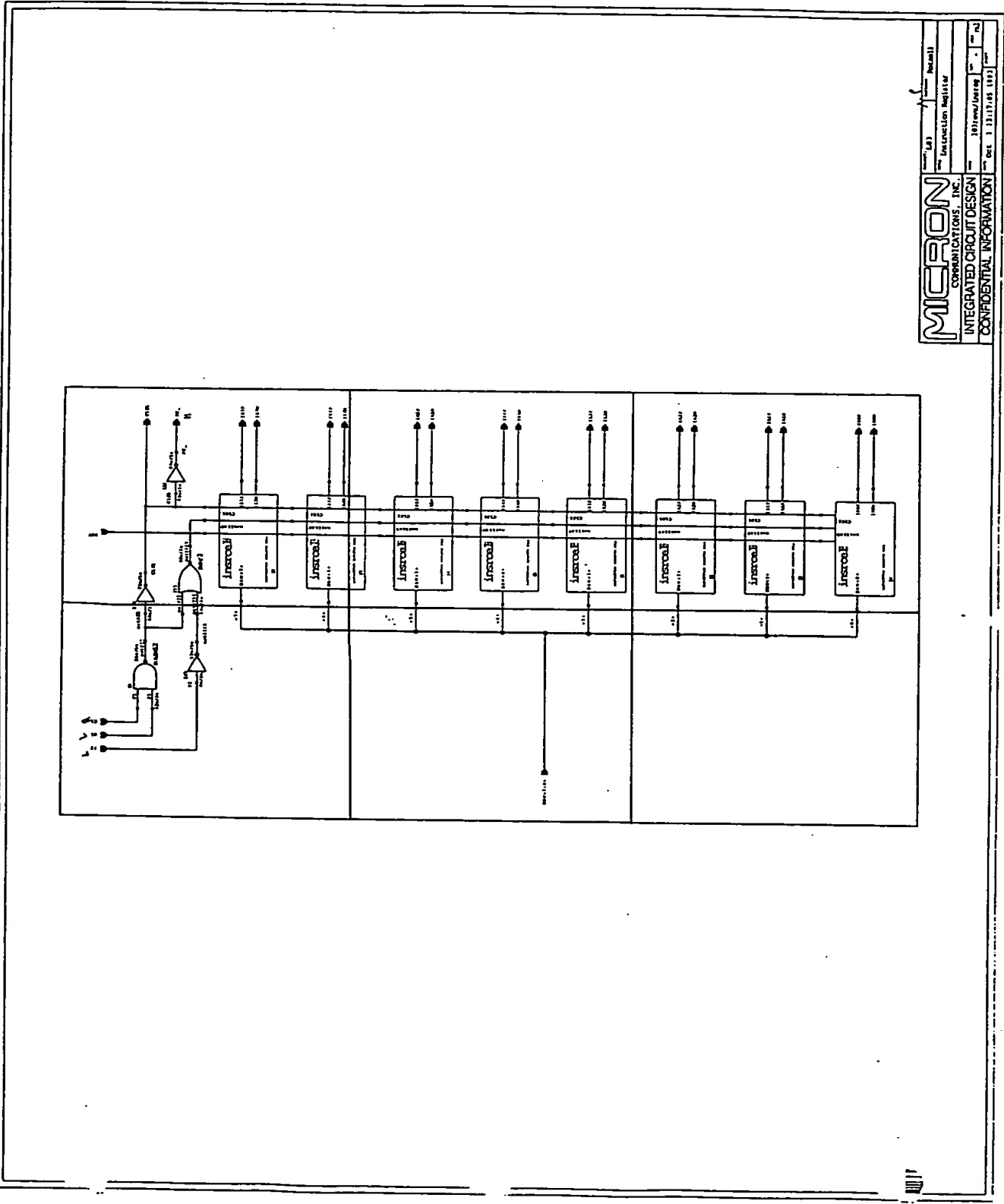


Fig. 7.05

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
Doc. 11117108 (10)	Rev. 11117108 (10)
Instruction Register	Doc. 11117108 (10)
Doc. 11117108 (10)	Rev. 11117108 (10)

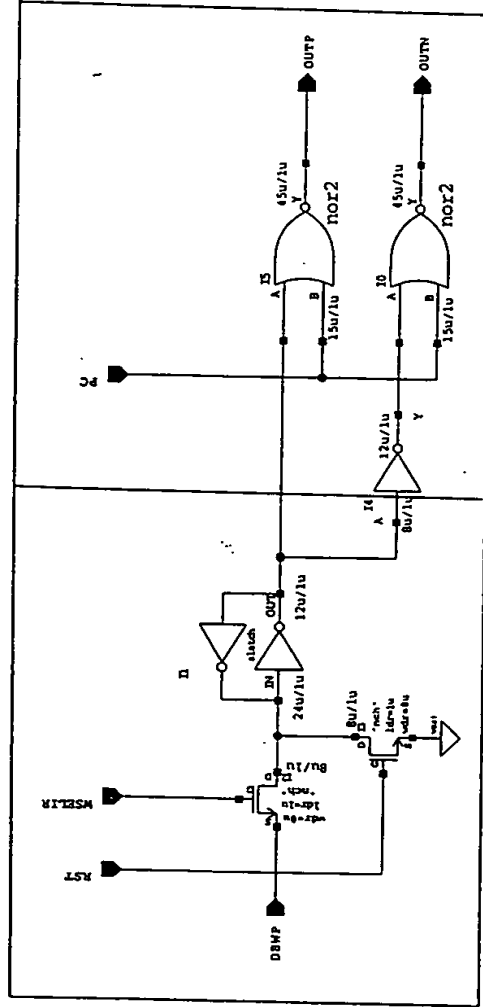
004400 00000000

MI40-030

7.0501AA	7.0501AB
----------	----------

Ex 7.0501

Fig. 7.0501



PROJECT: L03		DESIGNED: Rotzoll	
TITLE: Instruction Register Cell			
NAME: 103reva/insrcel		REV: -	ITEM: A
DATE: Oct 5 20:12:49 1993		SHEET: 1	

MICRON
 COMMUNICATIONS, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

7.06AA	7.06AB	7.06AC	7.06AD	7.06AE	7.06AF	7.06AG	7.06AH	7.06AI	7.06AJ	7.06AK	7.06AL	7.06AM	7.06AN
	7.06BB	7.06BC	7.06BD	7.06BE	7.06BF	7.06BG	7.06BH	7.06BI	7.06BJ	7.06BK	7.06BL	7.06BM	7.06BN
7.06CA	7.06CB	7.06CC	7.06CD	7.06CE	7.06CF	7.06CG	7.06CH	7.06CI	7.06CJ	7.06CK	7.06CL	7.06CM	7.06CN

II II II 7.06

三

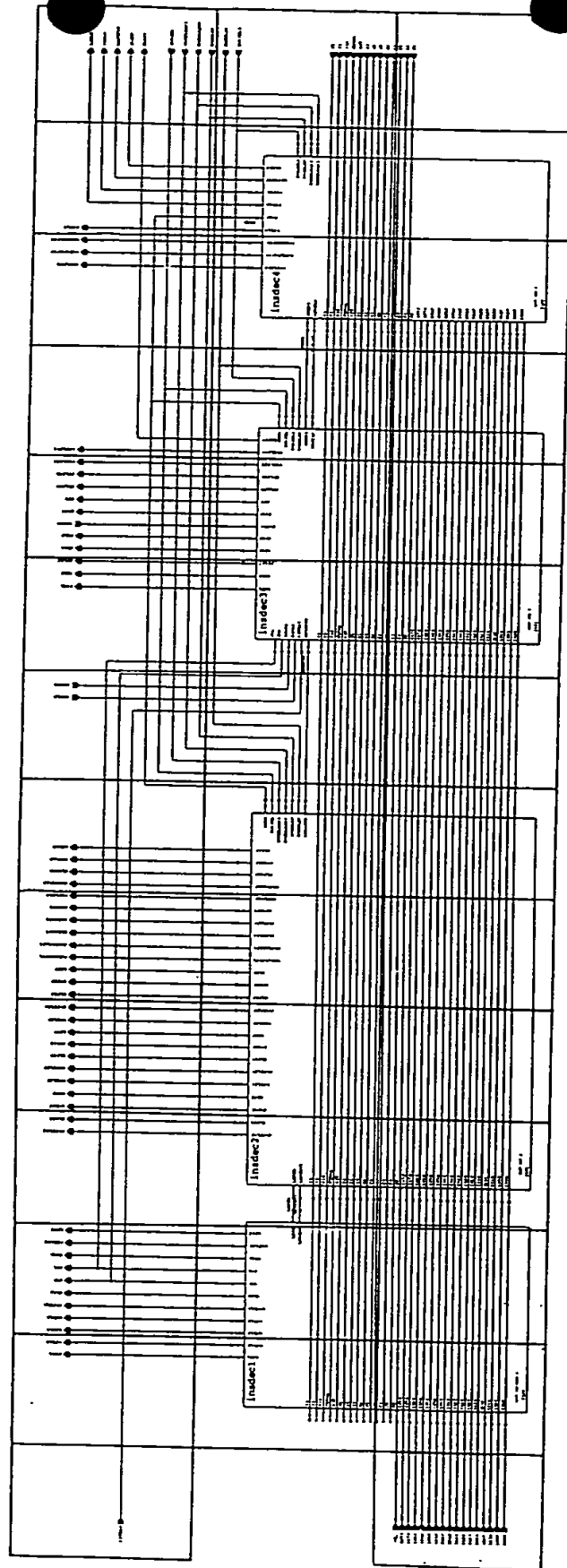


Fig. 7.06

MICRON
CONSULTANTS, THE
INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION

Итого 7.0600

00502602 024400

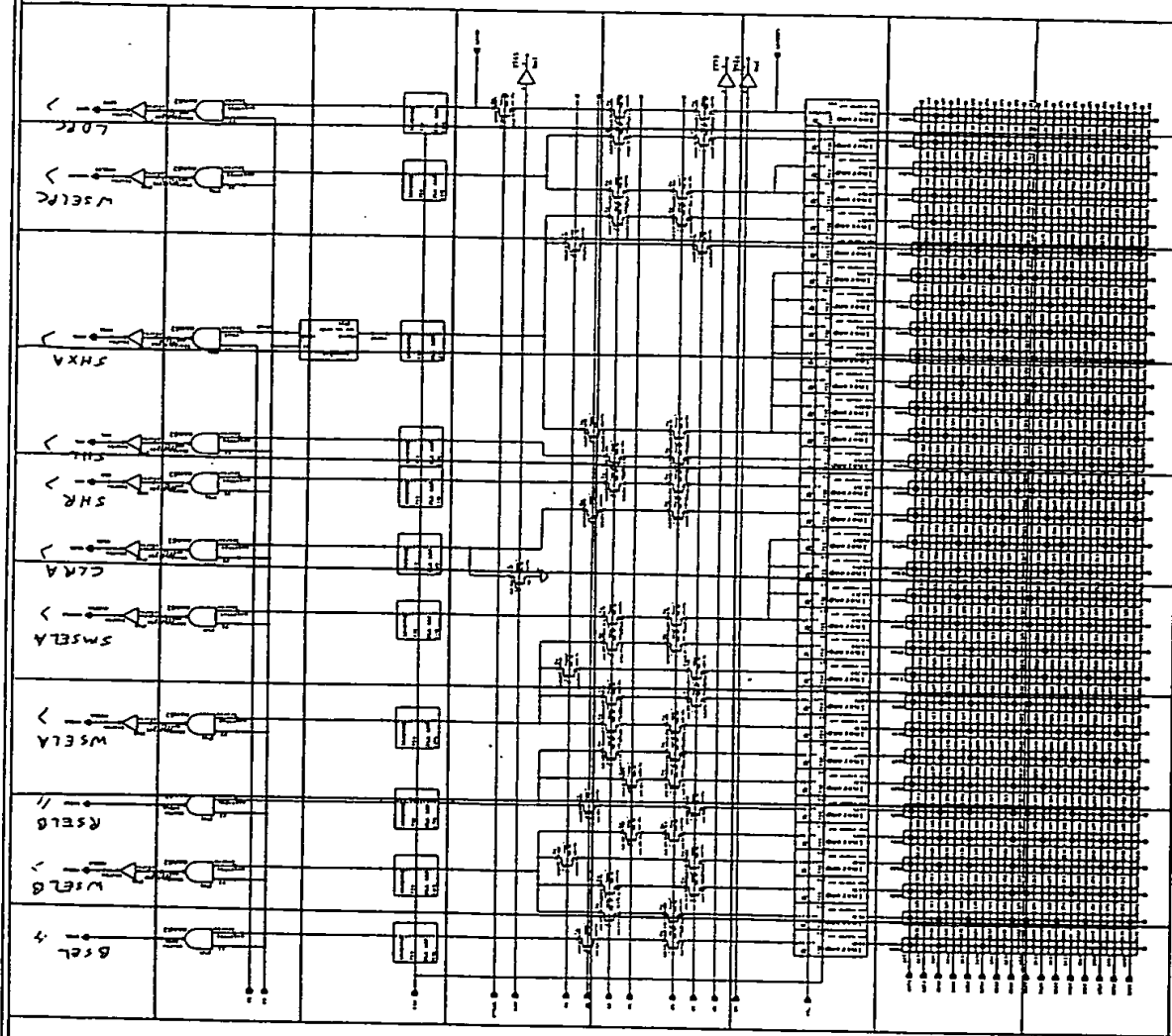
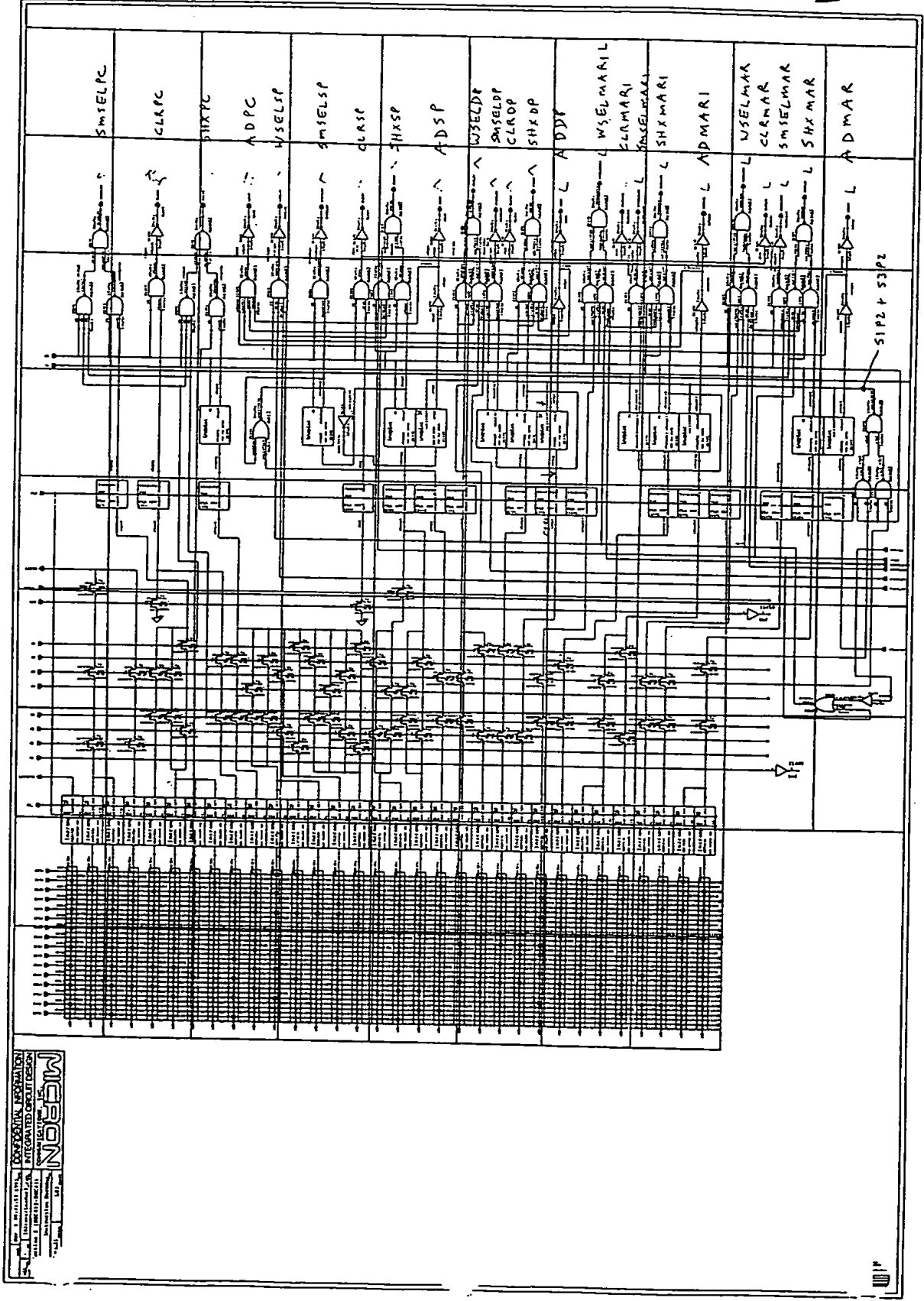


Fig. 7.0601

Итого 1.0602

004400 20900500



- ADPC is after
the 1st ADPC
ADPC, ADPC, ADPC,
ADPC

FIG. 7.0602

7.0603AA	7.0603AB	7.0603AC	7.0603AD	7.0603AE	7.0603AF		7.0603AH	7.0603AI	7.0603AJ		7.0603BK
7.0603BA	7.0603BB	7.0603BC	7.0603BD	7.0603BE	7.0603BF	7.0603BG	7.0603BH	7.0603BI	7.0603BJ		
7.0603CA	7.0603CB	7.0603CC	7.0603CD	7.0603CE	7.0603CF	7.0603CG	7.0603CH	7.0603CI	7.0603CJ		
7.0603DA	7.0603DB	7.0603DC	7.0603DD	7.0603DE	7.0603DF	7.0603DG	7.0603DH	7.0603DI	7.0603DJ		
7.0603EA	7.0603EB	7.0603EC	7.0603ED	7.0603EE	7.0603EF	7.0603EG	7.0603EH	7.0603EI	7.0603EJ		
7.0603FA	7.0603FB	7.0603FC	7.0603FD	7.0603FE	7.0603FF	7.0603FG	7.0603FH	7.0603FI	7.0603FJ		
7.0603GA	7.0603GB	7.0603GC	7.0603GD	7.0603GE	7.0603GF	7.0603GG	7.0603GH	7.0603GI	7.0603GJ		
7.0603HA	7.0603HB	7.0603HC	7.0603HD	7.0603HE	7.0603HF	7.0603HG	7.0603HH	7.0603HI	7.0603HJ		
7.0603IA	7.0603IB	7.0603IC	7.0603ID	7.0603IE	7.0603IF	7.0603IG	7.0603IH	7.0603II	7.0603IJ		
		7.0603JC	7.0603JD	7.0603JE	7.0603JF	7.0603JG		7.0603JI			

ЕОСН. II

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
---	---	---	---	---	---	---	---	---	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	-----

CC, carry



FIG. 7.0603

7.0604AA	7.0604AB	7.0604AC	7.0604AD	7.0604AE	7.0604AF	7.0604AG	7.0604AH	7.0604AI
7.0604BA	7.0604BB	7.0604BC	7.0604BD	7.0604BE	7.0604BF	7.0604BG	7.0604BH	7.0604BI
7.0604CA	7.0604CB	7.0604CC	7.0604CD	7.0604CE	7.0604CF	7.0604CG	7.0604CH	7.0604CI
7.0604DA	7.0604DB	7.0604DC	7.0604DD	7.0604DE	7.0604DF	7.0604DG	7.0604DH	7.0604DI
7.0604EA	7.0604EB	7.0604EC	7.0604ED	7.0604EE	7.0604EF	7.0604EG	7.0604EH	7.0604EI
7.0604FA	7.0604FB	7.0604FC	7.0604FD	7.0604FE	7.0604FF	7.0604FG	7.0604FH	7.0604FI
7.0604GA	7.0604GB	7.0604GC	7.0604GD	7.0604GE	7.0604GF	7.0604GG	7.0604GH	7.0604GI
7.0604HA	7.0604HB	7.0604HC	7.0604HD	7.0604HE	7.0604HF	7.0604HG		
7.0604IA	7.0604IB	7.0604IC	7.0604ID	7.0604IE	7.0604IF	7.0604IG		
7.0604JA	7.0604JB	7.0604JC	7.0604JD	7.0604JE	7.0604JF	7.0604JG	7.0604JH	7.0604JI

004400-00000000

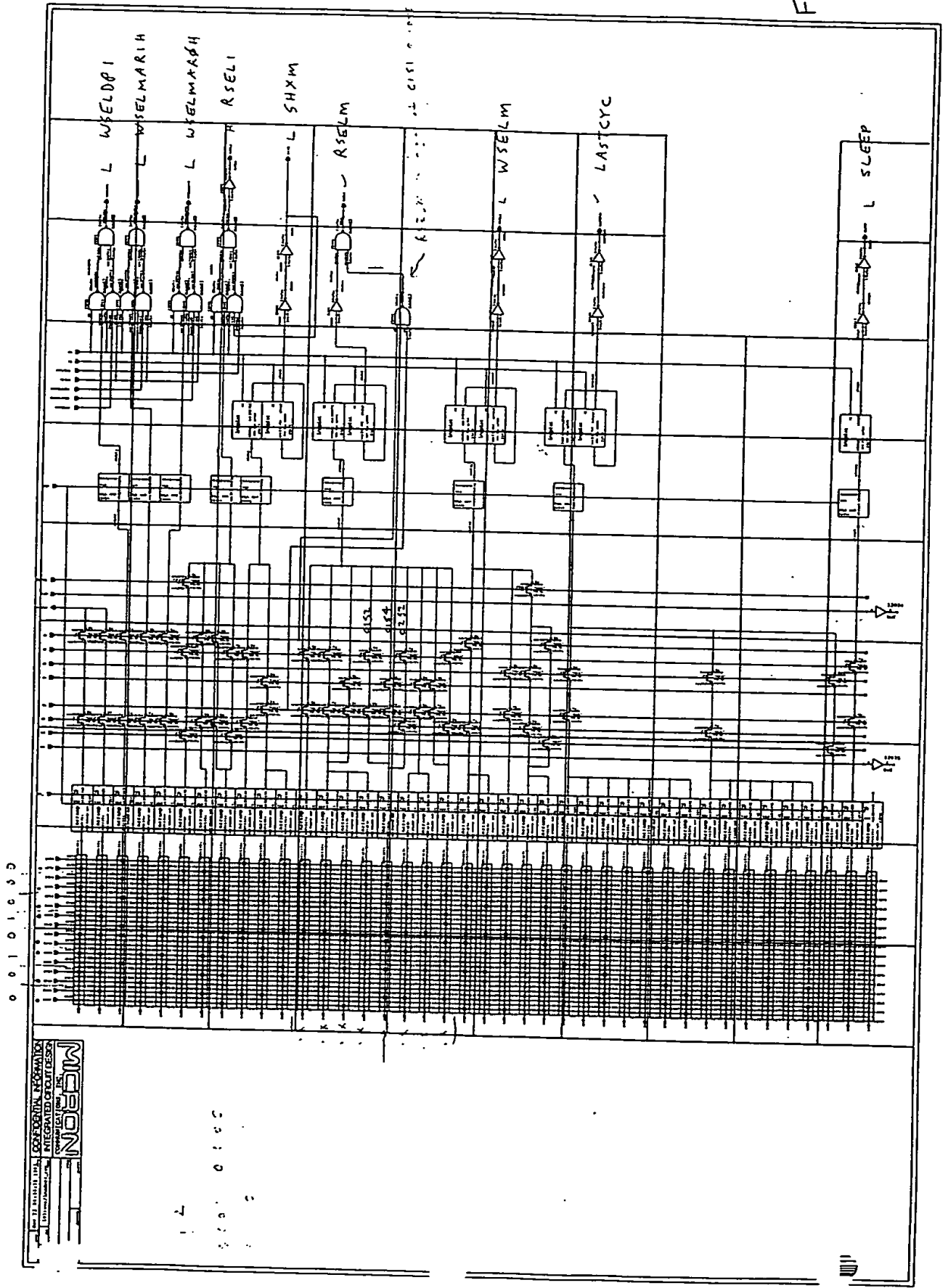
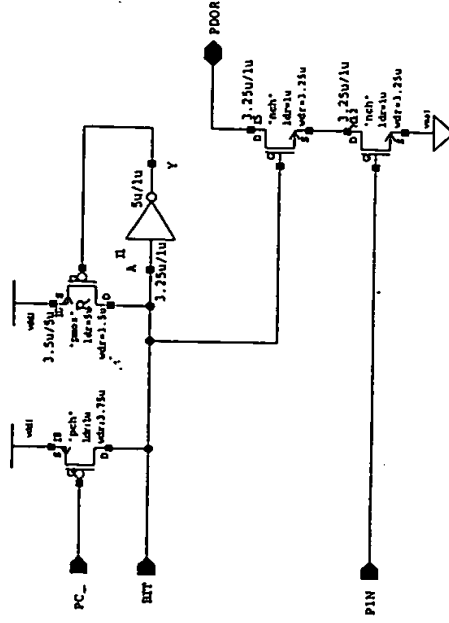
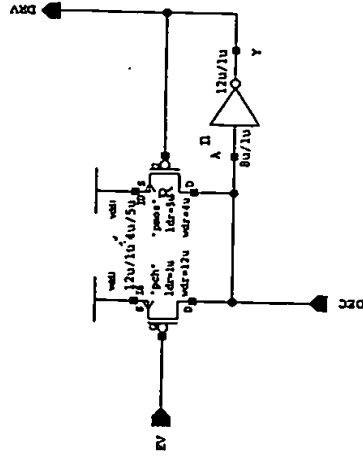


FIG. 7.06004

FIG. 7.060401



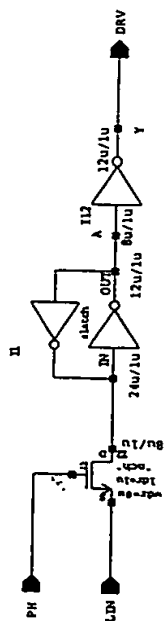
MICRON		PROJECT: L03	DATE: 10/20/93
COMMUNICATIONS, INC.		TITLE: Instruction Decoder ROM Amp	
INTEGRATED CIRCUIT DESIGN		NO.:	REV: -
CONFIDENTIAL INFORMATION		DATE: Oct 6 12:20:55 1993	



MICRON		PRODUCT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: Instruction Decoder PLA Amp	
INTEGRATED CIRCUIT DESIGN		MADE: 103reva/inspamp	REV: -
CONFIDENTIAL INFORMATION		DATE: Oct 6 12:21:34 1993	SIZE: A

FIG. 7.060402

FIG. 7.060903



2002

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

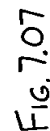
PROJECT: L03	SECTION: Rotzoll
TITLE: Instruction Decoder PLA Latch	
DATE: 103revA/Insplat	REV: - C221
DATE: Sep 29 16:10:56 1991	PROJECT: A

007720 26920300

MI40-030

7.07AA	7.07AB
7.07BA	7.07BB

IL 11 07 7.077



7.08AA

7.08BA

7.08CA

И.И.И. 7.088

NAME	ADDRESS	CITY	STATE	ZIP
JOHN A. BROWN	1234 E. 10th St.	Wichita	KS	67202
MARY L. GREEN	5678 N. 2nd St.	Omaha	NE	68104
ROBERT C. WHITE	9101 S. 15th St.	Lincoln	NE	68502
SARAH J. BLACK	3456 W. 5th St.	Lawrence	KS	66044
WILLIAM D. GRAY	7890 E. 12th St.	Overland Park	KS	66204
ELIZABETH K. HARRIS	2345 N. 10th St.	Springfield	MO	65804
FRANK M. JONES	6789 S. 8th St.	St. Louis	MO	63105
HELEN P. SMITH	1011 E. 14th St.	Kansas City	MO	64108
CHARLES R. WALKER	4567 W. 12th St.	Wichita	KS	67202
JOAN E. ROSS	8901 N. 18th St.	Omaha	NE	68104
ALBERT L. KING	1357 S. 6th St.	Lincoln	NE	68502
BETTY F. LEE	2468 E. 4th St.	Lawrence	KS	66044
EDWARD G. HILL	3579 W. 16th St.	Overland Park	KS	66204
GLORIA A. SCOTT	4680 N. 14th St.	Springfield	MO	65804
IRVING S. ADAMS	5791 S. 12th St.	St. Louis	MO	63105
JANE M. BAKER	6802 E. 10th St.	Kansas City	MO	64108
LEONARD T. NELSON	7913 W. 8th St.	Wichita	KS	67202
MARGARET W. CARROLL	8024 N. 16th St.	Omaha	NE	68104
NATHAN B. PETERSON	9135 S. 10th St.	Lincoln	NE	68502
OLIVER C. REED	1246 E. 6th St.	Lawrence	KS	66044
PATRICIA D. COOK	2357 W. 18th St.	Overland Park	KS	66204
QUENTIN E. BARNES	3468 N. 10th St.	Springfield	MO	65804
ROSAMUND F. FOSTER	4579 S. 8th St.	St. Louis	MO	63105
STANLEY G. GIBSON	5680 E. 14th St.	Kansas City	MO	64108
THERESA H. HENRY	6791 W. 12th St.	Wichita	KS	67202
WALTER I. JAMES	7802 N. 16th St.	Omaha	NE	68104
XENIA K. KELLEY	8913 S. 14th St.	Lincoln	NE	68502
YOUNG L. LAMAR	9024 E. 6th St.	Lawrence	KS	66044
ZACHARY M. LEWIS	1035 W. 18th St.	Overland Park	KS	66204

22

NOIR



COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION	DATE	14-51-40	1003	REV.	-	REV.	111
CONFIDENTIAL INFORMATION	DATE	10-5-40	1003	REV.	-	REV.	111

7.09AA	7.09AB	7.09AC	7.09AD	7.09AE	7.09AF
7.09BA	7.09BB	7.09BC	7.09BD	7.09BE	7.09BF

This is a detailed technical wiring diagram for an aircraft engine system. The diagram illustrates the complex network of electrical connections between various components. Key elements include:

- Wiring Diagram:** The main title of the document.
- Engine:** The central component being serviced, with numerous wires connected to its various parts.
- Fuel System:** Components related to the engine's fuel supply.
- Electrical System:** Components related to the engine's electrical power and control.
- Air Conditioning System:** Components related to the engine's climate control.
- Legend:** A key for the symbols used in the diagram, including:
 - Wires (represented by lines)
 - Connectors (represented by circles)
 - Switches (represented by rectangles)
 - Relays (represented by squares)
 - Grounds (represented by triangles)

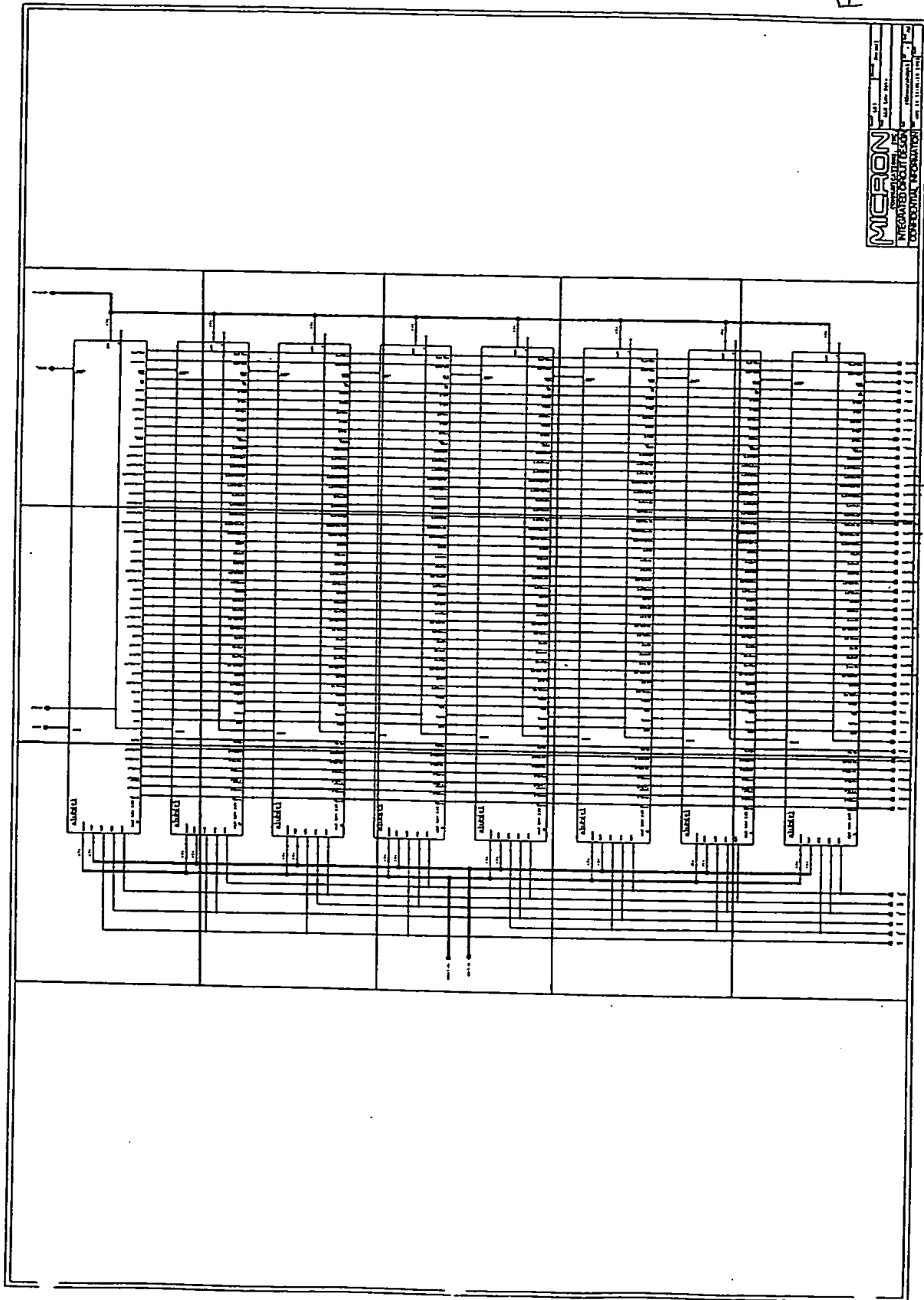
The diagram is a black and white line drawing, showing the intricate layout of the wiring harness. It is a technical drawing, likely from a manual or a technical specification document.

MICRON		Part No.	Lot	Q'ty	Unit Price	Total
SPECIALTY ELECTRONICS, INC.						
INTEGRATED CIRCUIT DESIG.						
CONFIDENTIAL INFORMATION						

7.0901AA	7.0901AB	7.0901AC	7.0901AD	7.0901AE
7.0901BA	7.0901BB	7.0901BC	7.0901BD	7.0901BE
7.0901CA	7.0901CB	7.0901CC	7.0901CD	7.0901CE

И. П. С. И. П. С. И.

Fig. 7.0901



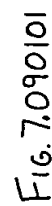
007720 20320500

MI40-030

7.090101AA	7.090101AB	7.090101AC	7.090101AD
------------	------------	------------	------------

II 7.090101

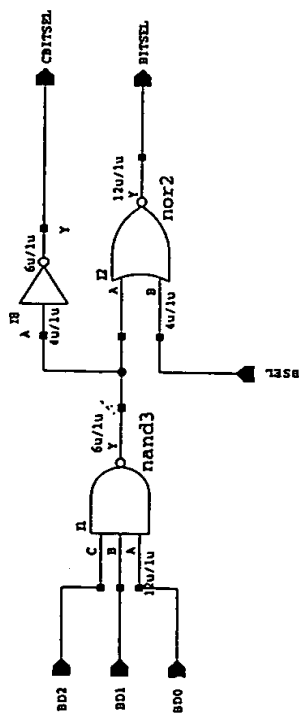
三



MICRON
FEDERAL CAPITAL, INC.
INTEGRATED CAPITAL OF SEAS
CONFIDENTIAL INFORMATION

DATE: 10/11/1991
TIME: 10:11:19
PAGE: 10

Fig. 7.09010101



MICRON			
COMMUNICATIONS, INC.			
INTEGRATED CIRCUIT DESIGN			
CONFIDENTIAL INFORMATION			
PROJECT:	L03	DESIGNER:	Rotzoll
TITLE:	ALU Bit Decoder Cell		
MAKER:	103reva/alubdec	REV:	- A
DATE:	Sep 29 16:07:43 1993		

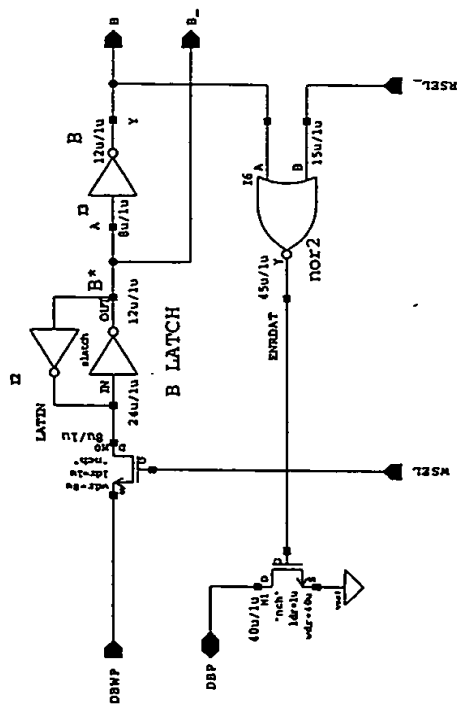


Fig. 7.09010102

MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: ALU B Register Cell	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/alubcell	REV: -
CONFIDENTIAL INFORMATION		DATE: Oct 1 15:32:35 1993	SIZE: A

004420-20920500

7.09010103AB

7.09010103AA

7.09010103

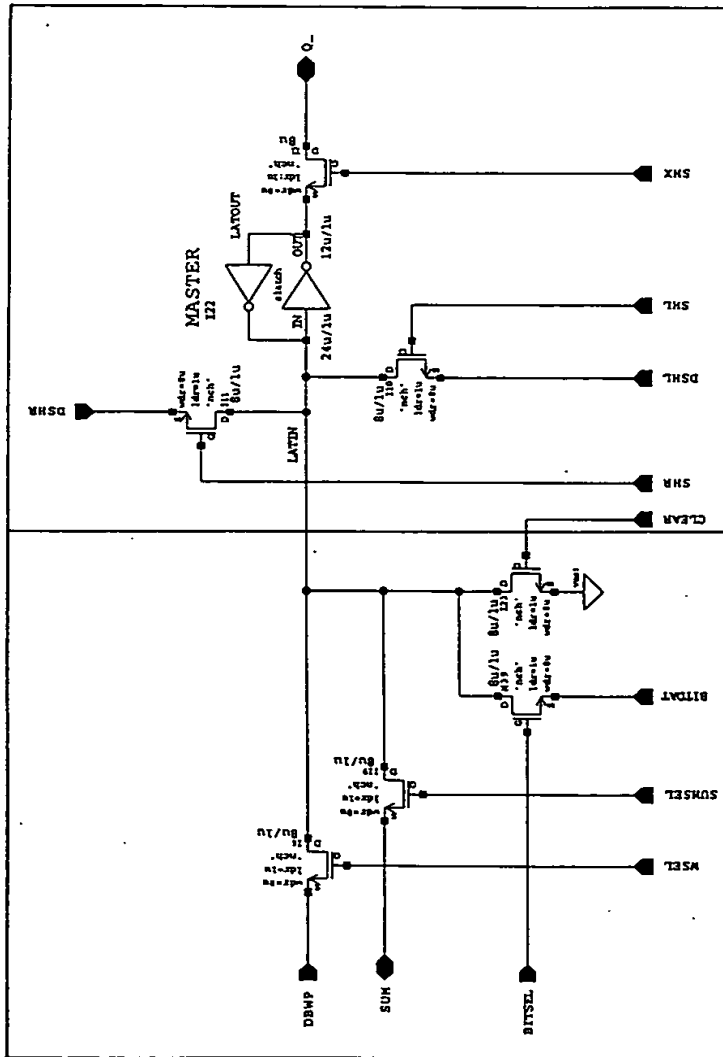


FIG. 7.09010103

MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: ALU A Register Cell	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/aluacell	REV: A
CONFIDENTIAL INFORMATION		DATE: Oct 1 15:41:37 1993	SHEET: 1

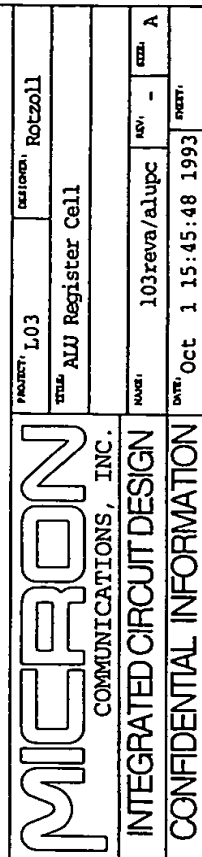
001120"20320500

MI40-030

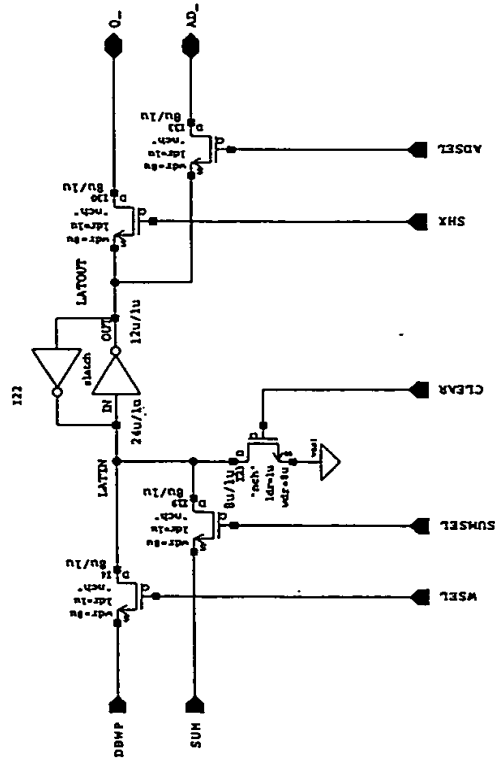
7.09010104AA	7.09010104AB
--------------	--------------

EX 7.09010104

FIG. 7.09010104



004420 20920300



MICRON		COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN		CONFIDENTIAL INFORMATION	
PROJECT: L03		DESIGNER: Rotzoll	
TITLE: ALU Register Cell		REV: -	
PAGE: 103reva/alurcell		SIZE: A	
DATE: Oct 1 15:51:03 1993		SHEET: 1	

FIG. 7.09010105

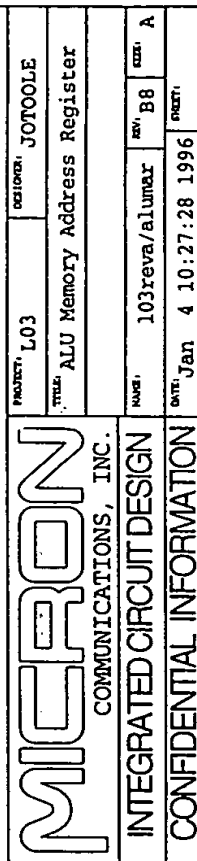
007420 20920500

MI40-030

7.09010106AA	7.09010106AB
--------------	--------------

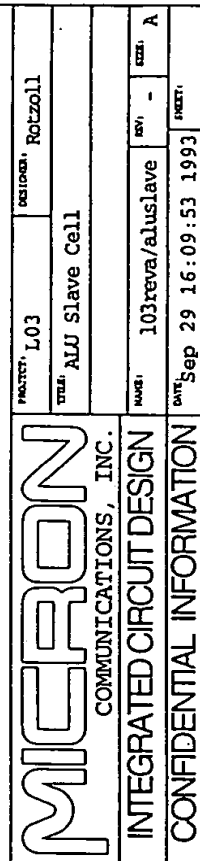
EX 109010106

FIG. 7.09010106



B8: added pch feedback device

FIG. 7.09010107



007720" 20320500

MI40-030

7.09010108AA	7.09010108AB	7.09010108AC
7.09010108BA	7.09010108BB	7.09010108BC

И. И. С. 7.09010108BB

001100 00000000

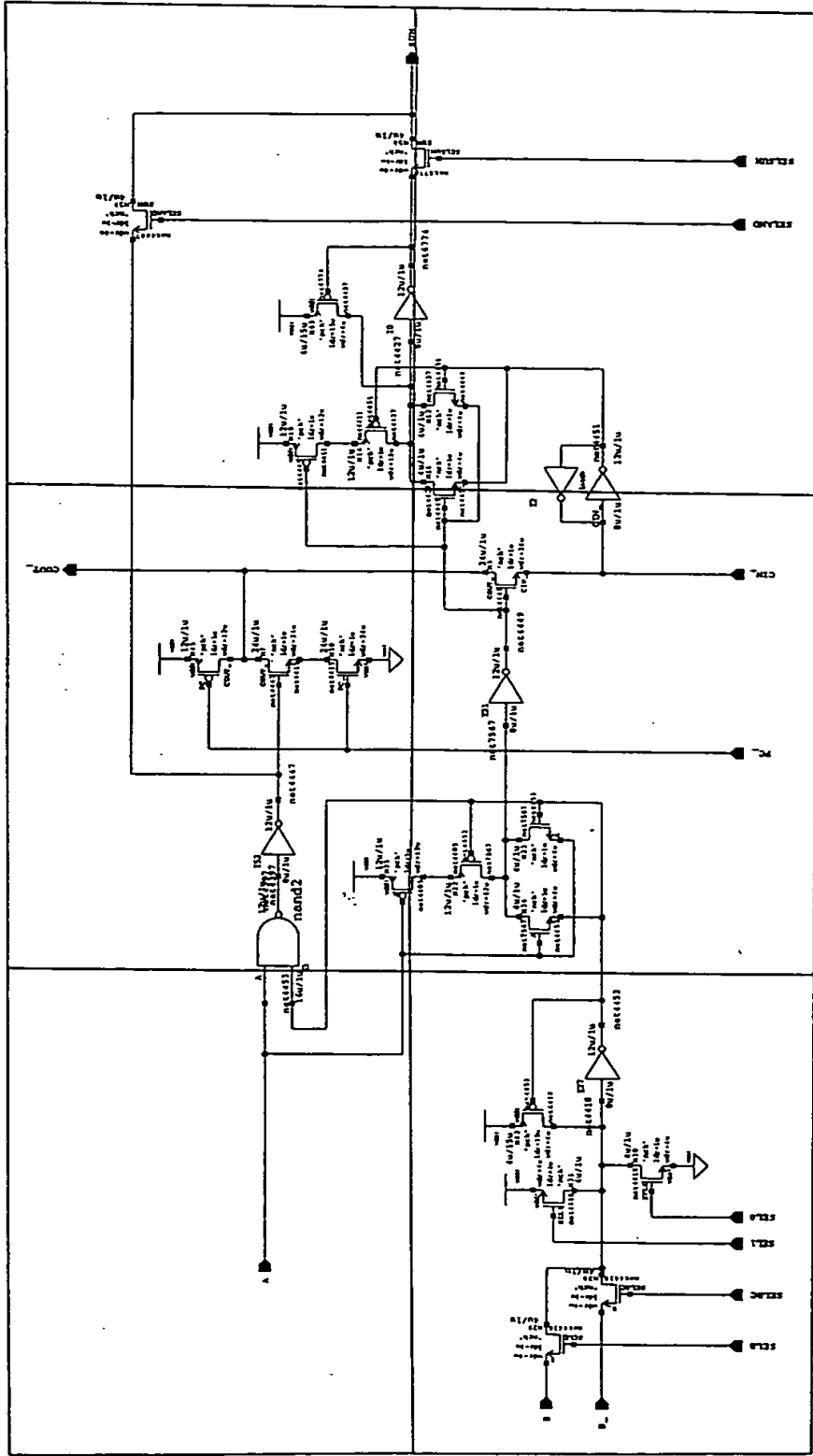


Fig. 7.09010108
B5: move feedback device from I21 to I27

MICRON		PROPERTY L03	DATE 08/01/91	JOTOOLE
COMMUNICATIONS, INC.		ALU Address		
INTEGRATED CIRCUIT DESIGN		NAME	103rova/aluadd	REV B5
CONFIDENTIAL INFORMATION		DATE	Sep 16 15:48:21 1995	REV rli

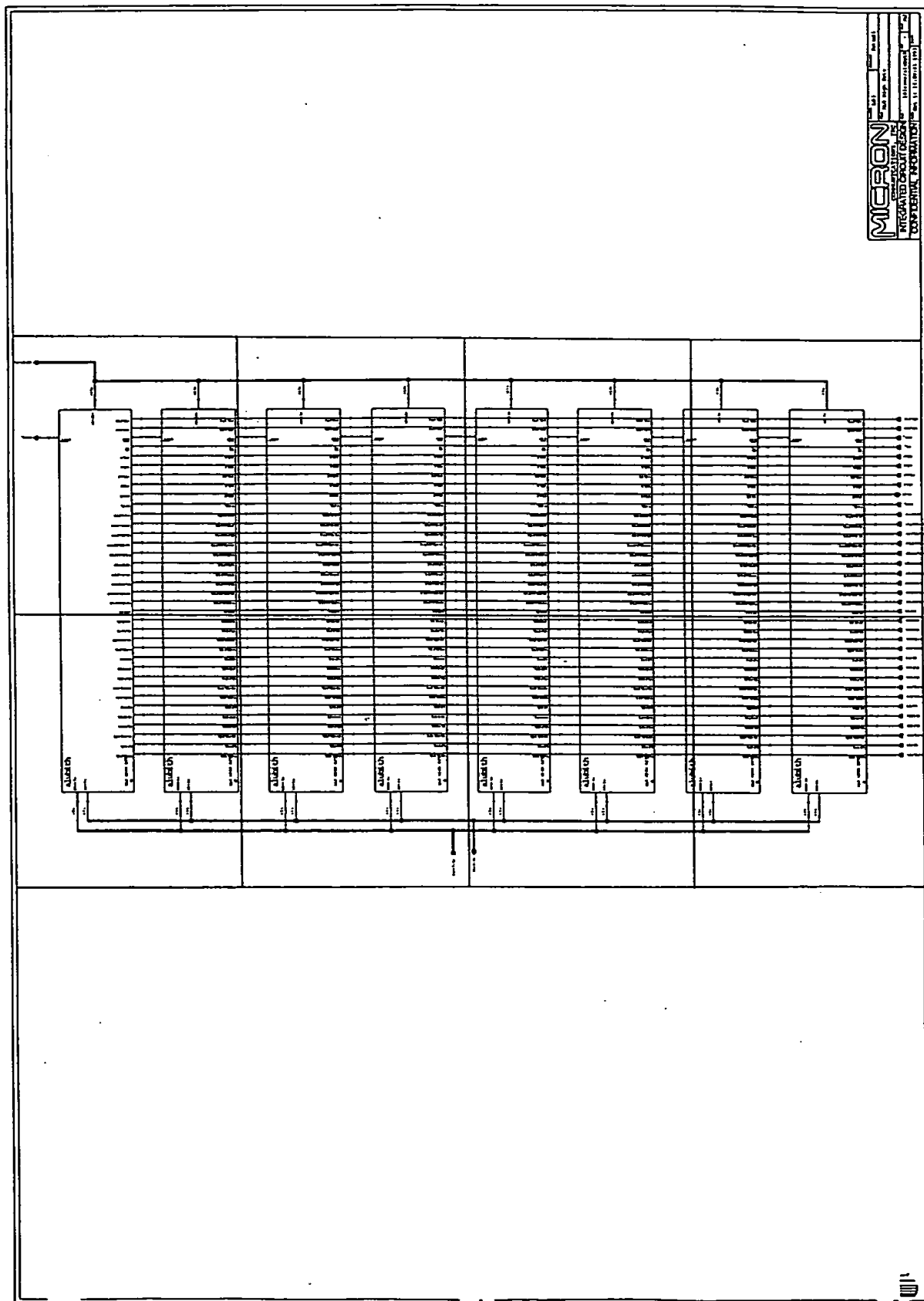
007420 20920500

MI40-030

7.0902AA	7.0902AB	7.0902AC	7.0902AD	7.0902BD
7.0902BA	7.0902BB	7.0902BC		

II 9 7.0902

Fig. 7.0902



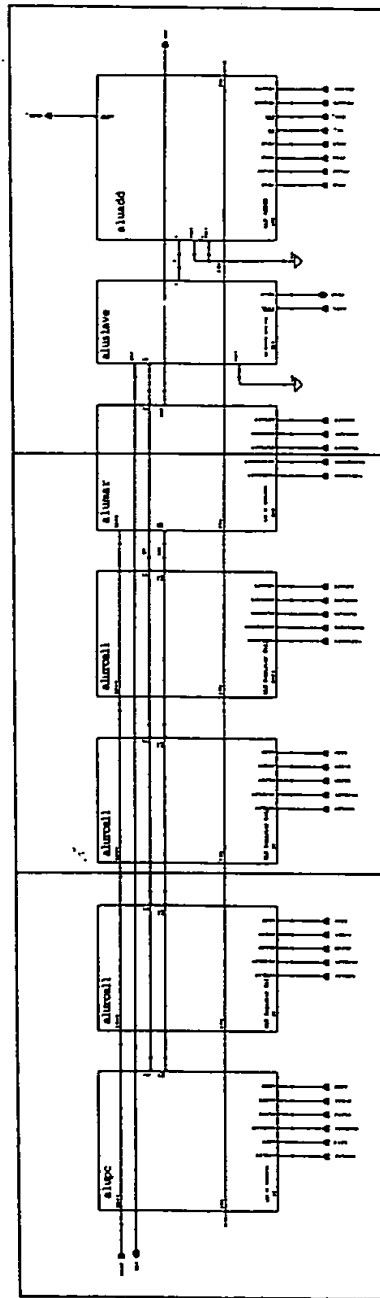
007420 20920500

MI40-030

7.090201AA	7.090201AB	7.090201AC
------------	------------	------------

II 7.090201

FIG. 7.090201



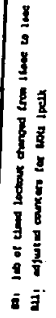
MICRON		Company (alt)		Fax (alt)	
CORPORATION, INC.		100 North 111			
INTEGRATED CIRCUIT DESIGN		100 North 111			
CONFIDENTIAL INFORMATION		100 North 111			

DD FORM 2692-550

MI40-030

7.10AA	7.10AB	7.10AC
7.10BA	7.10BB	7.10BC
7.10CA	7.10CB	7.10CC

II II II II



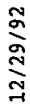


Fig. 7.1001

MICRON COMMUNICATIONS, INC.			
INTEGRATED CIRCUIT DESIGN			
CONFIDENTIAL INFORMATION			
PROJECT: L03	DESIGNER: Rotzoll		
TITLE: Timed Lockout Divider Cell			
NAME: 103reva/Eladcel	REV:	SER:	A
DATE: Sep 22 15:26:56 1994		ENCRYT:	

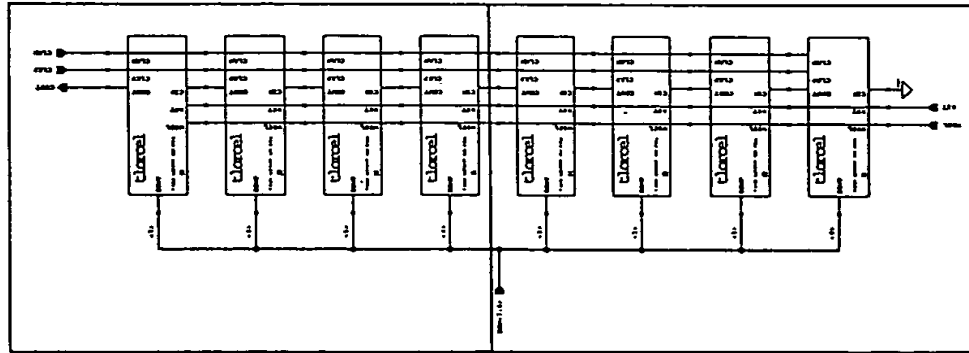
001120 20320300

MI40-030

7.11AA	7.11AB
--------	--------

Ex 07 11.11.11

FIG. 7.11



MICRON		L-3		Serial	
COMMUNICATIONS, INC.		Trained Lockout Register			
INTEGRATED CIRCUIT DESIGN		101 years/Class		12	
CONFIDENTIAL INFORMATION		Dec 12 1995 17:19			

DD FORM 20320566

MI40-030

7.1101AA	7.1101AB	7.1101AC
----------	----------	----------

II II II II II

007420" 26920500

MI40-030

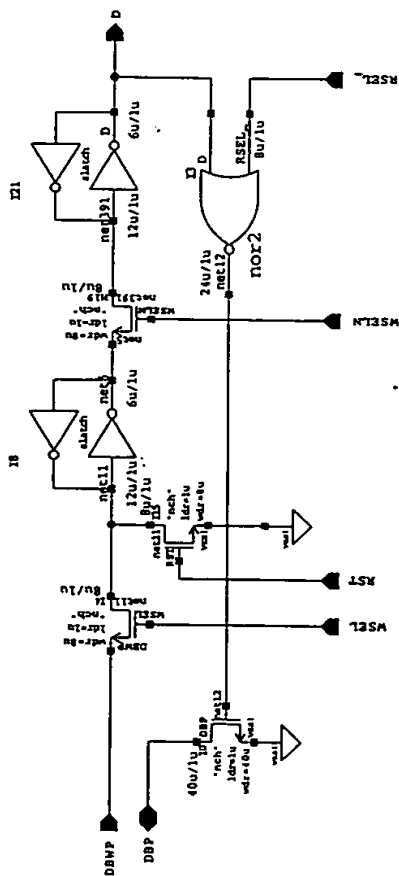
7.12AA	7.12AB	7.12AC
--------	--------	--------

IL 11 11 11

[illegible]

MICRON		PROJECT: L03	REVISION: R02011
COMMUNICATIONS, INC.		R/W Control Register	
INTEGRATED CIRCUIT DESIGN		NAME:	103rows/4096 REV: - PAGE: 1/11
CONFIDENTIAL INFORMATION		DATE: Nov 12 09:44:40 1993 USER:	

FIG. 7.1201



MICRAON			
COMMUNICATIONS, INC.			
INTEGRATED CIRCUIT DESIGN			
CONFIDENTIAL INFORMATION			
PROJECT: L03	DESIGNER: Rotzoll		
TITLE: R/W Control Register Cell			
NAME: 103reva/regcell	REV: -	FEED: A	
DATE: Nov 12 09:41:36 1993		SHEET: 1	

7.13AA

7.13BA

7.13

001120 20000000

004420 2030560

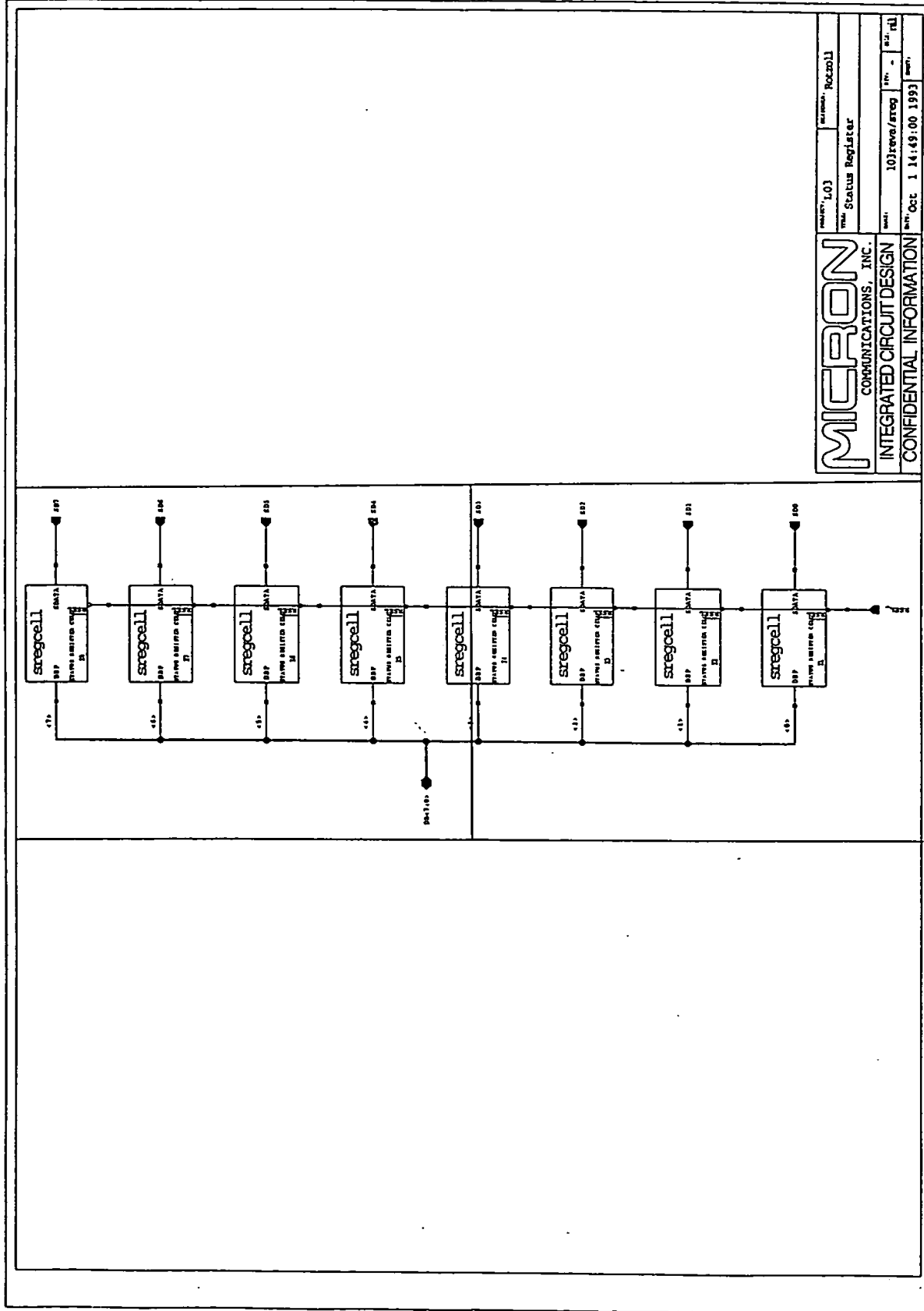
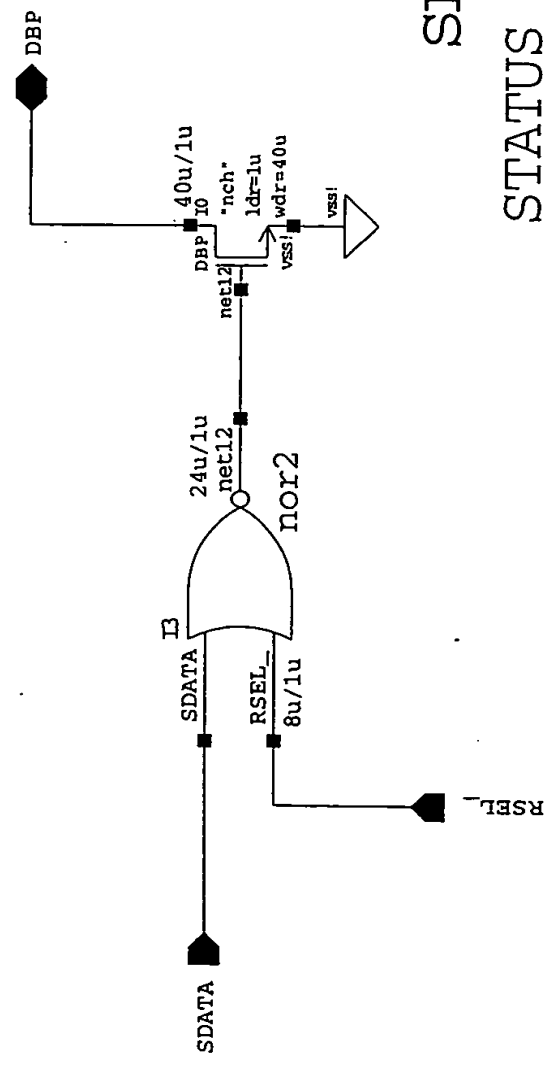


FIG. 7.13



sregcell

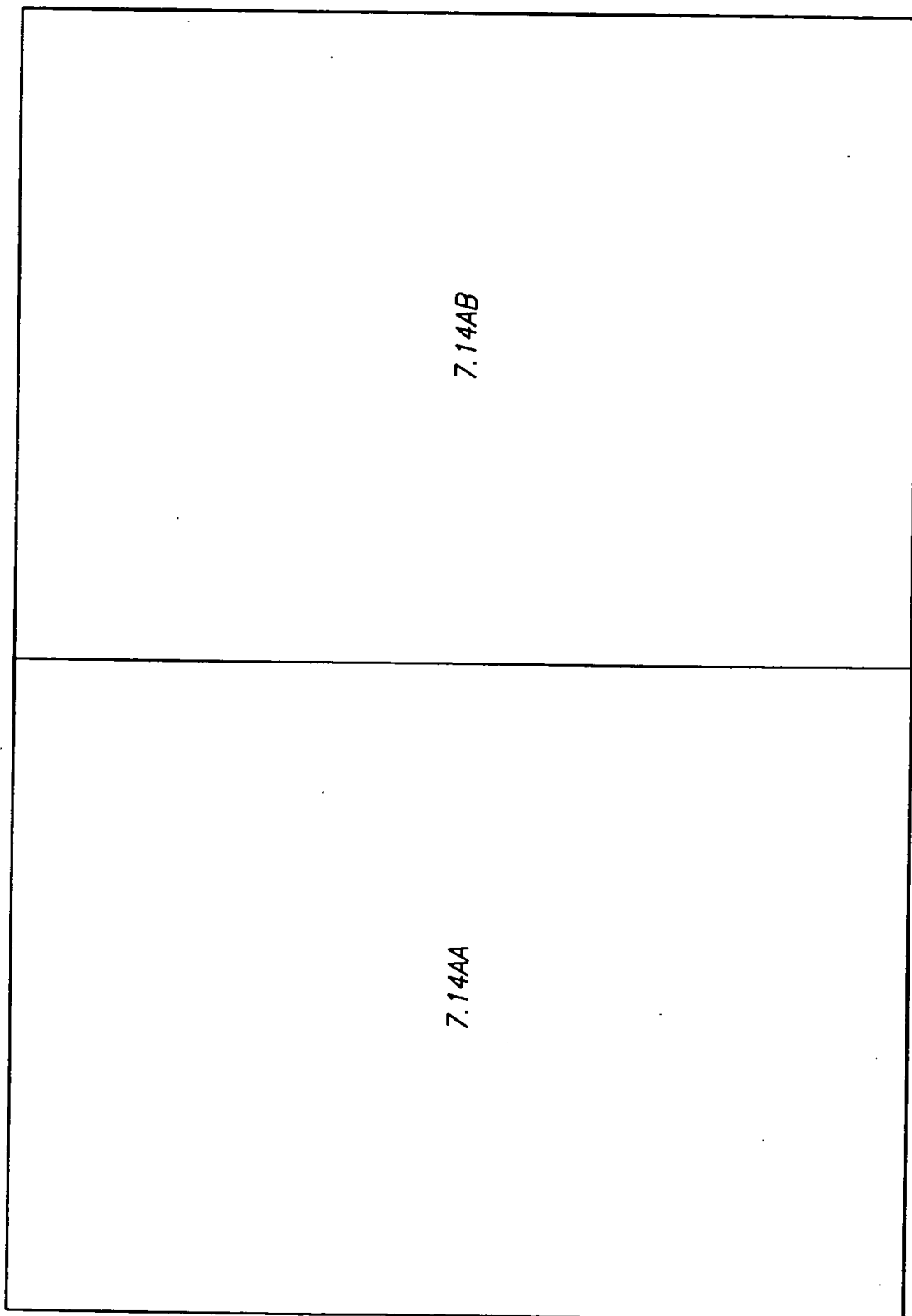
STATUS REGISTER CELL

R. Rotzoll

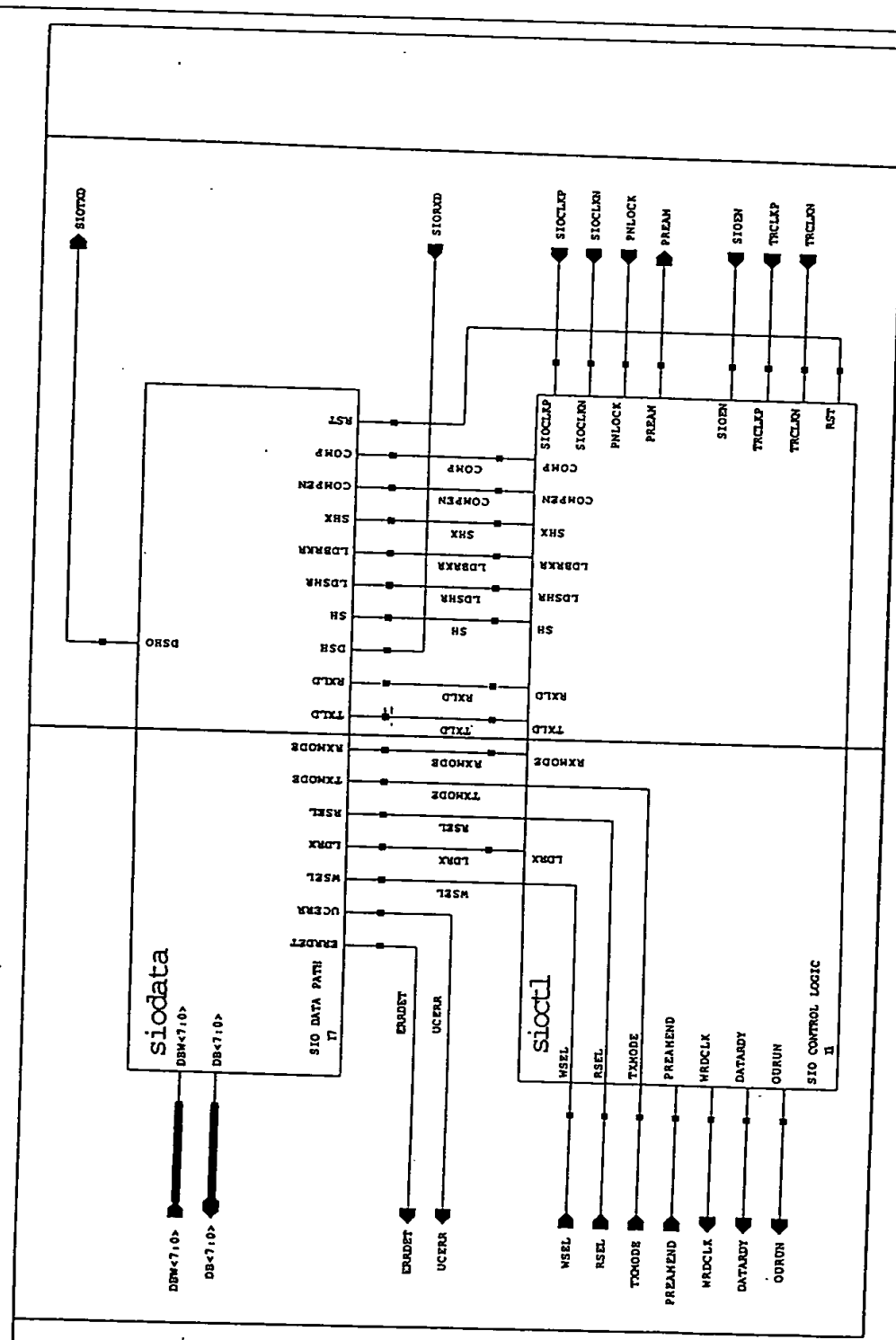
12/8/92

FIG. 7.1301

00110" 20920500



II II II 11.11.11



MICRON		PROJECT: L03	DESIGNER: ROTZOLL
COMMUNICATIONS, INC.		Serial I/O	
INTEGRATED CIRCUIT DESIGN		Serial/Block Enc/Dec	
CONFIDENTIAL INFORMATION		NAME: 103reva/sio	REV: B11
		DATE: Apr 4 10:16:14 1996	EXT: A

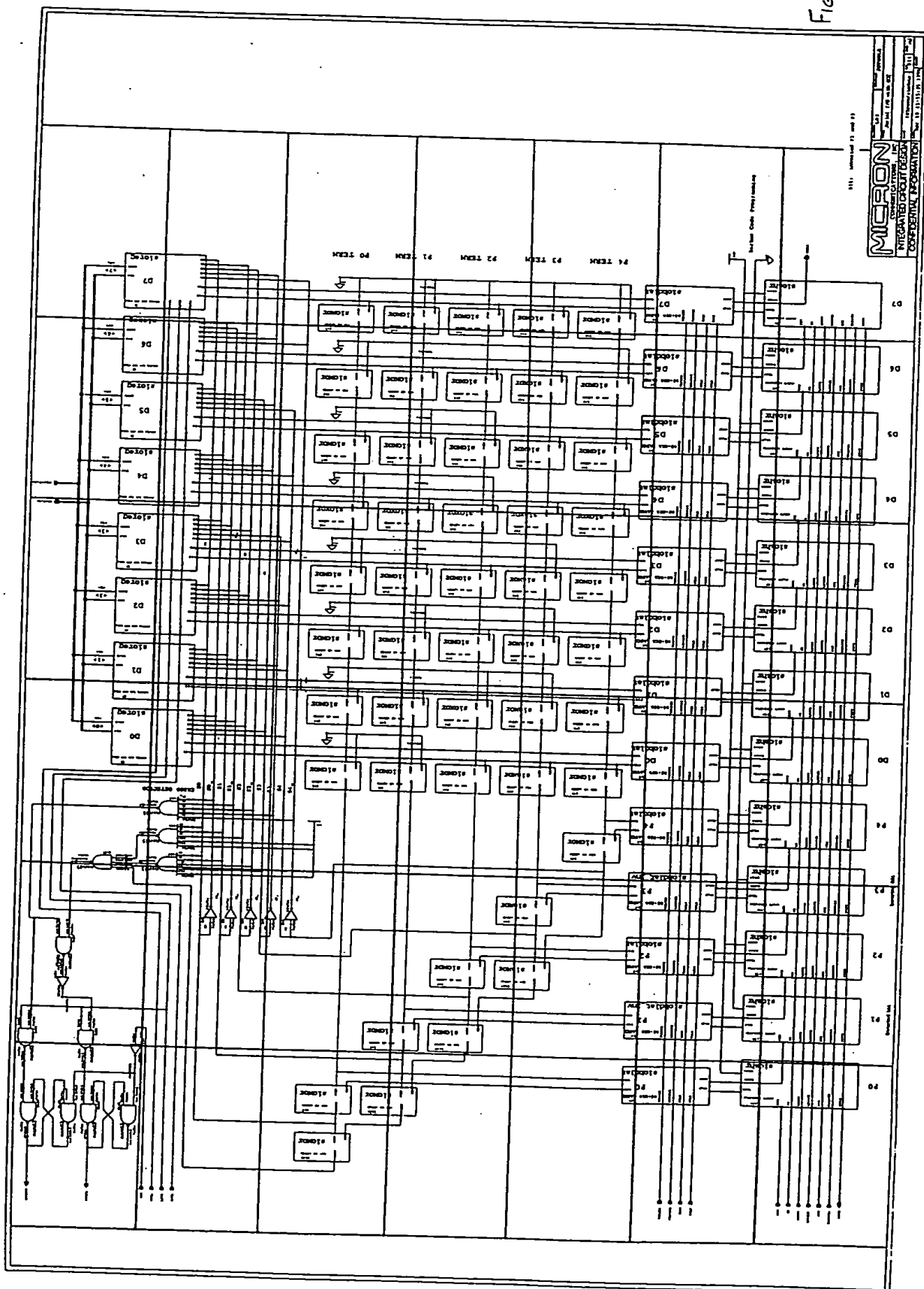
B8: deleted BRKREN
B11: added non-overlapping clocks

Fig. 7.14

7.1401AA	7.1401AB	7.1401AC	7.1401AD	7.1401AE	7.1401AF
7.1401BA	7.1401BB	7.1401BC	7.1401BD	7.1401BE	7.1401BF
7.1401CA	7.1401CB	7.1401CC	7.1401CD	7.1401CE	7.1401CF
7.1401DA	7.1401DB	7.1401DC	7.1401DD	7.1401DE	7.1401DF
7.1401EA	7.1401EB	7.1401EC	7.1401ED	7.1401EE	7.1401EF
7.1401FA	7.1401FB	7.1401FC	7.1401FD	7.1401FE	7.1401FF
7.1401GA	7.1401GB	7.1401GC	7.1401GD	7.1401GE	7.1401GF

MICRON
COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION

Date	Feb 1978
Page	1 of 1
Author	J. J. J. J.
Title	CONFIDENTIAL INFORMATION
Project No.	CONFIDENTIAL INFORMATION
Product No.	CONFIDENTIAL INFORMATION
Part No.	CONFIDENTIAL INFORMATION
Rev.	CONFIDENTIAL INFORMATION
Drawn By	CONFIDENTIAL INFORMATION
Checked By	CONFIDENTIAL INFORMATION
Approved By	CONFIDENTIAL INFORMATION

[illegible]

001120 220000

MI40-030

7.140101AA

7.140101AB

11 00 11.11.11.11.11.11

Fig. 7.140101

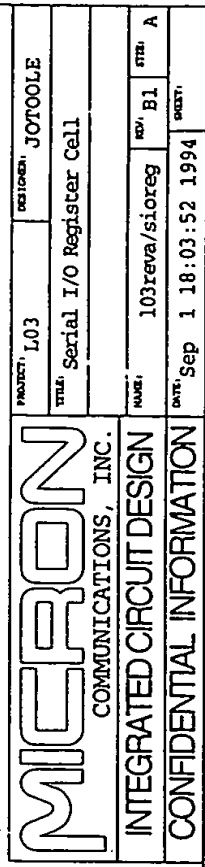


Fig. 7.140101

007120 20920500

MI40-030

7.140103AA

7.140103AB

II II 7.140103

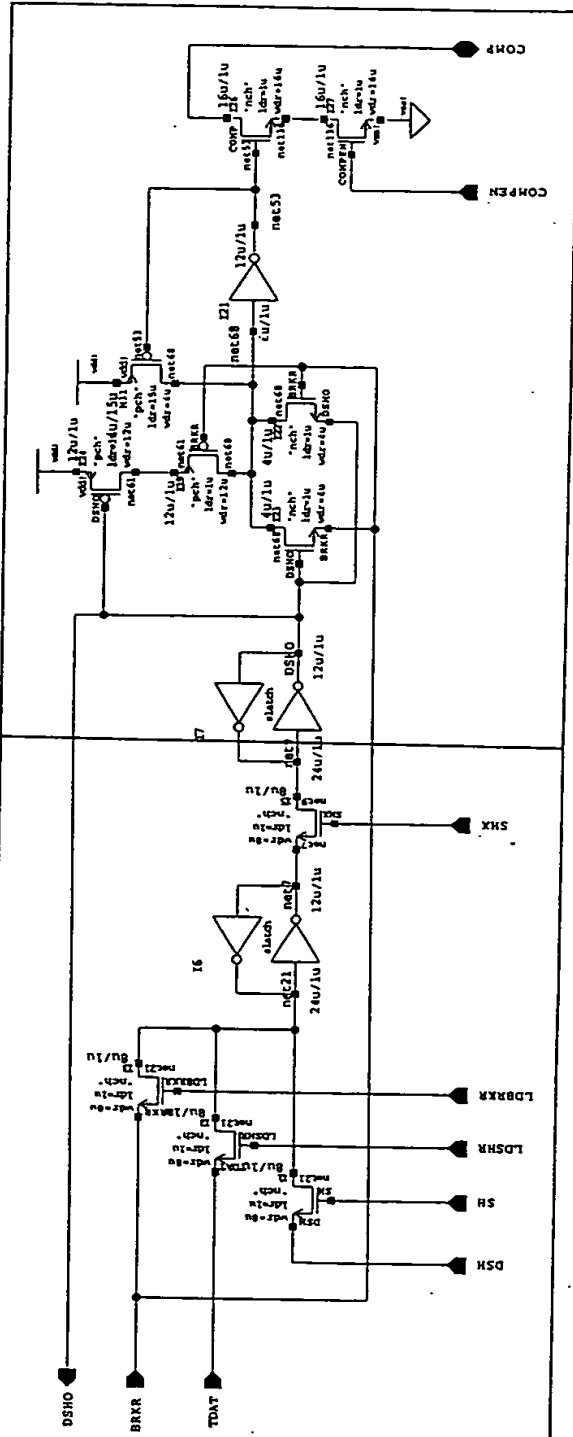
001420 20320500

MI40-030

7.140104AA	7.140104AB
------------	------------

II II II II II II II II

00420 2090000



7.140104

Fig. 7.140104

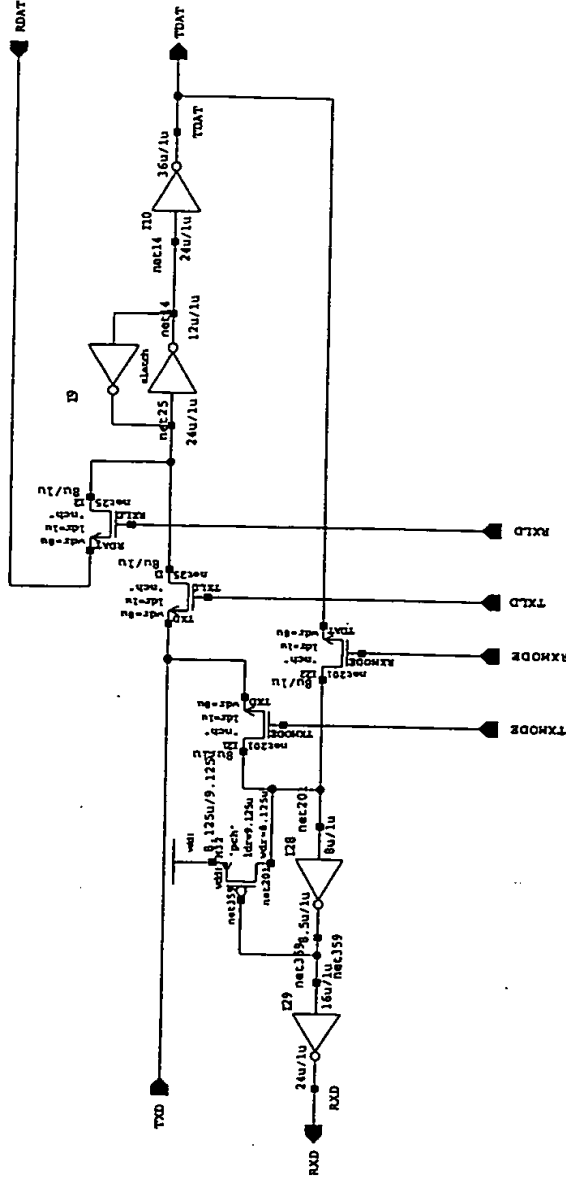
MICRON		PROJECT: L03	SECTION: J0700LE
COMMUNICATIONS, INC.		TITLE: SIO Shift Register	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/sioshr	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 2 08:06:26 1994	SHEET: A

001120" 26520500

MI40-030

7.140105AA	7.140105AB
------------	------------

IL 11 11.140105



B8: added feedback device

MICRON		PROJECT: L03	REVISION: J00001
COMMUNICATIONS, INC.		TITLE: SIO Bidirectional Latch	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/siobdlat	REV: B8
CONFIDENTIAL INFORMATION		DATE: Jan 8 11:04:57 1996	SHEET: A

Fig. 7.140105

2017.11.11

004420-20900900

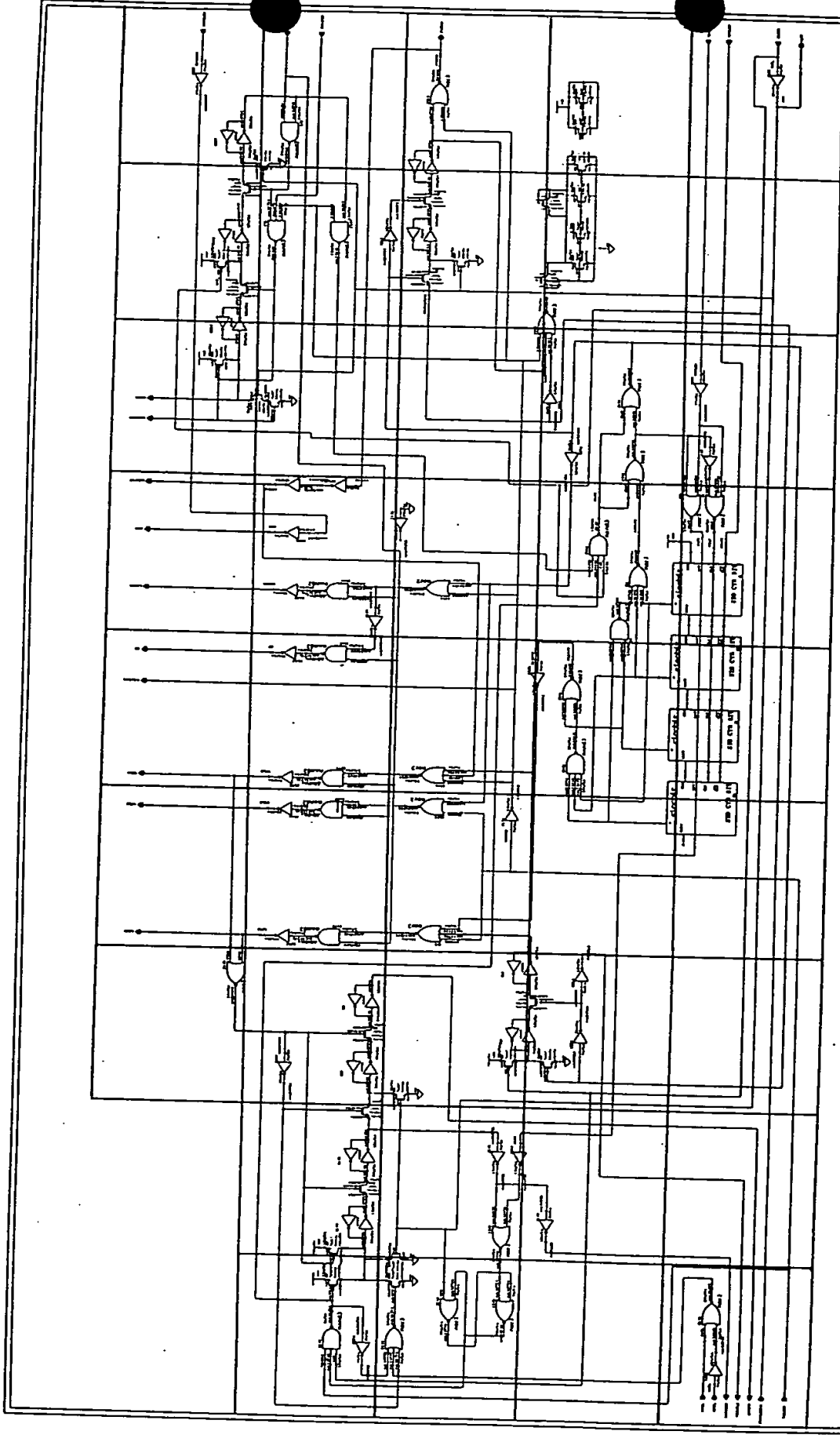


FIG. 7.1402

NOT: added function to 7400 pack
all logic gates
810: Improved the layout in 7400 pack
811: Replaced the 7400 pack with 7401
MICRON TECHNOLOGY, INC.
CORPORATION
10000 N. 10th Ave., Suite 100
Denver, CO 80231
Tel: 303.751.1111
Fax: 303.751.1112

001420 20320540

MI40-030

7.140201AA	7.140201AB
------------	------------

II 7.140201

004720-2030543

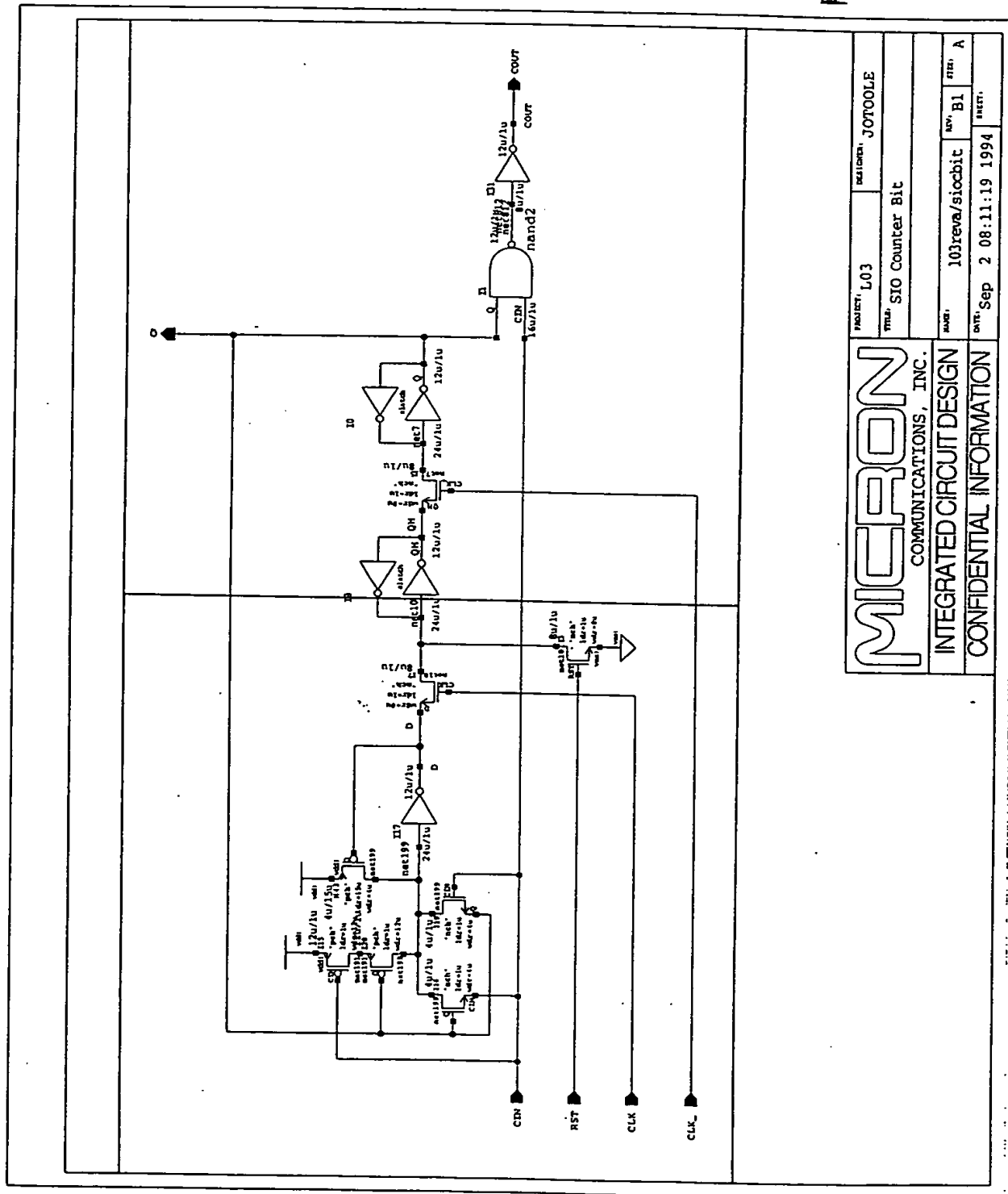
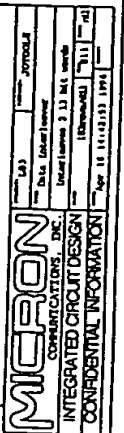


FIG. 7.190²⁰¹

MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: SIO Counter Bit	
INTEGRATED CIRCUIT DESIGN		MADE: 103revs/sioctbit	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 2 08:11:19 1994	HEET: A

7.15AA	7.15AB	7.15AC	7.15AD
7.15BA	7.15BB	7.15BC	7.15BD
7.15CA	7.15CB	7.15CC	7.15CD
7.15DA	7.15DB	7.15DC	7.15DD
7.15EA	7.15EB	7.15EC	

Fig. 7.15



7.1501AA

7.1501BA

7.1501CA

[illegible]

001100 00000000

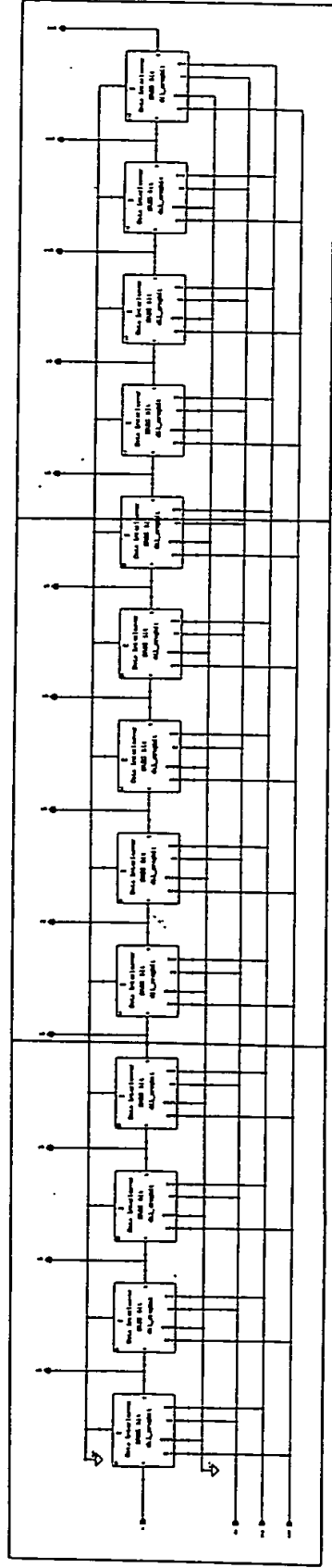


Fig. 7.1501

7.1502AA

7.1502BA

7.1502CA

ИД 7.1502

[illegible]

1

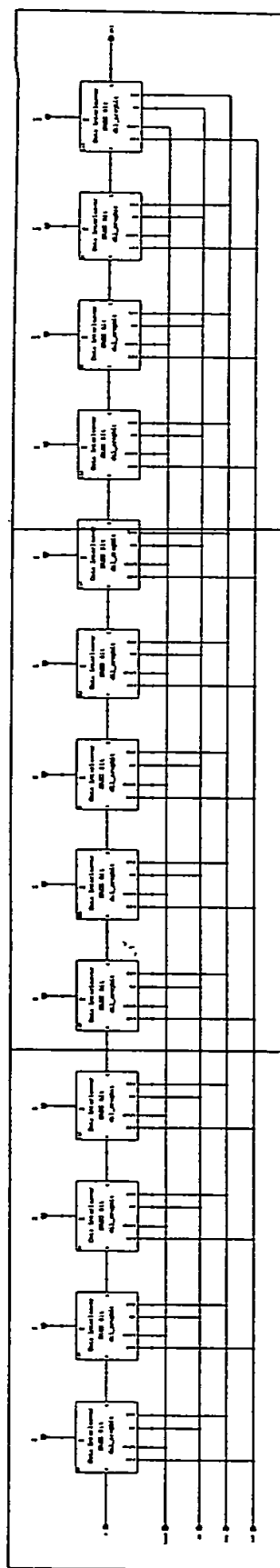
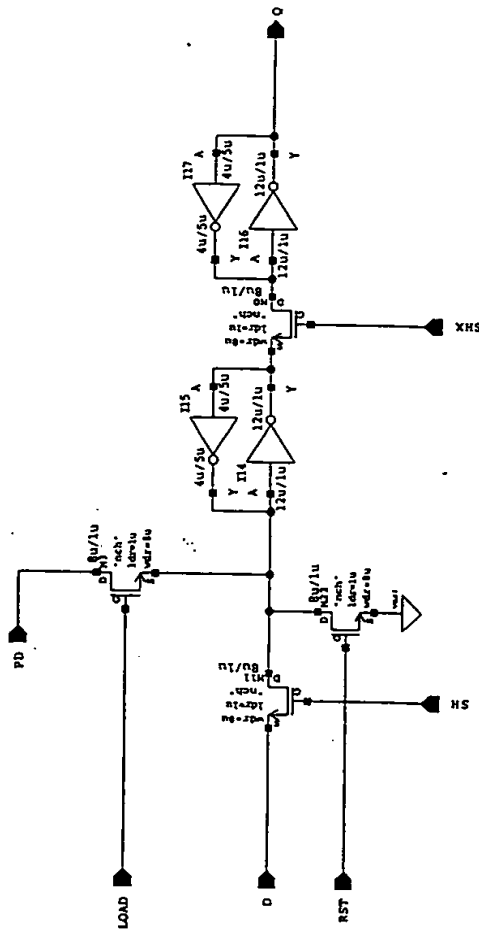


Fig. 7.1502

MICRON
COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION

Fig. 7.150201

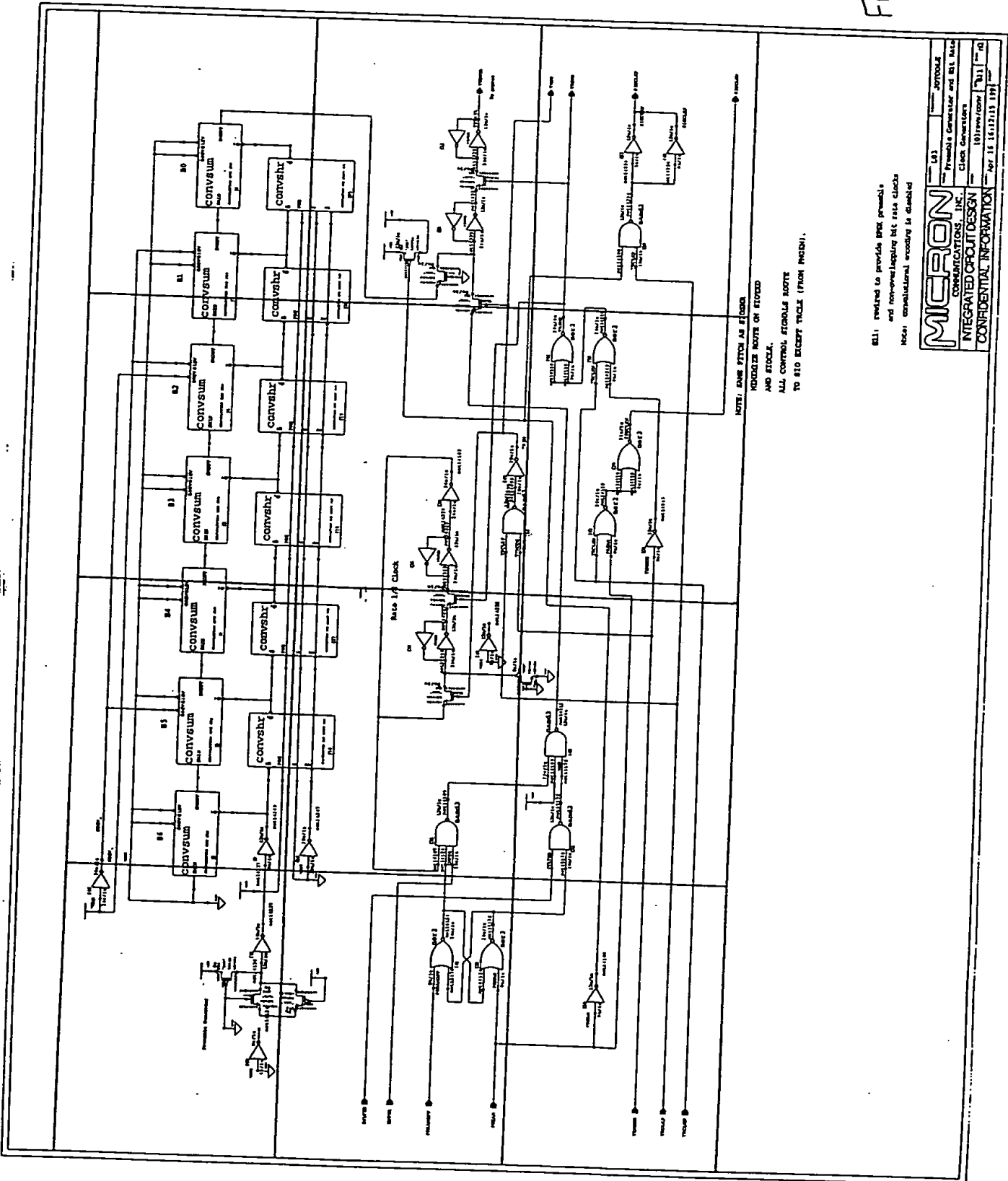


MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Data Interleaver Shift	
INTEGRATED CIRCUIT DESIGN		Register Bit	
CONFIDENTIAL INFORMATION		MAKER: 103reva/dil_sregbit	REV: B1
		DATE: Sep 27 10:25:07 1994	SIZE: A
		SECRET	

7.16AA	7.16AB	7.16AC	7.16AD
7.16BA	7.16BB	7.16BC	7.16BD
7.16CA	7.16CB	7.16CC	7.16CD

SECRET

Fig. 7.16



ELL: required to provide both parallel
and non-overlapping bit rate clocks
NOR: computational encoding is disabled

MICRON	
LA3	207004A
Preamble Generator and Bit Rate	
Clock Generators	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
10/11/1980	Rev 10 (1/12/13 1987)

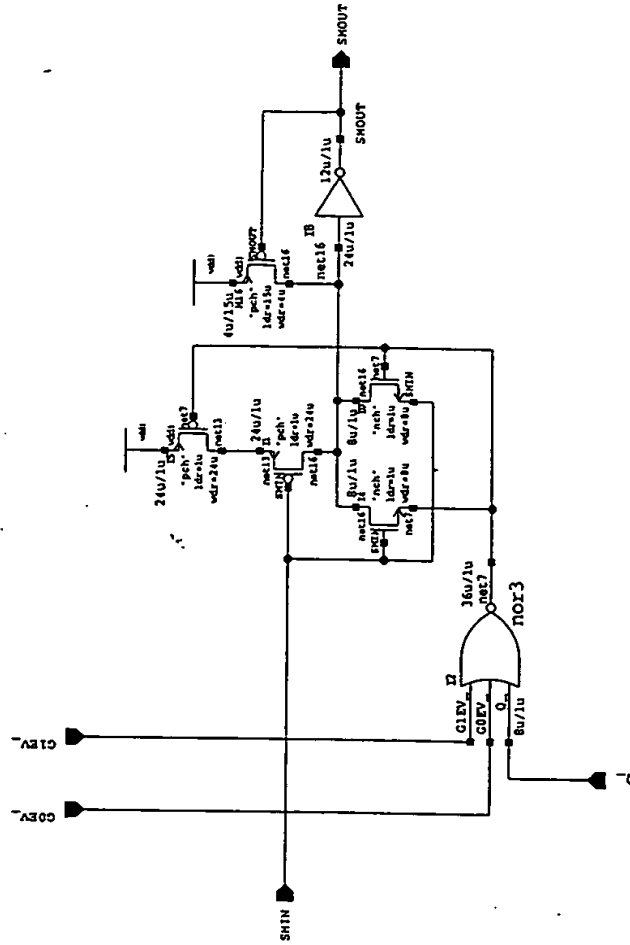


Fig. 7.1602

MICRON COMMUNICATIONS, INC.		PROJECT: L03		DESIGNER: JOTOOLE	
		TITLE: Convolutional Encoder Summer			
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/convsum	REV: B1	SIZE: A	
CONFIDENTIAL INFORMATION		DATE: Sep 2 10:32:17 1994		PAGE: 1	

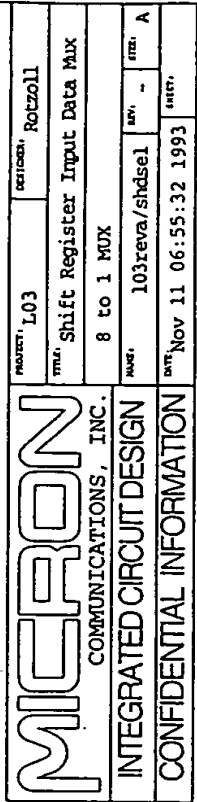
001120" 00300300

MI40-030

7.17AA	7.17AB
7.17BA	7.17BB

II. II. II

Fig. 7.17



7.18AA	7.18AB	7.18AC
7.18BA	7.18BB	7.18BC
7.18CA	7.18CB	7.18CC

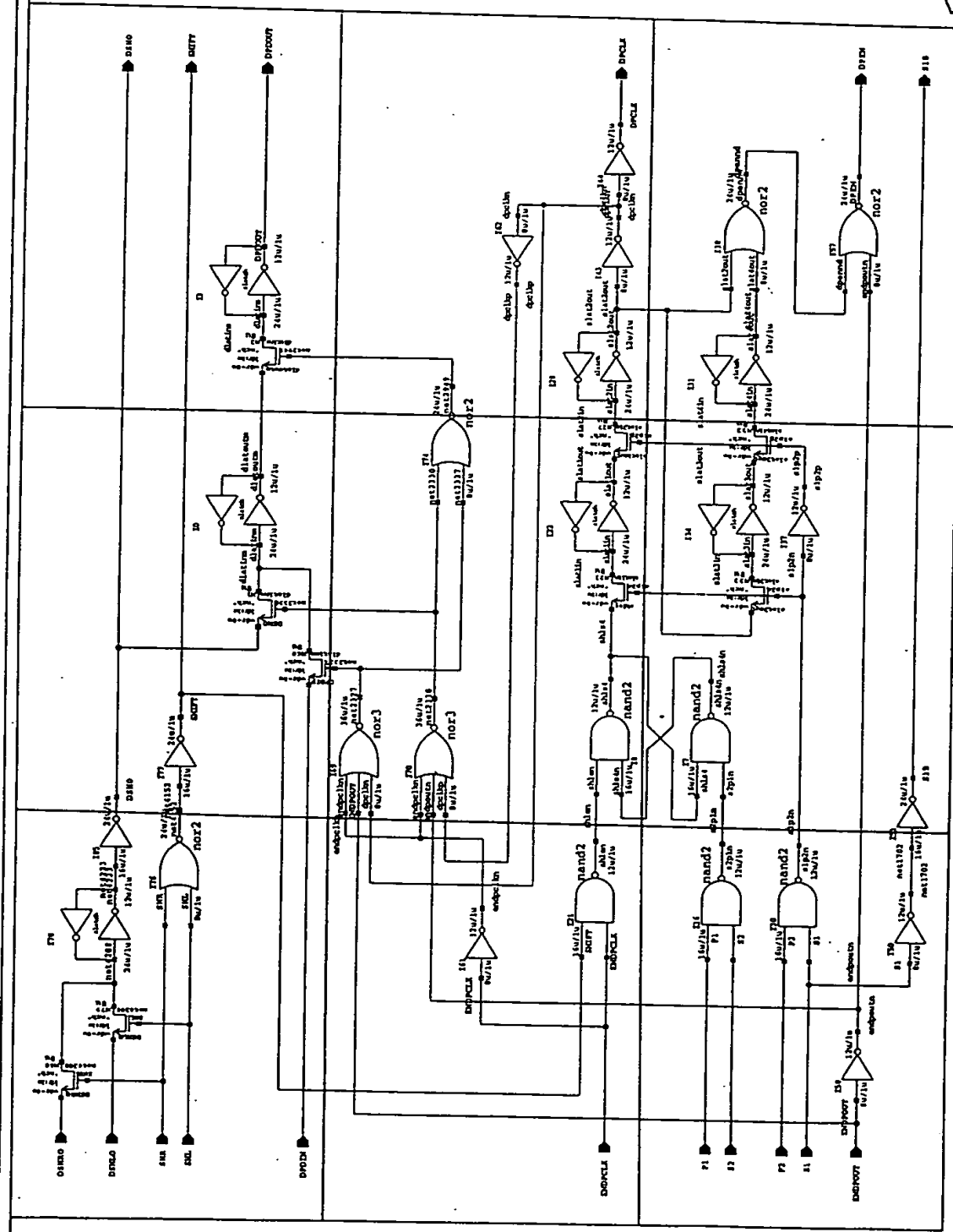
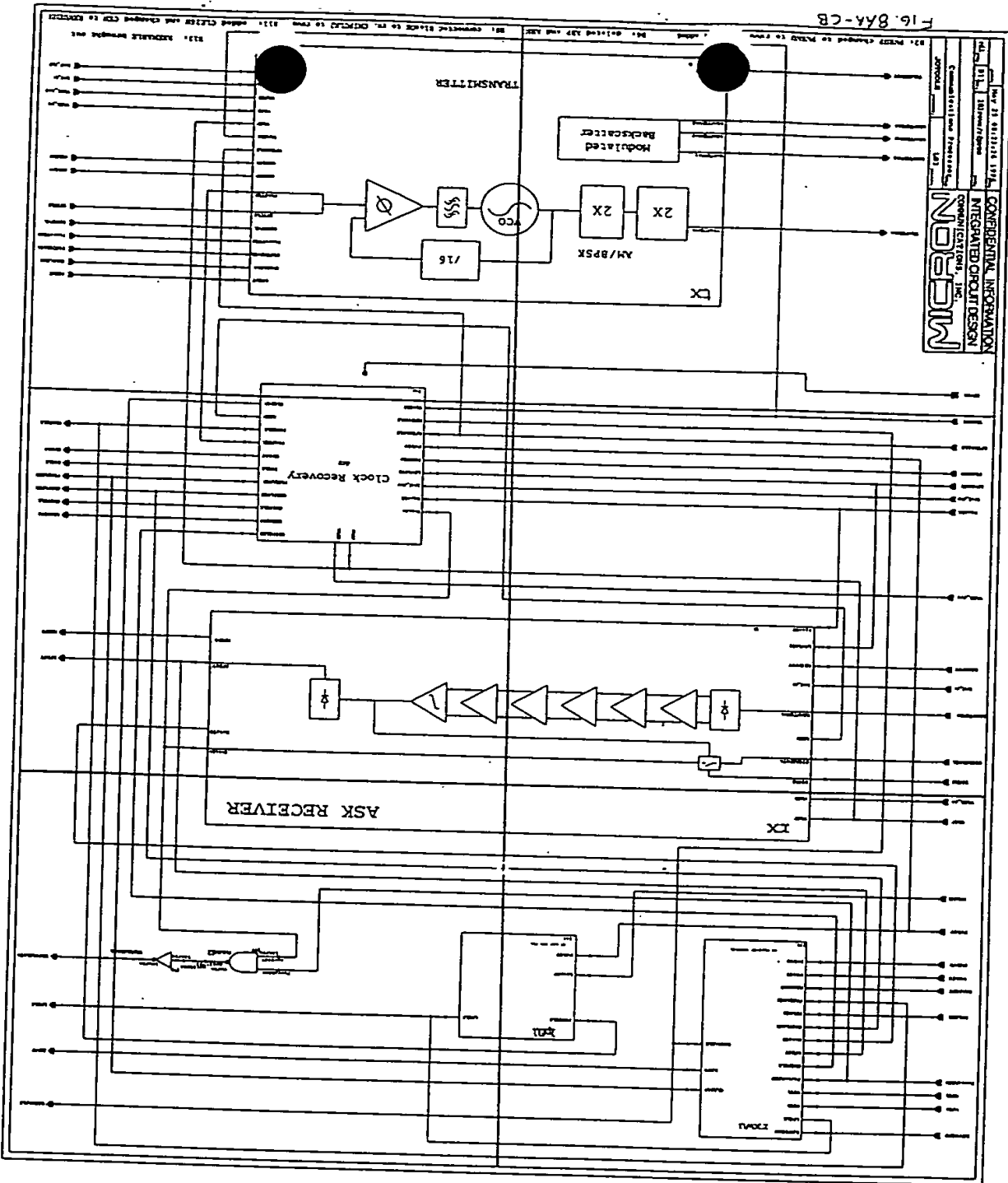


Fig. 7.18

MICRON		MODEL L03	REVISION	Rev 2.01
INTEGRATED CIRCUIT DESIGN		Digital Port Output Controller		
CONFIDENTIAL INFORMATION		DATE	10/19/93	BY
		DATE	Nov 12 10:05:40 1993	BY

8CB

(The page contains faint, illegible markings or bleed-through from the reverse side.)



09000000-004400

8.01AA	8.01AB	8.01AC	8.01AD	8.01AE
8.01BA	8.01BB	8.01BC	8.01BD	8.01BE
8.01CA	8.01CB	8.01CC	8.01CD	8.01CE
8.01DA	8.01DB	8.01DC	8.01DD	8.01DE

001420 20980500

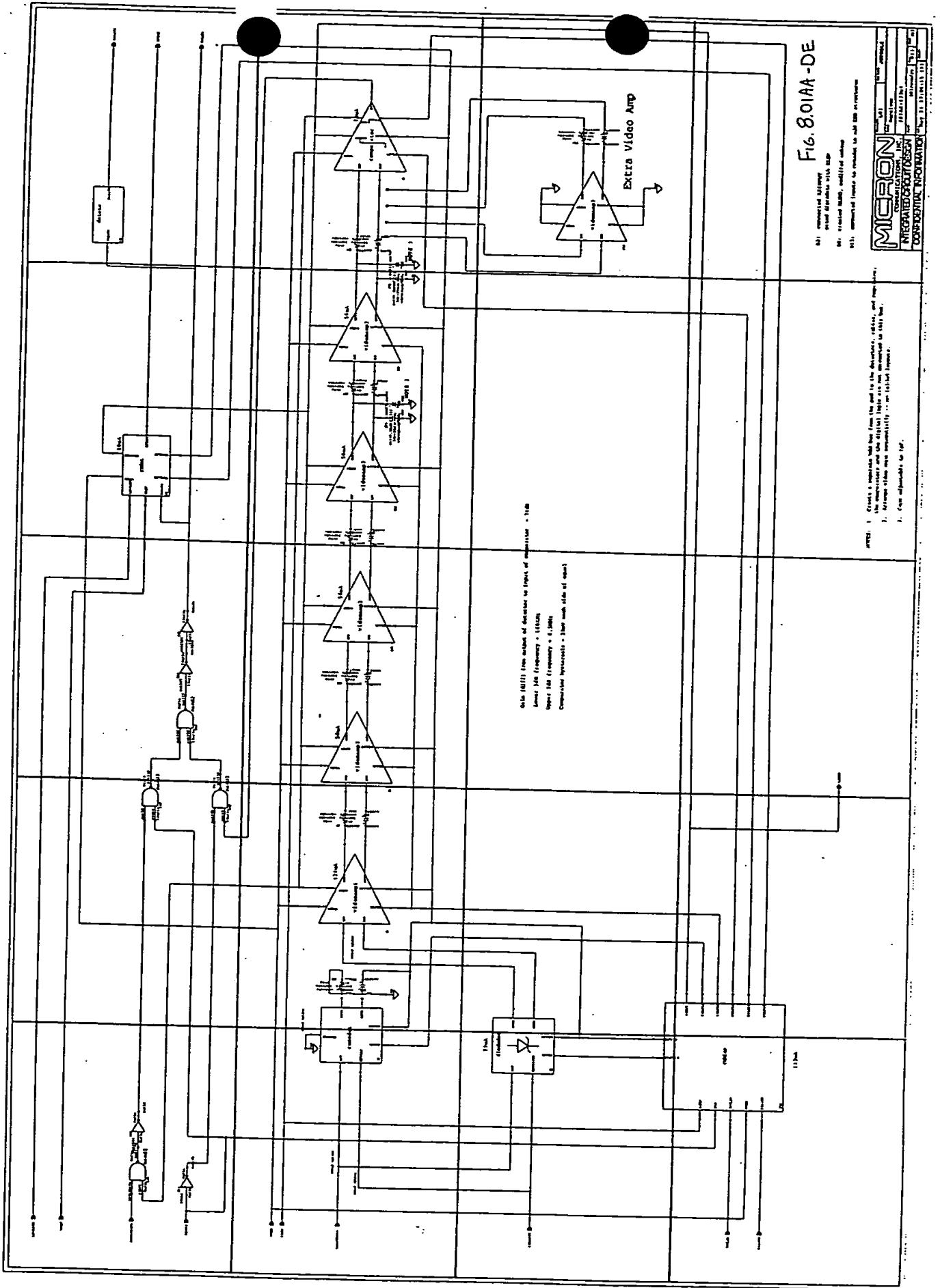


Fig. 8.01AA-DE

801AA-DE
Output delay time
Setup time
Hold time

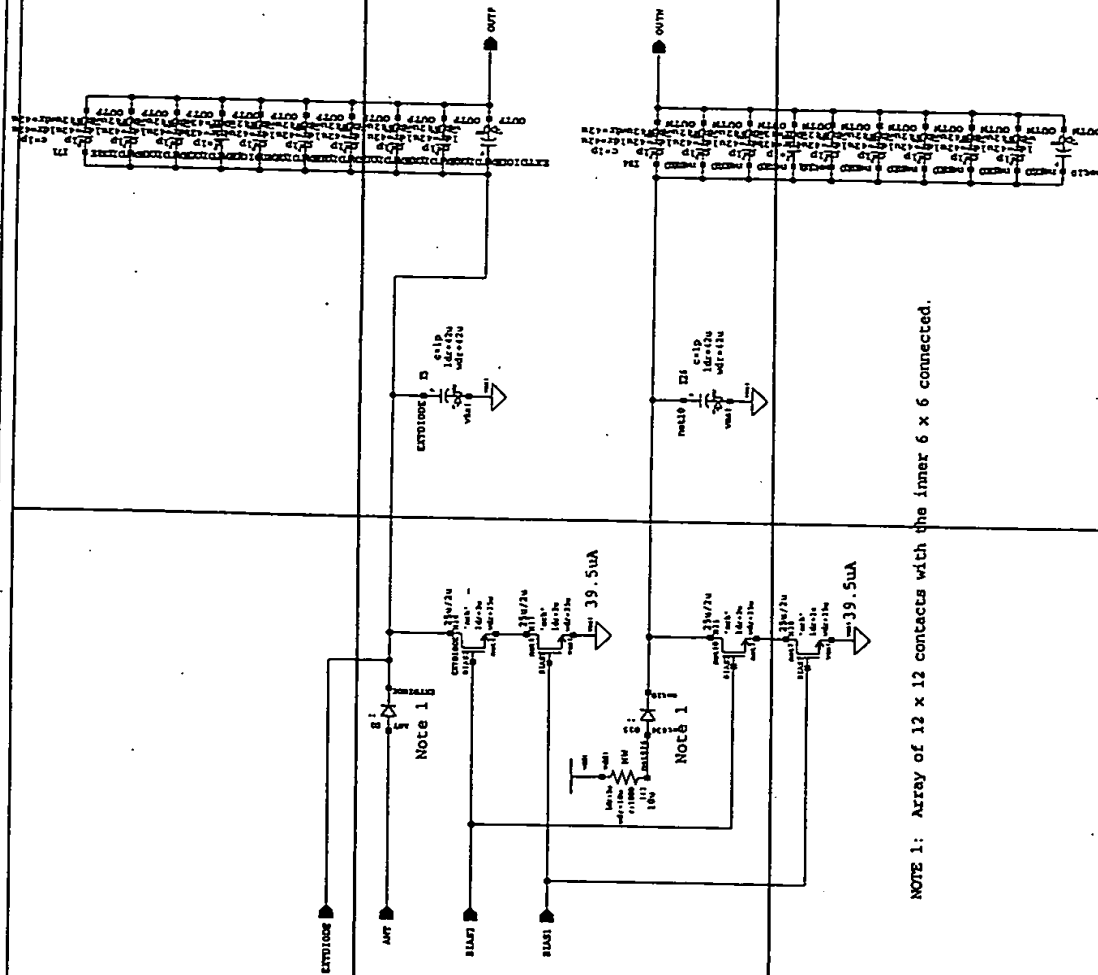
MICRON
TECHNOLOGIES, INC.
INTEGRATED CIRCUITS
CONFIDENTIAL INFORMATION

NOTES: 1. Output delay time from the input to the driver, video, and data lines.
2. Setup time and hold time are measured at 50% of the signal level.
3. Load capacitance is 10pF.

8.0101AA	8.0101AB
8.0101BA	8.0101BB
8.0101CA	8.0101CB

IL II BB. III III

DATA-630300



NOTE 1: Array of 12 x 12 contacts with the inner 6 x 6 connected.

- B2: connected EXTDIODE line
- B6: schottky array changed to 6x6
rf cap reduced to 1pF
- B8: increased Cc to 10pF; decreased Crf to 1pF
- B13: added 1K resistor in series with dummy diode for ESD

Fig. 8.0101AA-CB

MICRON		PROPERTY: L03	REVISION: J07000LE
COMMUNICATIONS, INC.		TYPE: Schottky Diode Detector	
INTEGRATED CIRCUIT DESIGN		IBIAS=79uA	
CONFIDENTIAL INFORMATION		103revs/diodedet	
		B13	
		May 24 13:54:28 1996	

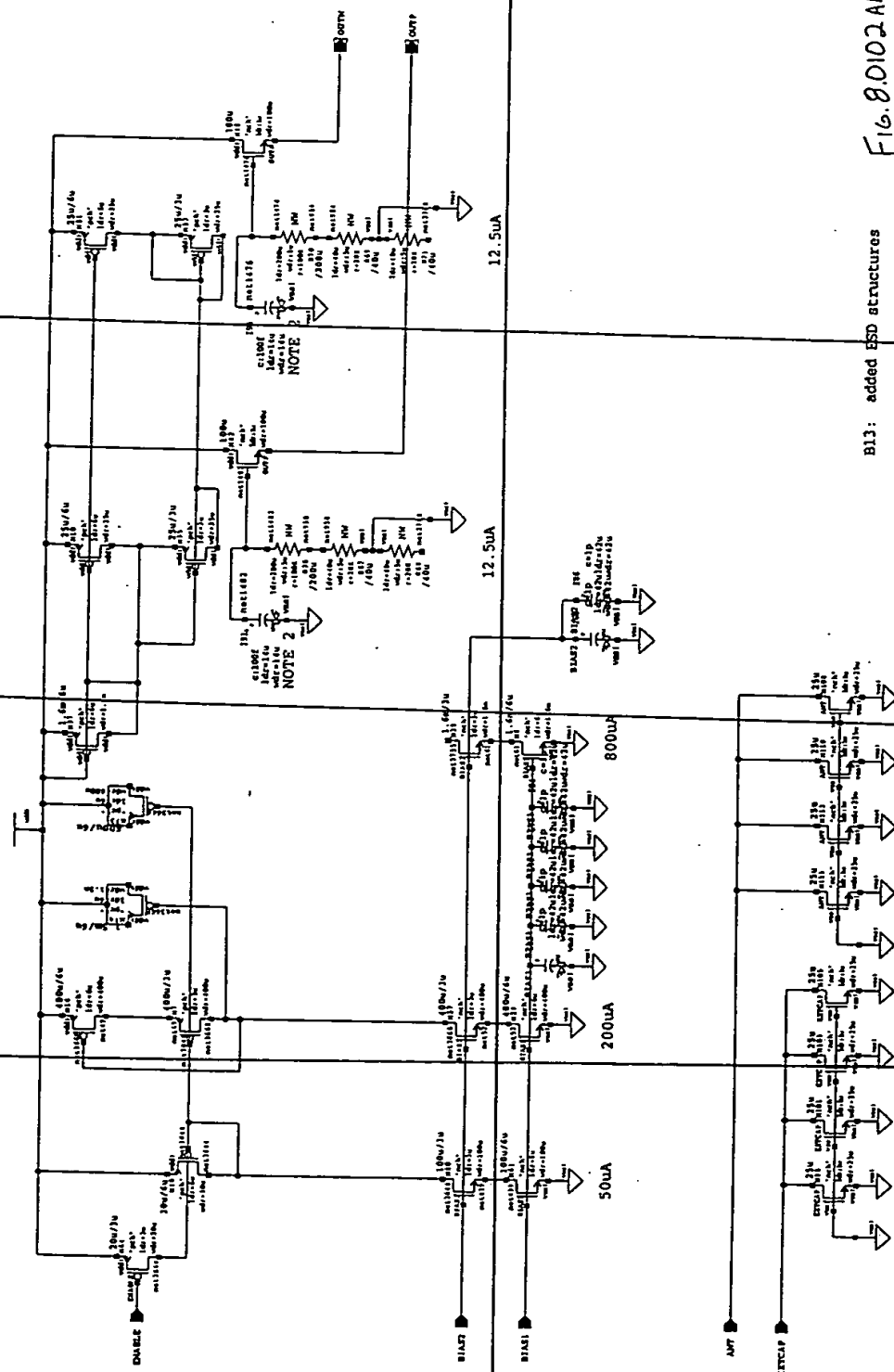
8.0102AA	8.0102AB	8.0102AC	8.0102AD
8.0102BA	8.0102BB	8.0102BC	

11 11 11 11 11

FIG. 8.0102 AA-BC

B13: added ESD structures

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
PROJECT: L03	REVISION: J0700LE
THE CHOS Square Law Detector	
IBIAS=1.175mA	
DATE: 103 reva/cmosdot	BY: B13
May 25 14:10:58 1996	



- NOTES:
1. All devices with $W's > 100$ are to be laid out with 100u segments.
 2. Cap adjustable to 300fF (23/23)

8.0103AA	8.0103AB	8.0103AC	8.0103AD	8.0103AE	8.0103AF
8.0103BA	8.0103BB	8.0103BC	8.0103BD	8.0103BE	8.0103BF
8.0103CA	8.0103CB	8.0103CC	8.0103CD	8.0103CE	8.0103CF

11 11 11 11 11 11

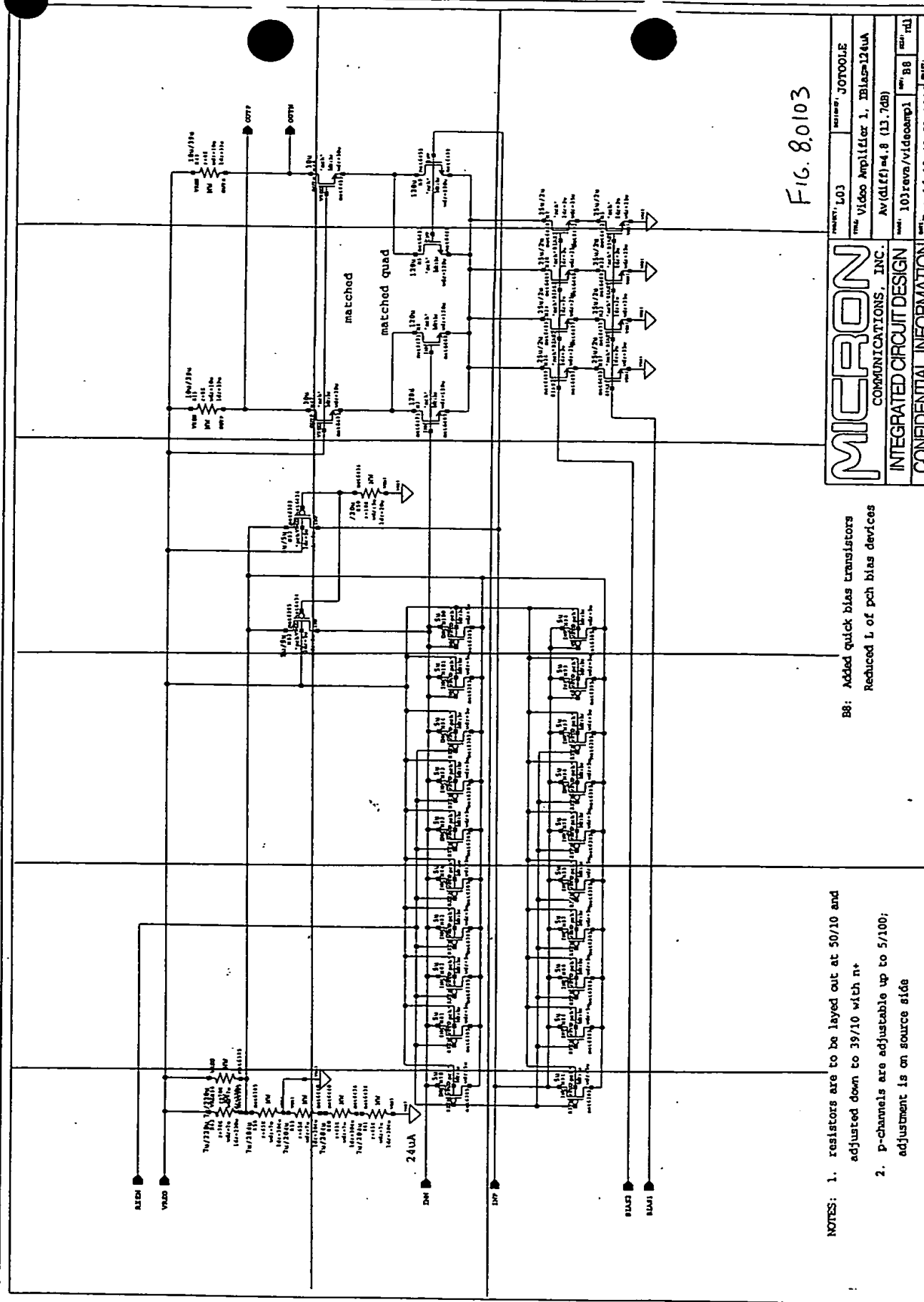


Fig. 8.0103

PART: L03		REV: J0700LE
Title: Video Amplifier 1, Bias=12uA		
Av(dB) = 4.8 (13.7dB)		
Rev: 103revs/videoamp1		Rev: 88
Date: Dec 16 13:40:53 1995		Rev: nll

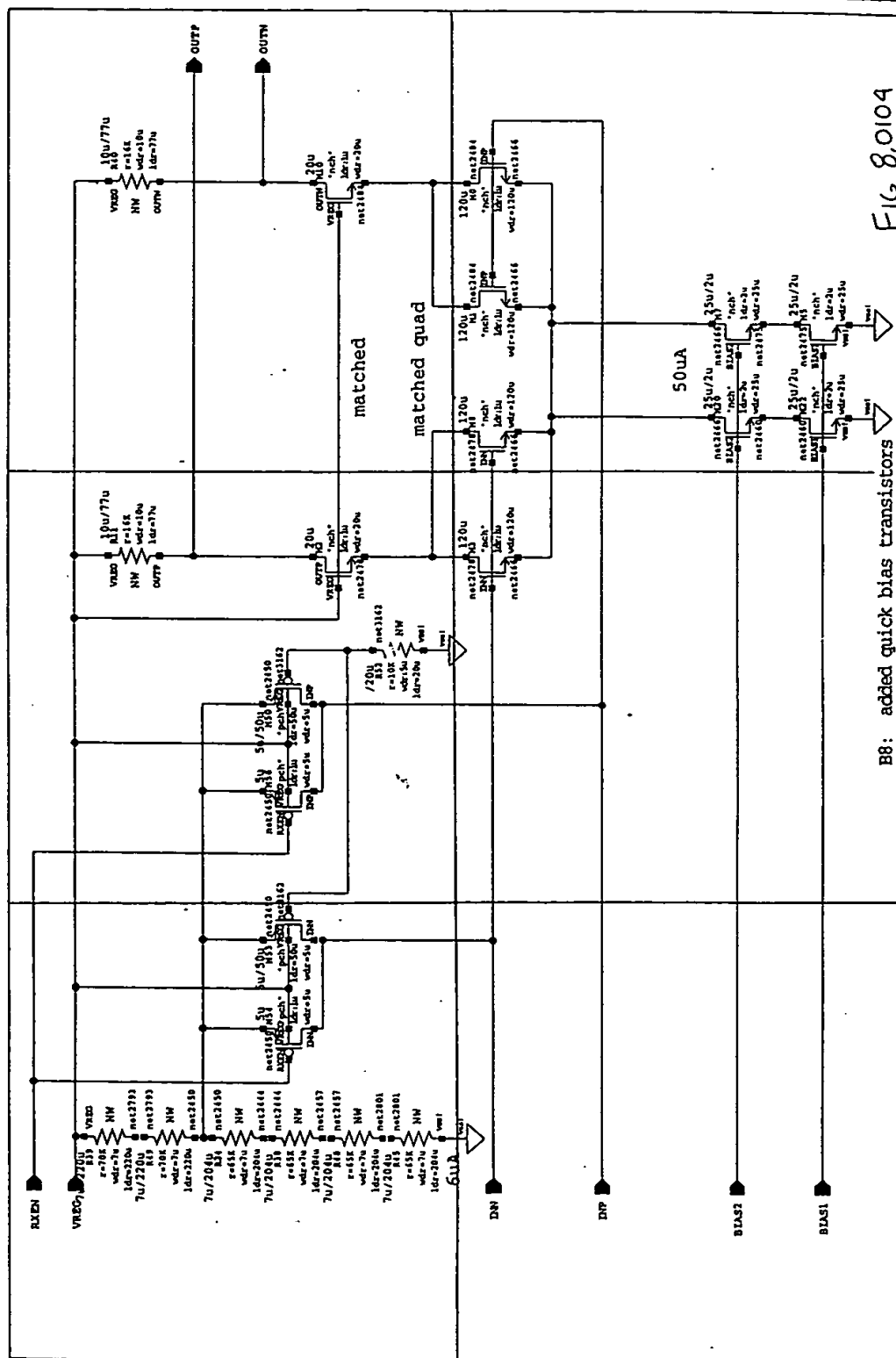
MICRON
 COMMUNICATIONS, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

88: Added quick bias transistors
 Reduced L of pch bias devices

NOTES: 1. resistors are to be layed out at 50/10 and
 adjusted down to 39/10 with n+
 2. p-channels are adjustable up to 5/100;
 adjustment is on source side

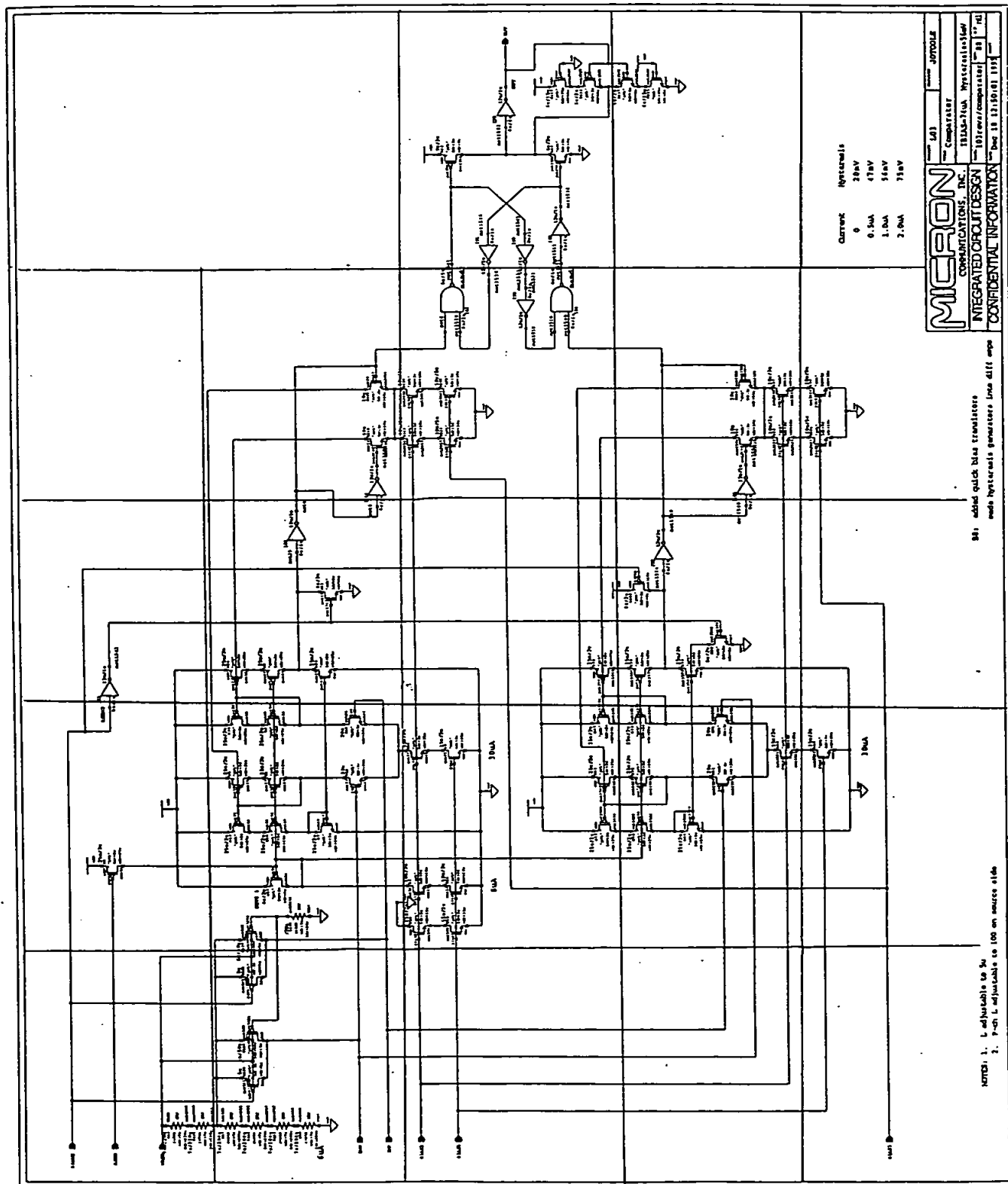
8.0104AA	8.0104AB	8.0104AC
8.0104BA	8.0104BB	8.0104BC

EX-1111



И. П. Е. 88.001.005

Fig. 8.0105



NOTES: 1. L adjustable to Su
2. P-on L adjustable to

020319018: added quick bias translators
made hysteresis generators into diff eqns

Current	Hysteresis
0	20mV
0.5uA	47mV
1.0uA	56mV
2.0uA	75mV

MICRON
COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN

NAME	LAST	FIRST	MIDDLE
COMPANY			
ADDRESS			
CITY			
STATE			
ZIP			
TELEPHONE			
FAX			
E-MAIL			
DATE			

CONFIDENTIAL INFORMATION

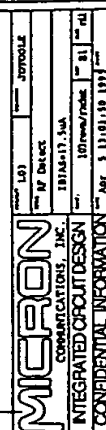
00000000000000000000

MI40-030

8.0106AA	8.0106AB	8.0106AC	8.0106AD
8.0106BA	8.0106BB	8.0106BC	8.0106BD
8.0106CA	8.0106CB	8.0106CC	8.0106CD

IL 07 8.0106

FIG. 8.0106



8.0107AA	8.0107AB	8.0107AC	8.0107AD	8.0107AE	8.0107AF	8.0107AG	8.0107AH	8.0107AI	8.0107AJ	8.0107AK	8.0107AL	8.0107AM	
8.0107BA	8.0107BB	8.0107BC	8.0107BD	8.0107BE	8.0107BF	8.0107BG	8.0107BH	8.0107BI	8.0107BJ	8.0701BK	8.0107BL	8.0107BM	8.0107BN
8.0107CA	8.0107CB	8.0107CC	8.0107CD	8.0107CE	8.0107CF	8.0107CG	8.0107CH	8.0107CI	8.0107CJ	8.0107CK	8.0107CL	8.0107CM	8.0107CN
8.0107DA	8.0107DB	8.0107DC	8.0107DD	8.0107DE	8.0107DF	8.0107DG	8.0107DH	8.0107DI	8.0107DJ	8.0107DK	8.0107DL	8.0107DM	8.0107DN
8.0107EA	8.0107EB	8.0107EC	8.0107ED	8.0107EE	8.0107EF	8.0107EG	8.0107EH	8.0107EI	8.0107EJ	8.0107EK	8.0107EL	8.0107EM	8.0107EN
8.0107FA	8.0107FB	8.0107FC	8.0107FD	8.0107FE	8.0107FF	8.0107FG	8.0107FH	8.0107FI	8.0107FJ	8.0107FK	8.0107FL	8.0107FM	8.0107FN
8.0107GA	8.0107GB	8.0107GC	8.0107GD	8.0107GE	8.0107GF	8.0107GG	8.0107GH	8.0107GI	8.0107GJ	8.0107GK	8.0107GL	8.0107GM	8.0107GN

И. И. Б. 8.01.07

[illegible]

001: Plaintiff seeks injunctive relief to prevent the defendant from disclosing the confidential information of the defendant's business.



001123 00000000

8.0108AA	8.0108AB	8.0108AC
----------	----------	----------

JE II 00 00.00.00

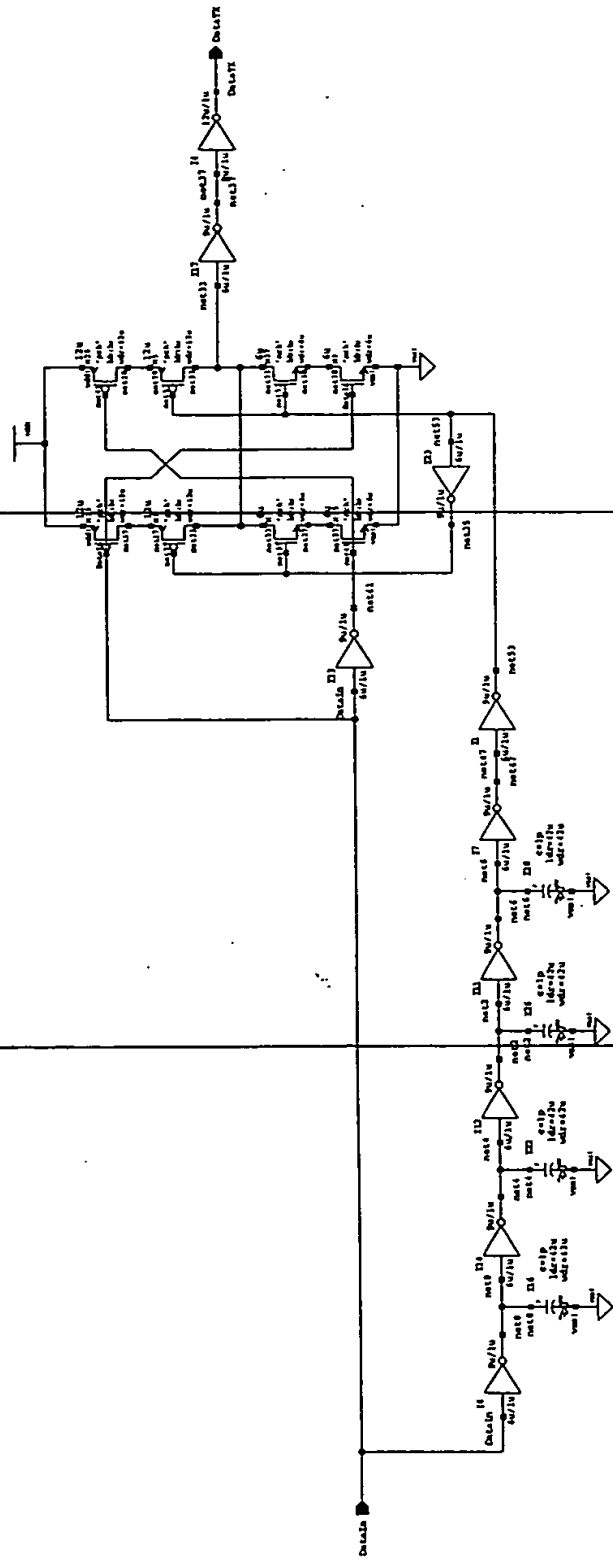


Fig. 8.0108

MICRON		IDENT#	L03	FUNCTION#	J0700LE
COMMUNICATIONS, INC.		Data Transition Detector			
INTEGRATED CIRCUIT DESIGN		Output Pulse Width = 40ns (nom)			
CONFIDENTIAL INFORMATION		DATE	10/19/94	REV	B1
		Dec 14 17:11:15 1994			

8.02AA	8.02AB	8.02AC
8.02BA	8.02BB	8.02BC

208.0000

NOIR

B10: first divider stage bypassed

MICROTECH, INC.		PROJECT	DATE
COMMUNICATIONS, INC.		DESIGN	REV
INTEGRATED CIRCUIT DESIGN		TEST	DATE
CONFIDENTIAL INFORMATION		REVISION	REVISION

00000000000000000000

8.0201AA	8.0201AB
----------	----------

11 11 11 11 11 11 11 11 11 11

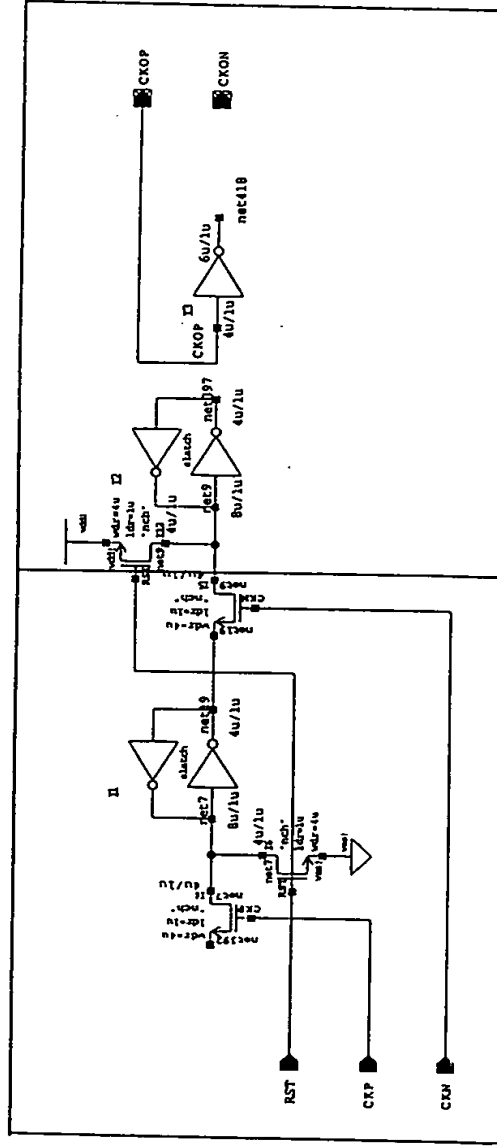


Fig. 8.0201

MICRON COMMUNICATIONS, INC.				PROJECT: L03	DESIGNER: JOTOOLE
INTEGRATED CIRCUIT DESIGN				TITLE: Timed Lockout Divider Cell	
CONFIDENTIAL INFORMATION				NAME: t03reva/tldcel_bypass	REV: B10
				DATE: Mar 26 13:54:47 1996	SIZE: A

B10: new cell to bypass 1st counter stage

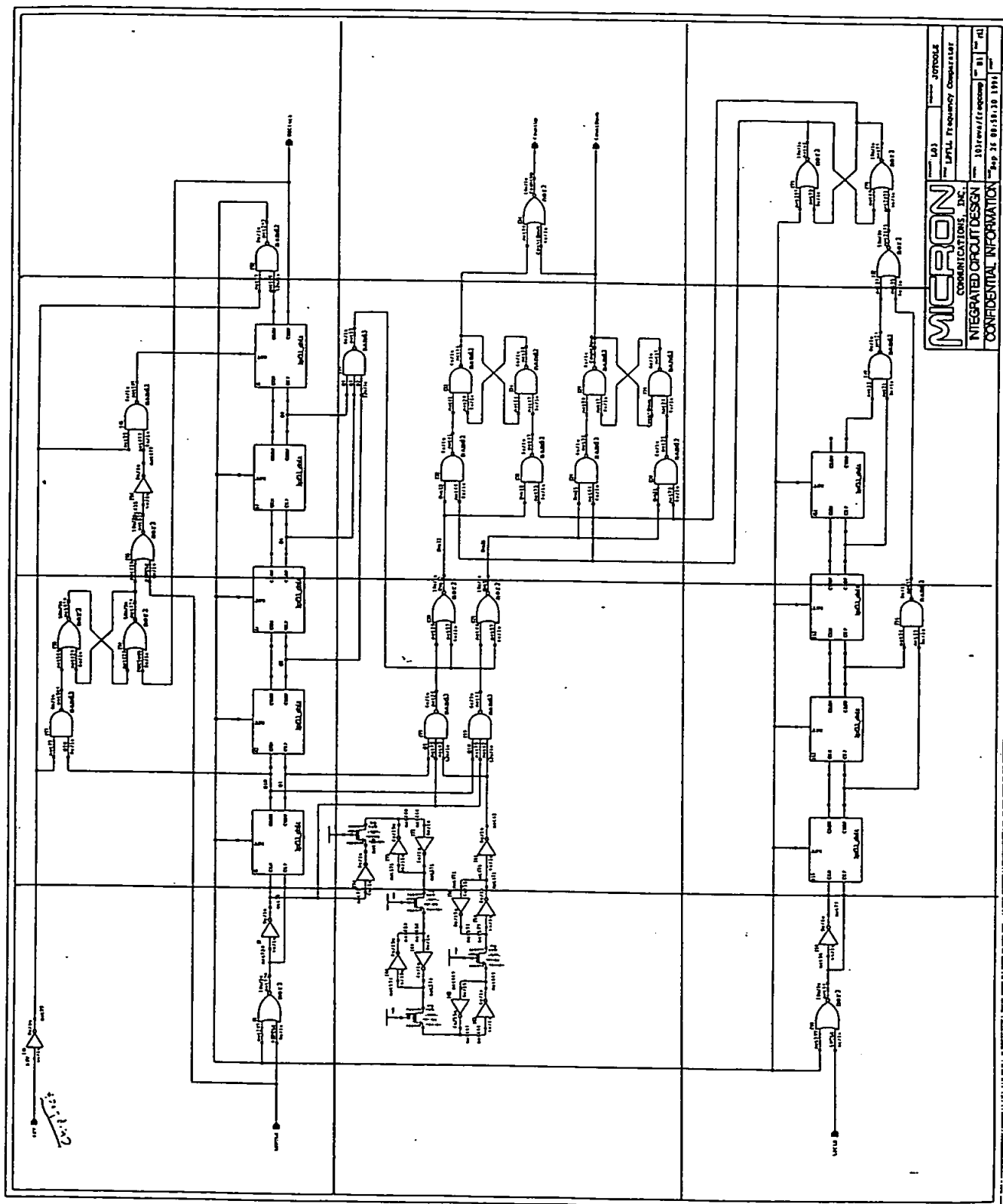
001420" 20920500

MI40-030

8.0202AA	8.0202AB	8.0202AC	8.0202AD
8.0202BA	8.0202BB	8.0202BC	8.0202BD
8.0202CA	8.0202CB	8.0202CC	8.0202CD

II II 8.0202

Fig. 8.0202



MICRON
COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN

INTEGRATED CIRCUIT DESIGN

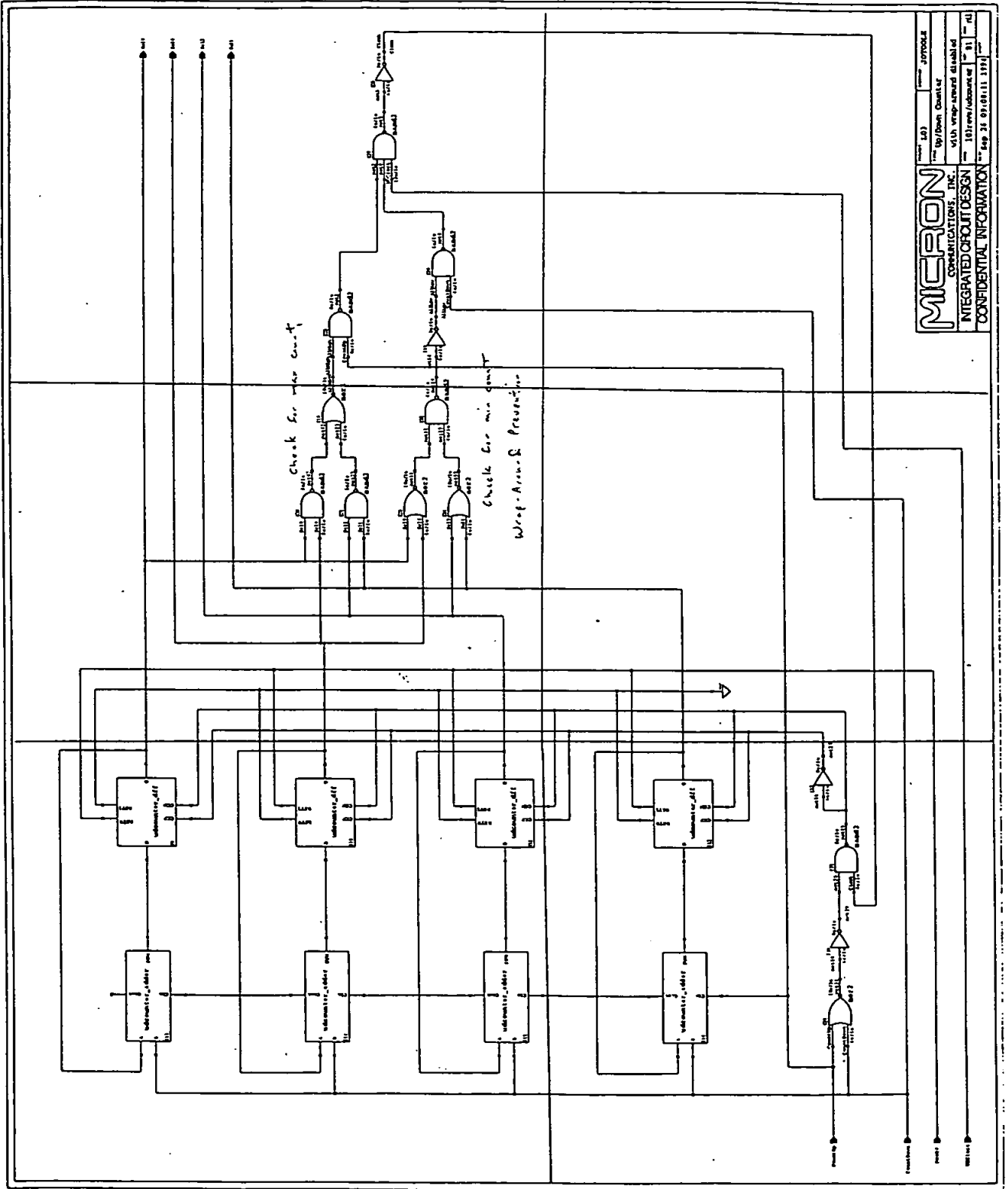
CONFIDENTIAL INFORMATION

007420 20920500

MI40-030

8.0203AA	8.0203AB	8.0203AC
8.0203BA	8.0203BB	8.0203BC

11.11.11 88.11.11.11



MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
DATE: 08/11/1971	DESIGNER: J. J. J.
PROJECT: 1010000000000000	REVISION: 01
SHEET 21 OF 21	

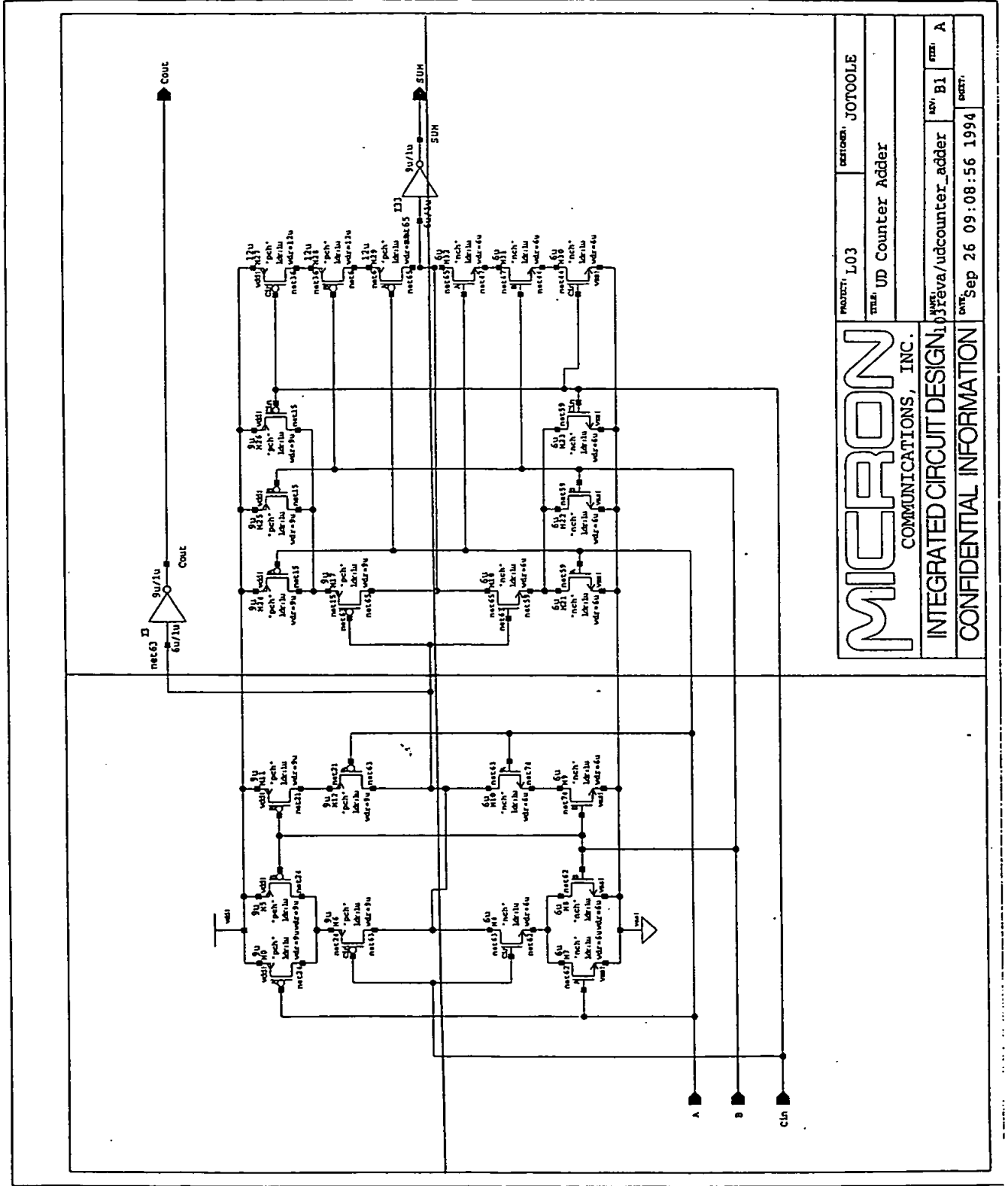
FIG. 8.0203

007720 20920900

MI40-030

8.020301AA	8.020301AB
8.020301BA	8.020301BB

II II 8.020301



MICRON
COMMUNICATIONS, INC.

PROJECT: L03

UD Counter Adder

DATE: 26 09:08:56 1994

DESIGNER: JOTOOLE

REV: B1

DATE: A

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

001123 26920500

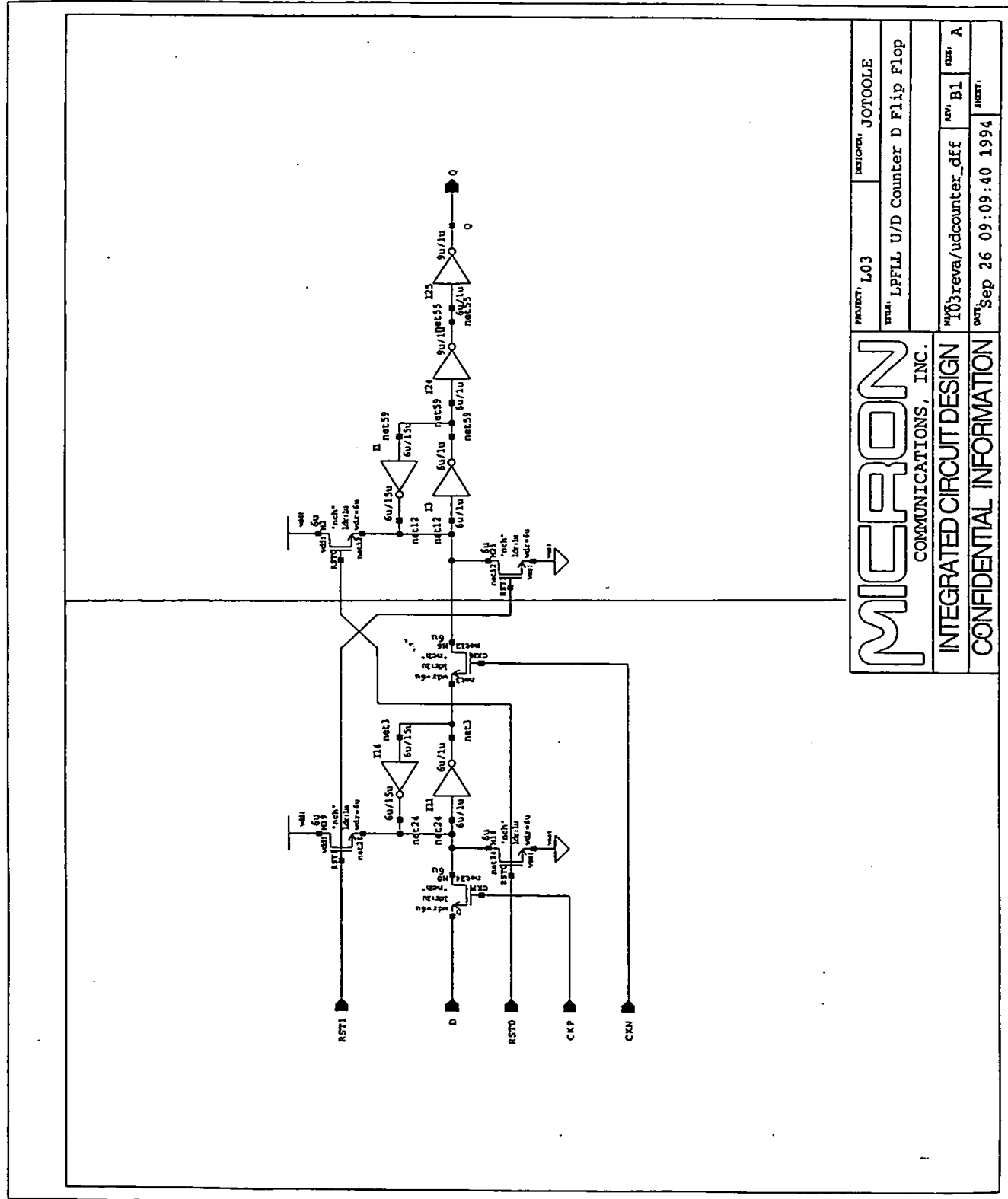
MI40-030

8.020302AB

8.020302AA

LE 88.020302

SECRET



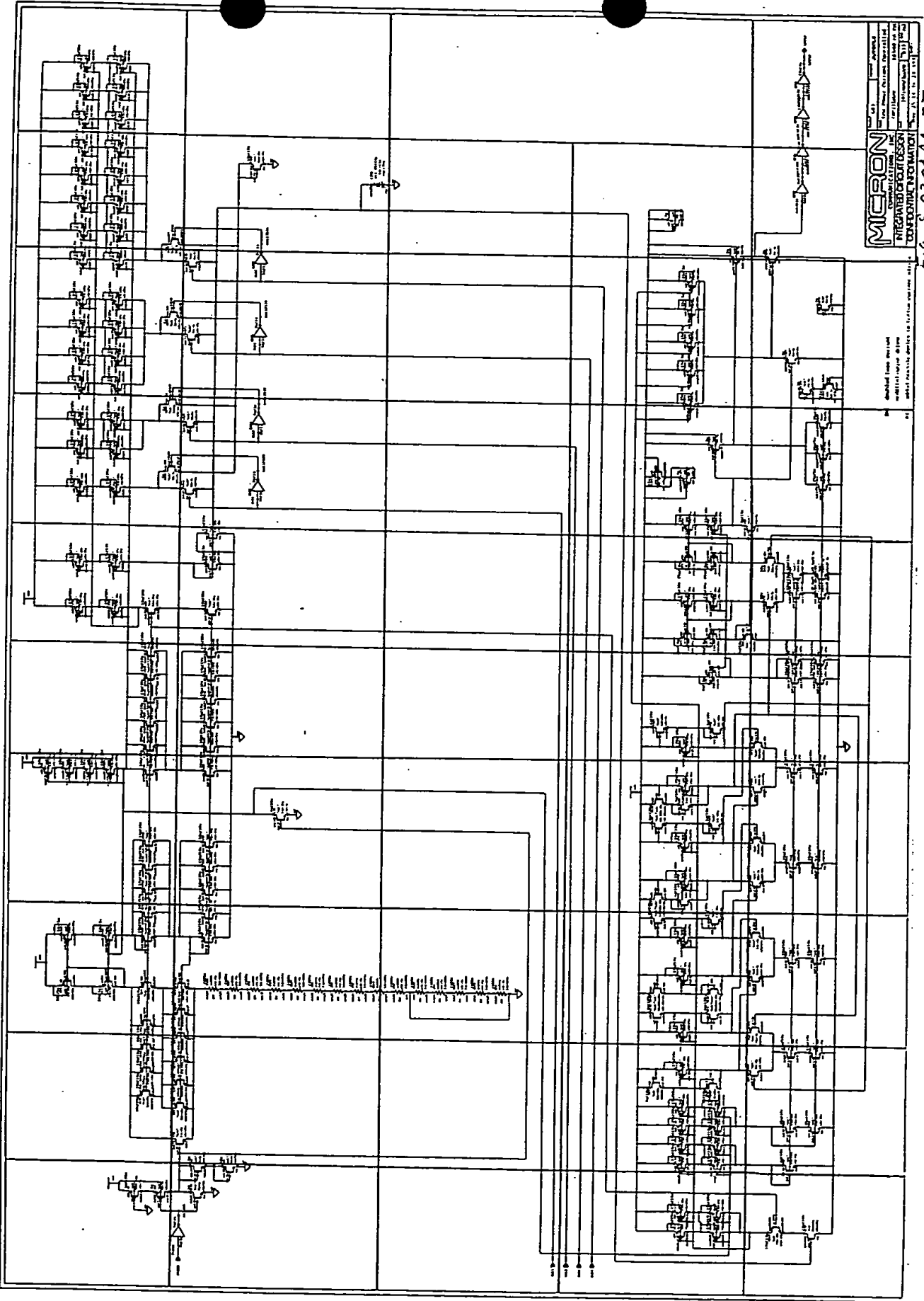
MICRON		COMMUNICATIONS, INC.	
PROJECT: L03		DESIGNER: JOTOOLE	
TITLE: LPFLL U/D Counter D Flip Flop		REV: B1	
DATE: 10/3/94		REV: A	
CONFIDENTIAL INFORMATION		DATE: Sep 26 09:09:40 1994	

Fig. 8.020302

8.0204AA	8.0204AB	8.0204AC	8.0204AD	8.0204AE	8.0204AF	8.0204AG	8.0204AH	8.0204AI	8.0204AJ
8.0204BA	8.0204BB	8.0204BC	8.0204BD	8.0204BE	8.0204BF	8.0204BG	8.0204BH	8.0204BI	8.0204BJ
8.0204CA	8.0204CB	8.0204CC	8.0204CD	8.0204CE	8.0204CF	8.0204CG	8.0204CH	8.0204CI	
8.0204DA	8.0204DB	8.0204DC	8.0204DD	8.0204DE	8.0204DF	8.0204DG	8.0204DH	8.0204DI	
8.0204EA	8.0204EB	8.0204EC	8.0204ED	8.0204EE	8.0204EF	8.0204EG	8.0204EH	8.0204EI	8.0204EJ

11 11 11 11 11 11 11 11 11 11

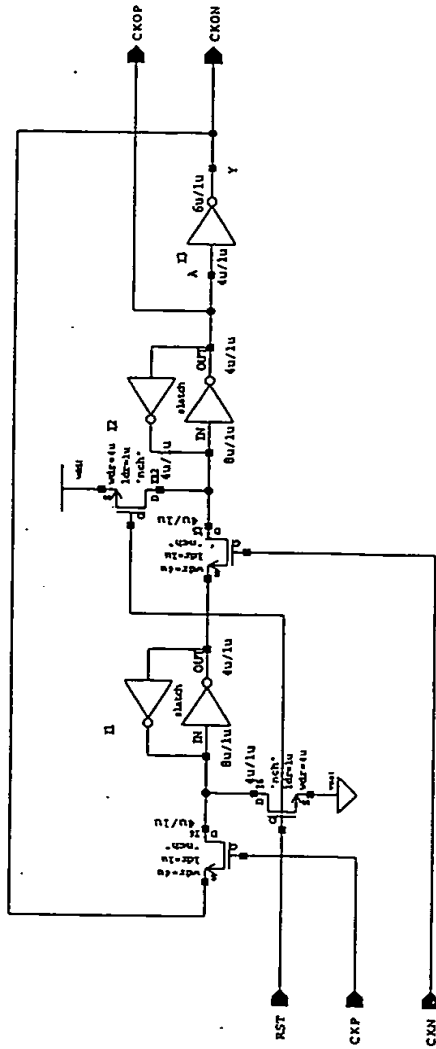
00410 2090500



MICRON
INTEGRATED CIRCUIT DESIGN
CORPORATION
10000 NE 28TH AVE
BEVERLY HILLS, CA 90212
TEL: (310) 207-1000
FAX: (310) 207-1001

Fig. 8.020+AA-EJ

001120 00000000



12/29/92

MICRON
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

PROJECT: L03	DESIGNER: Rotzoll
TITLE: Timed Lockout Divider Cell	
NAME: 103reva/ldcel	REV: A
DATE: Sep 22 15:26:56 1994	HEET: 1

~~116~~
F1680205

001420 00000000

8.03AB	8.03AA
--------	--------

LEWIS & CLARK

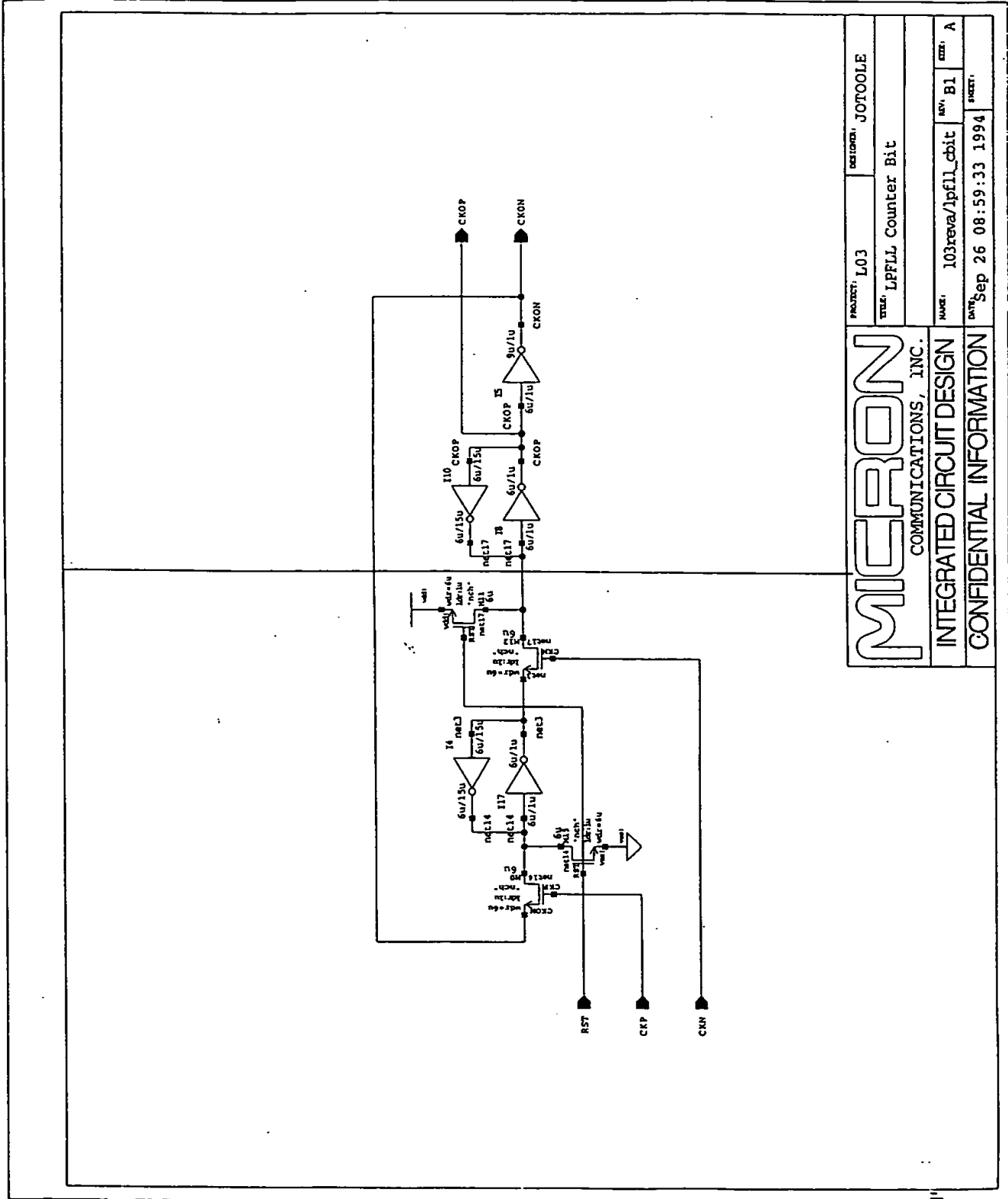


Fig. 8.03

8.04AA	8.04AB	8.04AC	8.04AD	8.04AE	8.04AF
8.04BA	8.04BB	8.04BC	8.04BD	8.04BE	8.04BF
8.04CA	8.04CB	8.04CC	8.04CD	8.04CE	8.04CF
8.04DA	8.04DB	8.04DC	8.04DD	8.04DE	
8.04EA	8.04EB	8.04EC	8.04ED	8.04EE	

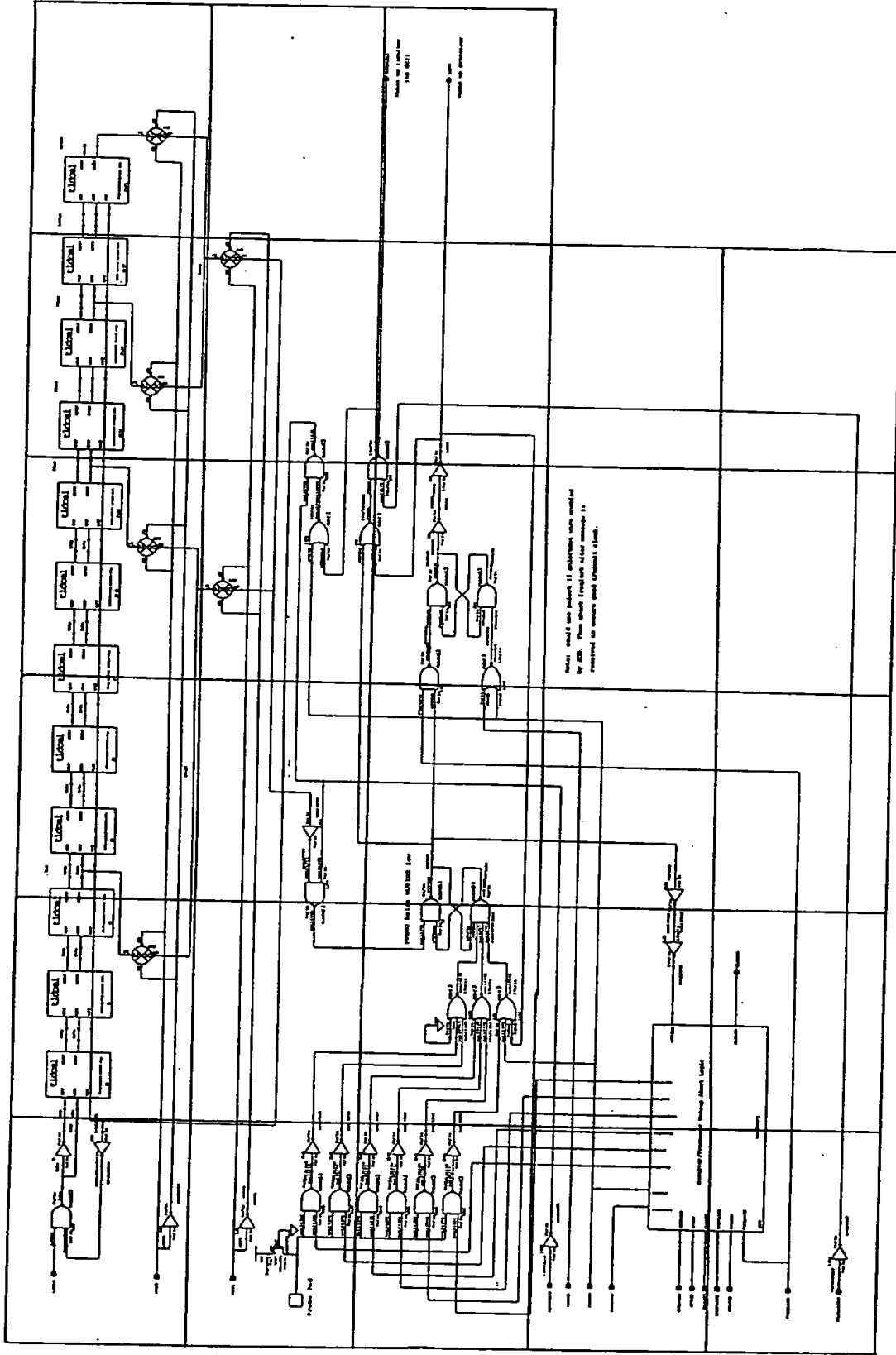
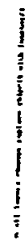


Fig. 804

8.0401AA	8.0401AB	8.0401AC	8.0401AD	8.0401AE
8.0401BA	8.0401BB	8.0401BC	8.0401BD	8.0401BE
8.0401CA	8.0401CB	8.0401CC	8.0401CD	8.0401CE
8.0401DA	8.0401DB	8.0401DC	8.0401DD	8.0401DE

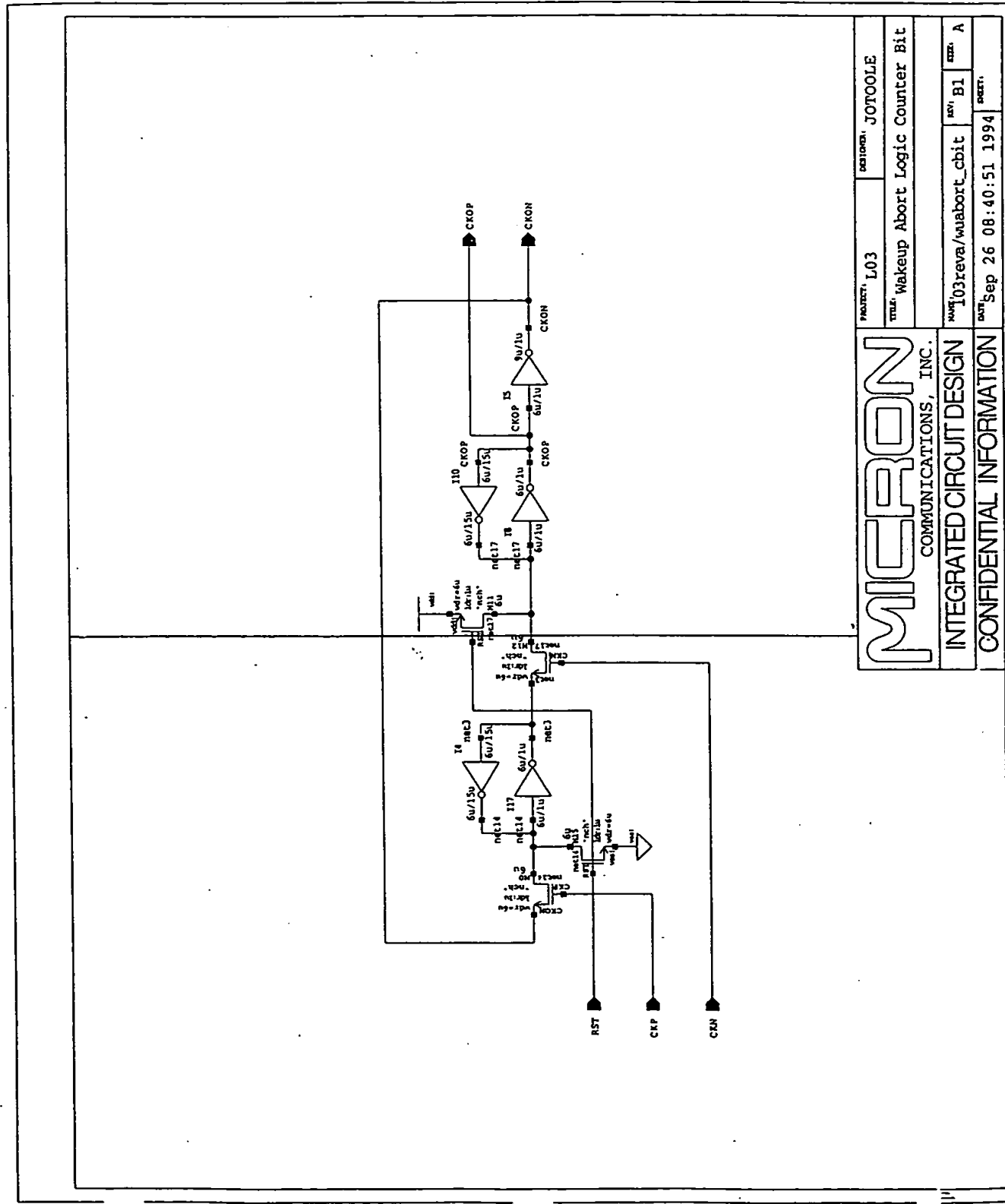
II II II BB.004000 II

.....



8.040101AB

II II II 88.07.07.07.07



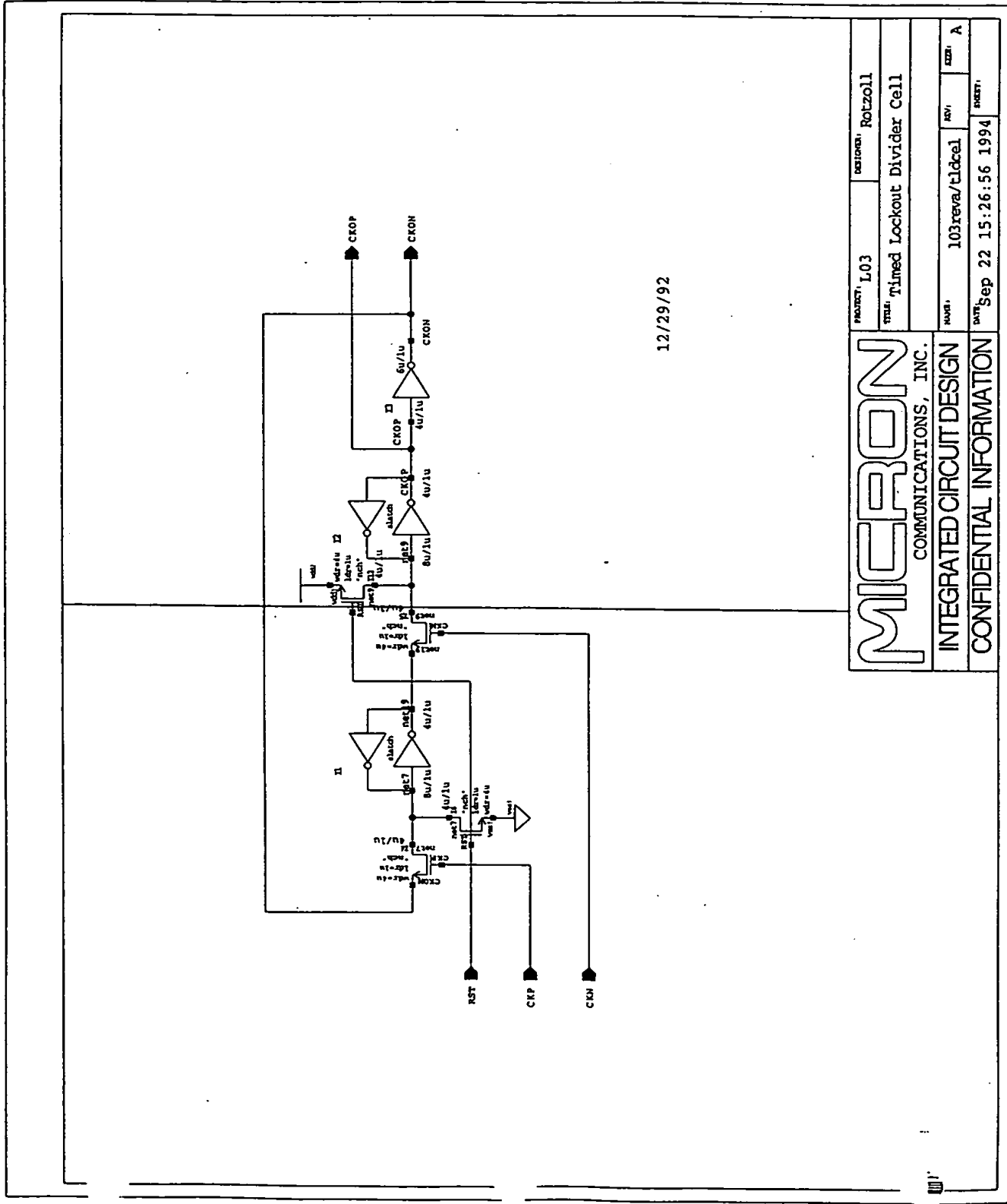
MICRON		PART NO. L03	DESIGNER JOTOOLE
COMMUNICATIONS, INC.		TITLE Wakeup Abort Logic Counter Bit	
INTEGRATED CIRCUIT DESIGN		REV. B1	REV. A
CONFIDENTIAL INFORMATION		DATE Sep 26 08:40:51 1994	

FIG. 8.090101

00770 000000

8.0402AA	8.0402AB
----------	----------

11 11 11 88.04.02

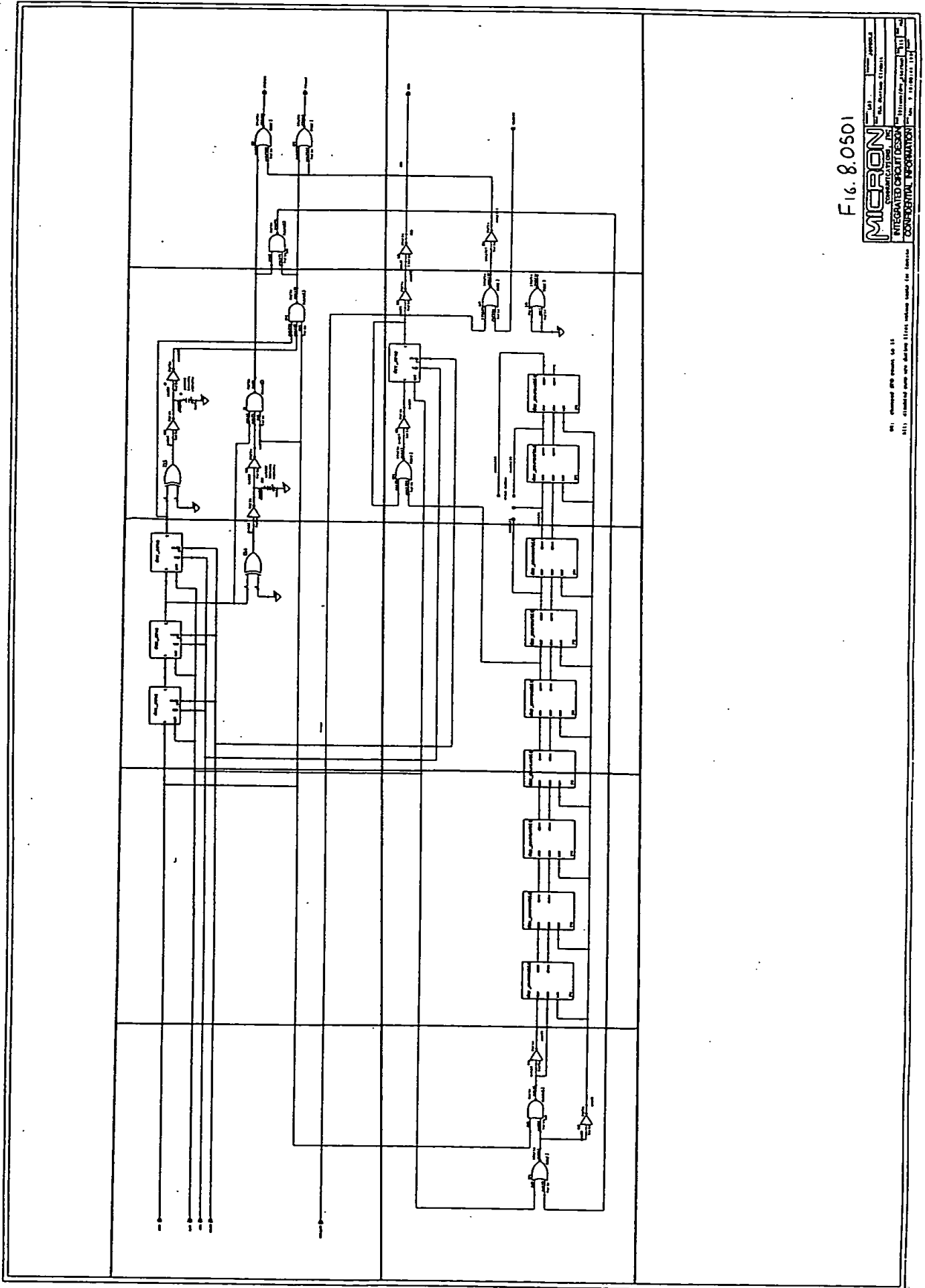


MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: Titled Lockout Divider Cell	
INTEGRATED CIRCUIT DESIGN		NOV: 103revA/tldcel	REV: A
CONFIDENTIAL INFORMATION		DATE: Sep 22 15:26:56 1994	

FIG. 8.0402

8.05AA	8.05AB	8.05AC	8.05AD	8.05AE
8.05BA	8.05BB	8.05BC	8.05BD	8.05BE
8.05CA	8.05CB	8.05CC	8.05CD	8.05CE
8.05DA	8.05DB	8.05DC	8.05DD	8.05DE

004720 20920300

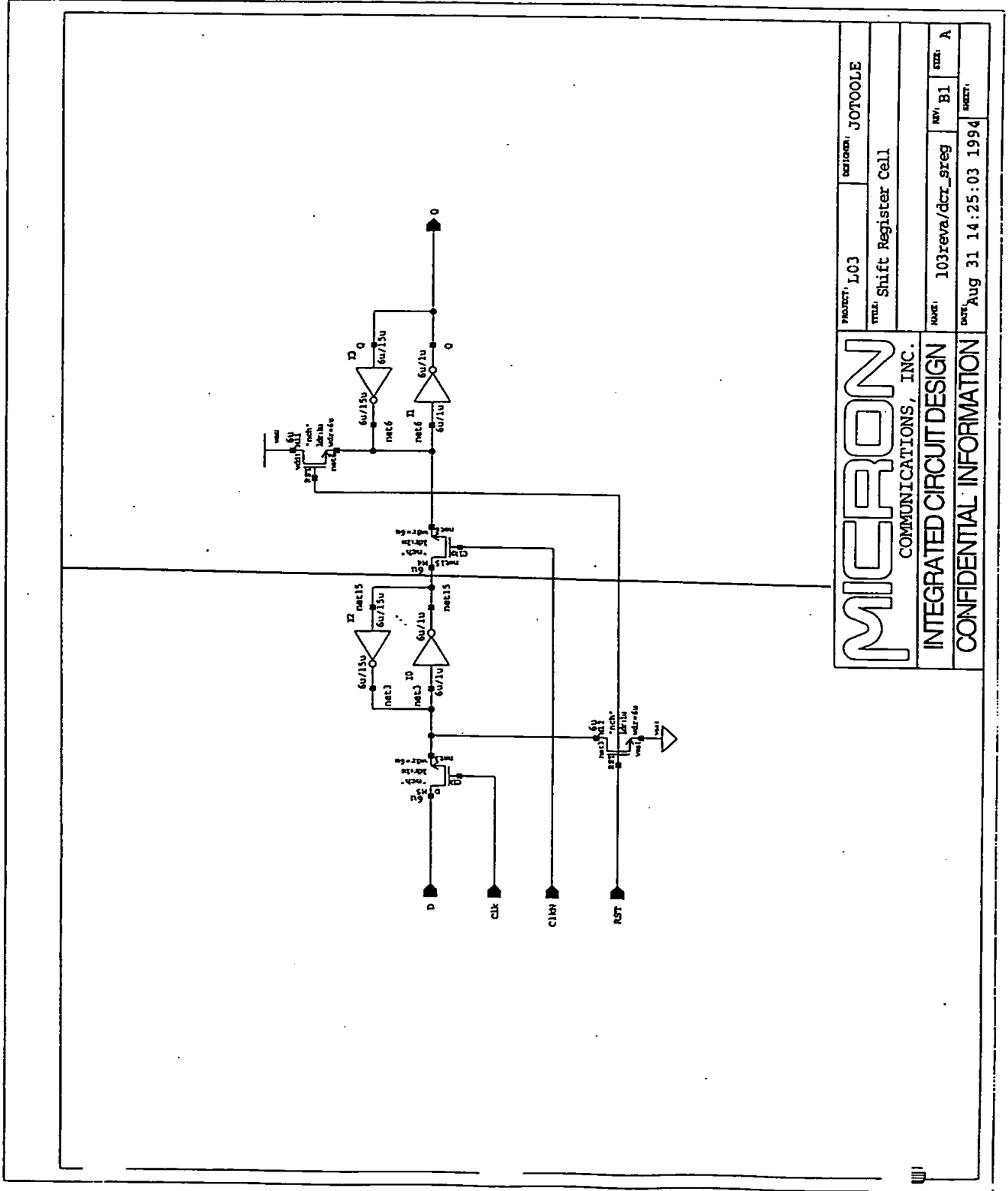


00720: 20920900

8.050101AB

8.050101AA

IF 07 88.05010100



MICRON		PROJECT: L03	REVISION: J0000LE
COMMUNICATIONS, INC.		TITLE: Shift Register Cell	
INTEGRATED CIRCUIT DESIGN		NAME: 103revs/dcr_sreg	REV: B1
CONFIDENTIAL INFORMATION		DATE: Aug 31 14:25:03 1994	REV: A

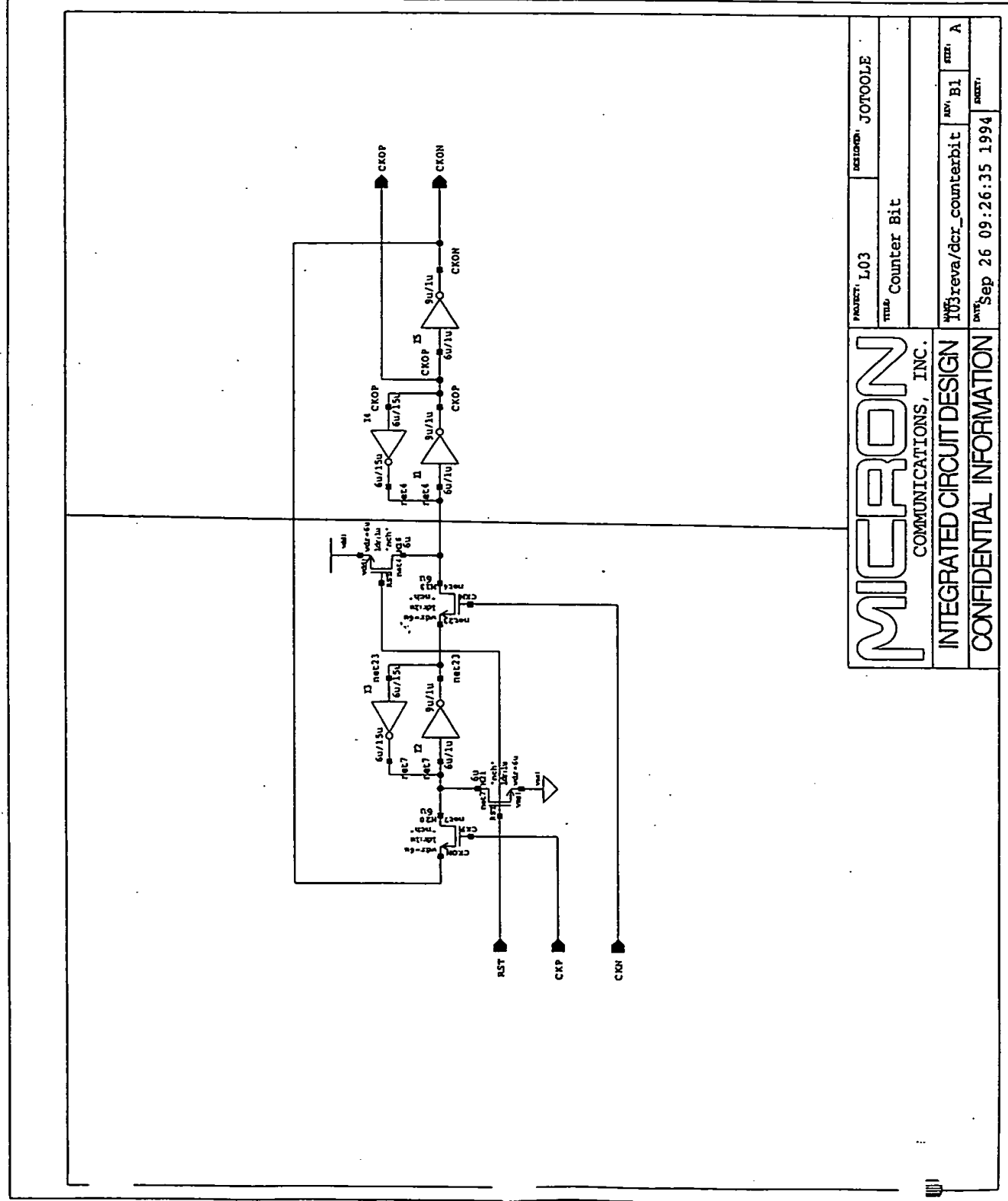
Fig. 8.050101

001120 20920540

8.050102AB

8.050102AA

11 05 01 02



MICRON COMMUNICATIONS, INC.		PROJECT: L03	DESIGNER: JOTOOLE
INTEGRATED CIRCUIT DESIGN		Title: Counter Bit	
CONFIDENTIAL INFORMATION		rev: reva/dcr_counterbit	REV: B1
		DATE: Sep 26 09:26:35 1994	REV: A

Fig. 8.050102

8.0502AA	8.0502AB	8.0502AC	8.0502AD
8.0502BA	8.0502BB	8.0502BC	8.0502BD
8.0502CA	8.0502CB	8.0502CC	8.0502CD

IF II 07 88.0502

004420 2632330

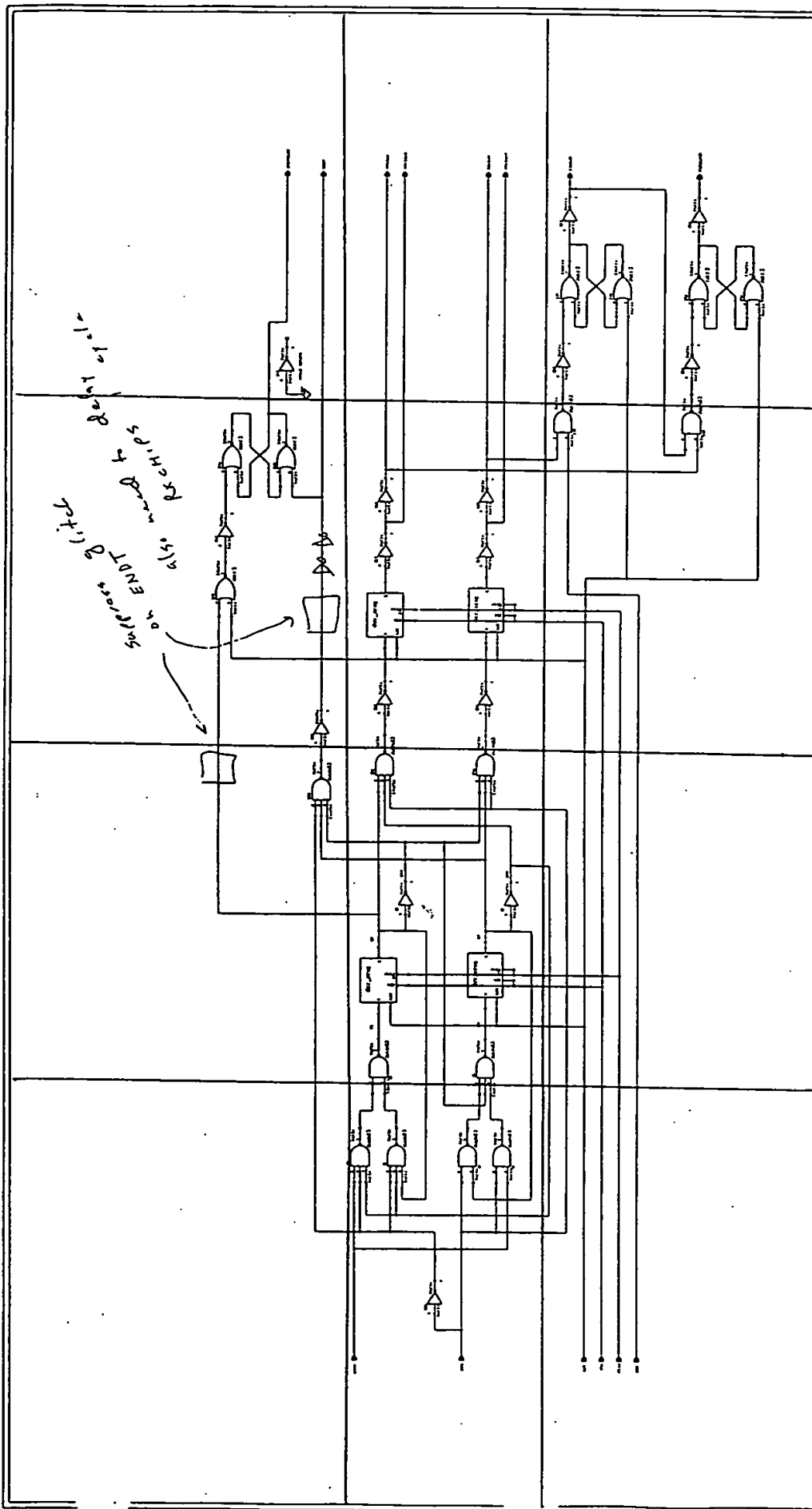


Fig 8.0502

8.05034A	8.05034B	8.05034C	8.05034D	8.05034E	8.05034F	8.05034G	8.05034H	8.05034I	8.05034J	8.05034K	8.05034L	8.05034M	8.05034N	8.05034O
8.0503BA	8.0503BB	8.0503BC	8.0503BD	8.0503BE	8.0503BF	8.0503BG	8.0503BH	8.0503BI	8.0503BJ	8.0503BK	8.0503BL	8.0503BM	8.0503BN	8.0503BO
8.0503CA	8.0503CB	8.0503CC	8.0503CD	8.0503CE	8.0503CF	8.0503CG	8.0503CH	8.0503CI	8.0503CJ	8.0503CK	8.0503CL	8.0503CM	8.0503CN	8.0503CO
8.0503DA	8.0503DB	8.0503DC	8.0503DD	8.0503DE	8.0503DF	8.0503DG	8.0503DH	8.0503DI	8.0503DJ	8.0503DK	8.0503DL	8.0503DM	8.0503DN	8.0503DO
8.0503EA	8.0503EB	8.0503EC	8.0503ED	8.0503EE	8.0503EF	8.0503EG	8.0503EH	8.0503EI	8.0503EJ	8.0503EK	8.0503EL	8.0503EM	8.0503EN	
8.0503FA	8.0503FB	8.0503FC	8.0503FD	8.0503FE	8.0503FF	8.0503FG	8.0503FH	8.0503FI	8.0503FJ	8.0503FK	8.0503FL	8.0503FM	8.0503FN	

001120 26523566

MI40-030

8.0504AA	8.0504AB	8.0504AC	8.0504AD	
8.0504BA	8.0504BB	8.0504BC	8.0504BD	
8.0506CA	8.0504CB	8.0504CC	8.0504CD	8.0504CE
8.0504DA	8.0504DB	8.0504DC	8.0504DD	8.0504DE
8.0504EA	8.0504EB	8.0504EC	8.0504ED	8.0504EE

II II II 88.0504

MI40-030

8.050401AA	8.050401AB	8.050401AC	8.050401AD	8.050401AE	8.050401AF	8.050401AG	8.050401AH	8.050401AI	8.050401AJ		
8.050401BA	8.050401BB	8.050401BC	8.050401BD	8.050401BE	8.050401BF	8.050401BG	8.050401BH	8.050401BI	8.050401BJ	8.050401BK	
8.050401CA	8.050401CB	8.050401CC	8.050401CD	8.050401CE	8.050401CF	8.050401CG	8.050401CH	8.050401CI	8.050401CJ	8.050401CK	

II 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105 106 107 108 109 110 111 112 113 114 115 116 117 118 119 120 121 122 123 124 125 126 127 128 129 130 131 132 133 134 135 136 137 138 139 140 141 142 143 144 145 146 147 148 149 150 151 152 153 154 155 156 157 158 159 160 161 162 163 164 165 166 167 168 169 170 171 172 173 174 175 176 177 178 179 180 181 182 183 184 185 186 187 188 189 190 191 192 193 194 195 196 197 198 199 200 201 202 203 204 205 206 207 208 209 210 211 212 213 214 215 216 217 218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 233 234 235 236 237 238 239 240 241 242 243 244 245 246 247 248 249 250 251 252 253 254 255 256 257 258 259 260 261 262 263 264 265 266 267 268 269 270 271 272 273 274 275 276 277 278 279 280 281 282 283 284 285 286 287 288 289 290 291 292 293 294 295 296 297 298 299 300 301 302 303 304 305 306 307 308 309 310 311 312 313 314 315 316 317 318 319 320 321 322 323 324 325 326 327 328 329 330 331 332 333 334 335 336 337 338 339 340 341 342 343 344 345 346 347 348 349 350 351 352 353 354 355 356 357 358 359 360 361 362 363 364 365 366 367 368 369 370 371 372 373 374 375 376 377 378 379 380 381 382 383 384 385 386 387 388 389 390 391 392 393 394 395 396 397 398 399 400 401 402 403 404 405 406 407 408 409 410 411 412 413 414 415 416 417 418 419 420 421 422 423 424 425 426 427 428 429 430 431 432 433 434 435 436 437 438 439 440 441 442 443 444 445 446 447 448 449 450 451 452 453 454 455 456 457 458 459 460 461 462 463 464 465 466 467 468 469 470 471 472 473 474 475 476 477 478 479 480 481 482 483 484 485 486 487 488 489 490 491 492 493 494 495 496 497 498 499 500 501 502 503 504 505 506 507 508 509 510 511 512 513 514 515 516 517 518 519 520 521 522 523 524 525 526 527 528 529 530 531 532 533 534 535 536 537 538 539 540 541 542 543 544 545 546 547 548 549 550 551 552 553 554 555 556 557 558 559 560 561 562 563 564 565 566 567 568 569 570 571 572 573 574 575 576 577 578 579 580 581 582 583 584 585 586 587 588 589 590 591 592 593 594 595 596 597 598 599 600 601 602 603 604 605 606 607 608 609 610 611 612 613 614 615 616 617 618 619 620 621 622 623 624 625 626 627 628 629 630 631 632 633 634 635 636 637 638 639 640 641 642 643 644 645 646 647 648 649 650 651 652 653 654 655 656 657 658 659 660 661 662 663 664 665 666 667 668 669 670 671 672 673 674 675 676 677 678 679 680 681 682 683 684 685 686 687 688 689 690 691 692 693 694 695 696 697 698 699 700 701 702 703 704 705 706 707 708 709 710 711 712 713 714 715 716 717 718 719 720 721 722 723 724 725 726 727 728 729 730 731 732 733 734 735 736 737 738 739 740 741 742 743 744 745 746 747 748 749 750 751 752 753 754 755 756 757 758 759 760 761 762 763 764 765 766 767 768 769 770 771 772 773 774 775 776 777 778 779 780 781 782 783 784 785 786 787 788 789 790 791 792 793 794 795 796 797 798 799 800 801 802 803 804 805 806 807 808 809 810 811 812 813 814 815 816 817 818 819 820 821 822 823 824 825 826 827 828 829 830 831 832 833 834 835 836 837 838 839 840 841 842 843 844 845 846 847 848 849 850 851 852 853 854 855 856 857 858 859 860 861 862 863 864 865 866 867 868 869 870 871 872 873 874 875 876 877 878 879 880 881 882 883 884 885 886 887 888 889 890 891 892 893 894 895 896 897 898 899 900 901 902 903 904 905 906 907 908 909 910 911 912 913 914 915 916 917 918 919 920 921 922 923 924 925 926 927 928 929 930 931 932 933 934 935 936 937 938 939 940 941 942 943 944 945 946 947 948 949 950 951 952 953 954 955 956 957 958 959 960 961 962 963 964 965 966 967 968 969 970 971 972 973 974 975 976 977 978 979 980 981 982 983 984 985 986 987 988 989 990 991 992 993 994 995 996 997 998 999 1000 1001 1002 1003 1004 1005 1006 1007 1008 1009 1010 1011 1012 1013 1014 1015 1016 1017 1018 1019 1020 1021 1022 1023 1024 1025 1026 1027 1028 1029 1030 1031 1032 1033 1034 1035 1036 1037 1038 1039 1040 1041 1042 1043 1044

0014120 20000000

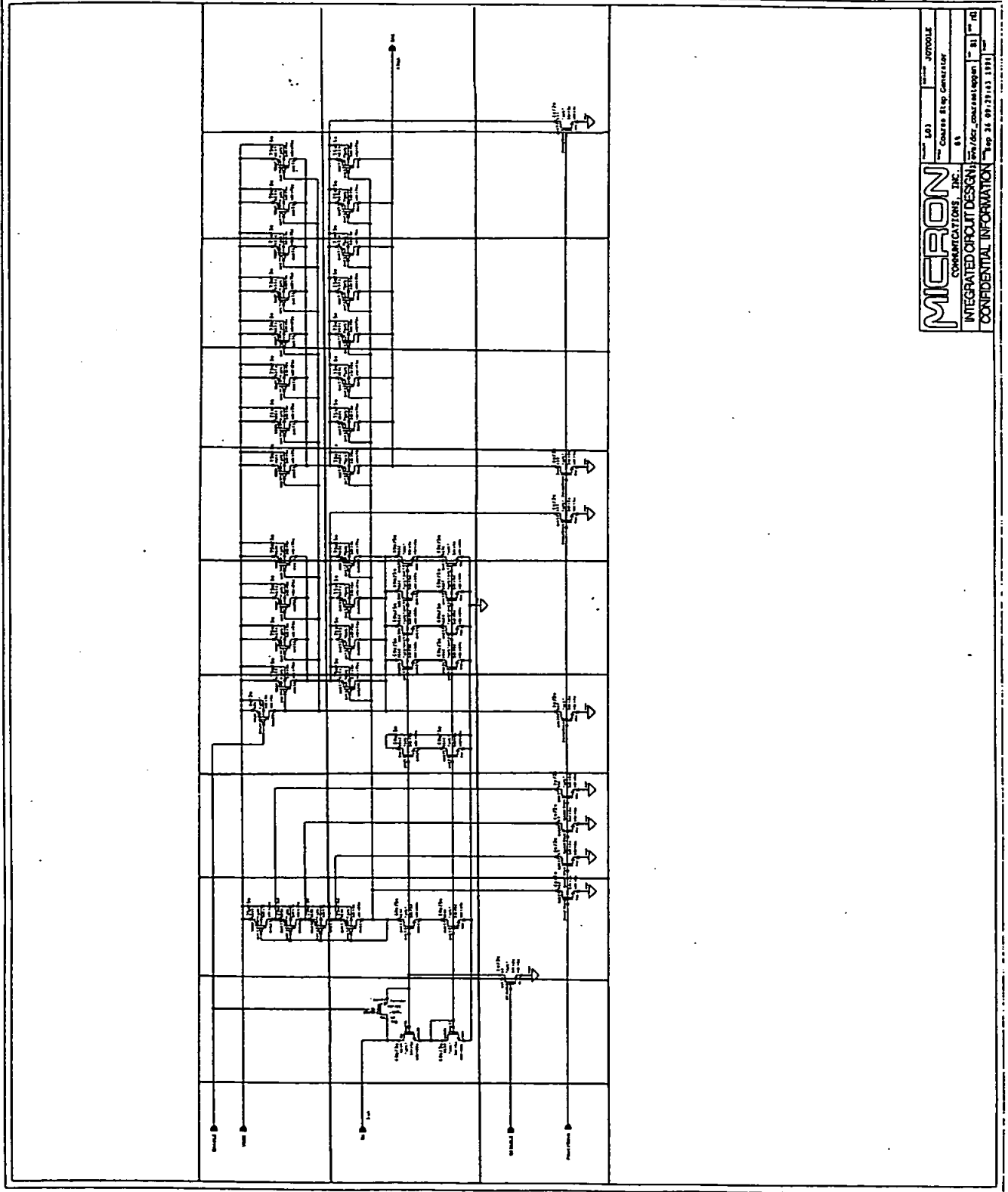


Fig 8.050401

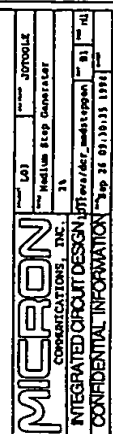
007720-20920560

MI40-030

8.050402AA	8.050402AB	8.050402AC	8.050402AD	8.050402AE	8.050402AF	8.050402AG	8.050402AH	8.050402AI	8.050402AJ
8.050402BA	8.050402BB	8.050402BC	8.050402BD	8.050402BE	8.050402BF	8.050402BG	8.050402BH	8.050402BI	8.050402BJ
8.050402CA	8.050402CB	8.050402CC	8.050402CD	8.050402CE	8.050402CF	8.050402CG	8.050402CH	8.050402CI	8.050402CJ

IF 09 88.050402

FIG. 8.050402



00740" 20920500

MI40-030

8.050403AA	8.050403AB	8.050403AC	8.050403AD	8.050403AE	8.050403AF	8.050403AG	8.050403AH	8.050403AI
8.050403BA	8.050403BB	8.050403BC	8.050403BD	8.050403BE	8.050403BF	8.050403BG	8.050403BH	8.050403BI

IE II 07 88.050403

001420 20920500

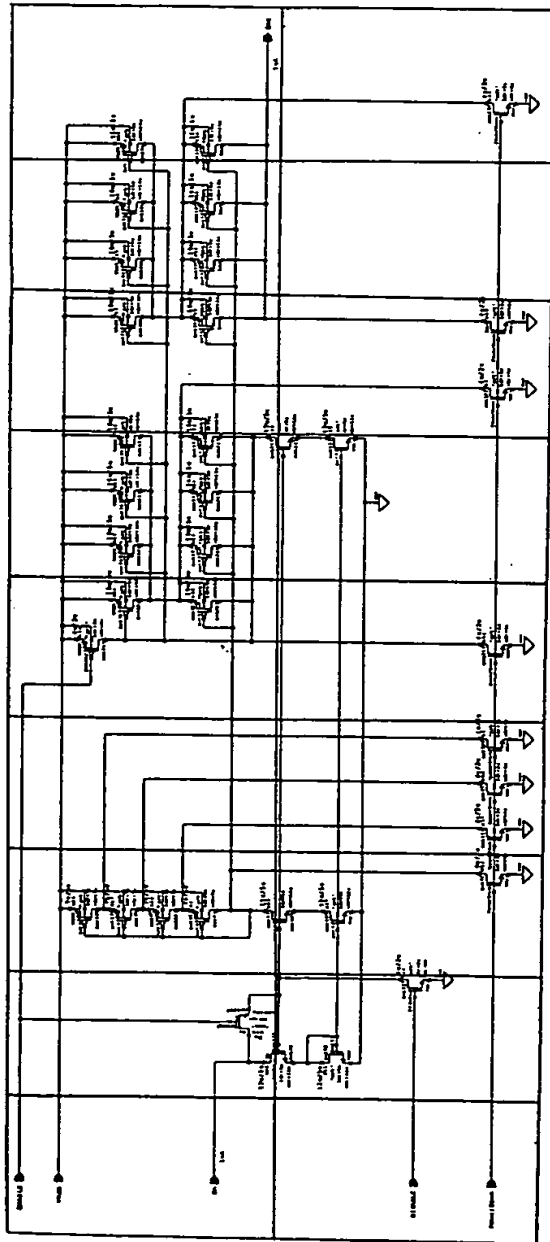


Fig. 8.050403

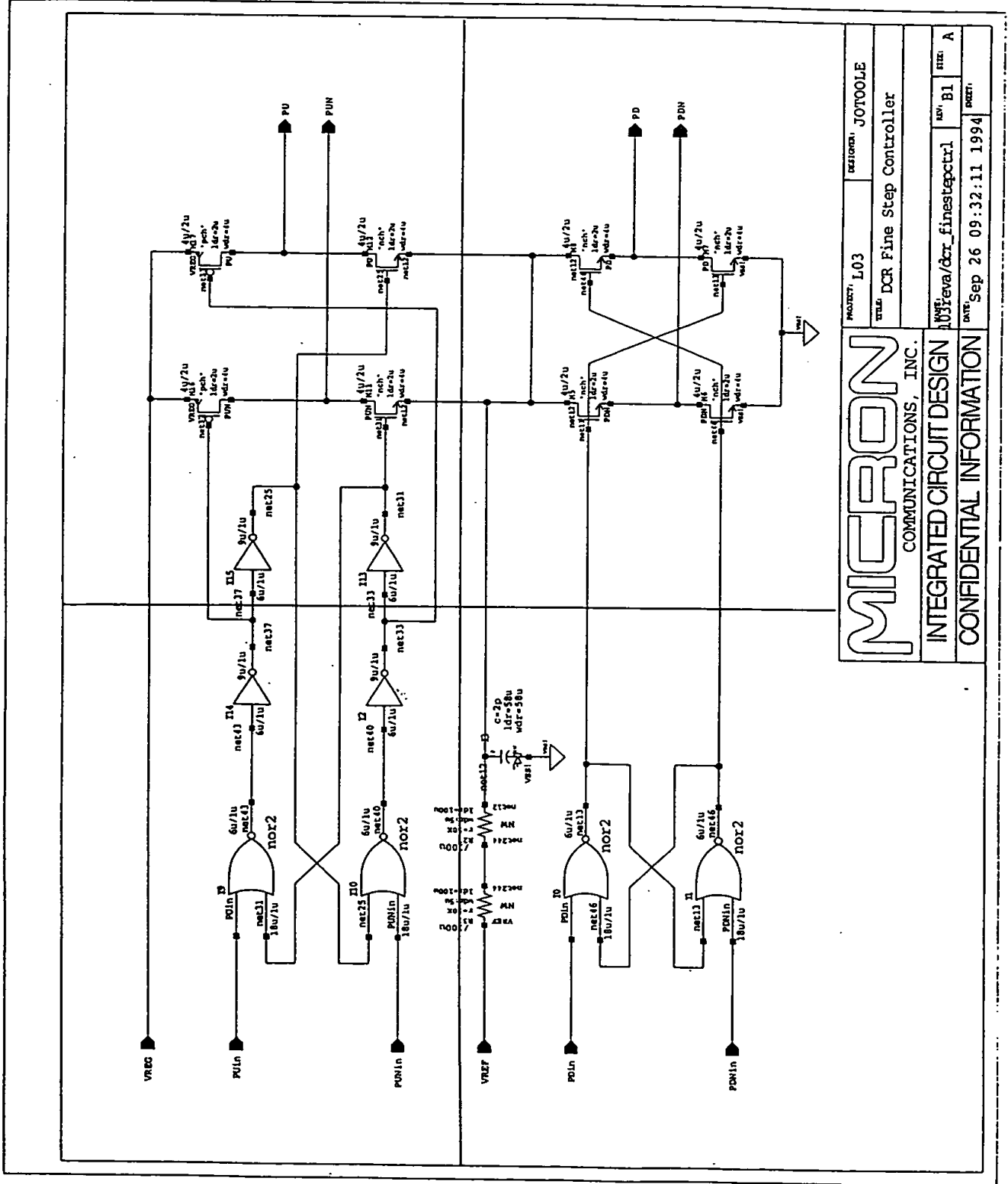
MICRON		LAS	JOYCE
COMMUNICATIONS, INC.		Medium Film Strip Controller	
INTEGRATED CIRCUIT DESIGN		9-11	
CONFIDENTIAL INFORMATION		77/dec_jan/1980	B1
		Rev 24 09-11-18-1181	

001400 00000000

MI40-030

8.050404AA	8.050404AB
8.050404BA	8.050404BB

MI 40 030 8.050404



MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: DCR Fine Step Controller	
INTEGRATED CIRCUIT DESIGN		DESIGN: 103feva/dcr_finesstepctrl	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 26 09:32:11 1994	HEET: A

Fig. 8.050409

8.0504054A	8.0504054B	8.0504054C	8.0504054D	8.0504054E	8.0504054F	8.0504054G	8.0504054H	8.0504054I	8.0504054J	8.0504054K	8.0504054L	8.0504054M
8.0504058A	8.0504058B	8.0504058C	8.0504058D	8.0504058E	8.0504058F	8.0504058G	8.0504058H	8.0504058I	8.0504058J	8.0504058K	8.0504058L	8.0504058M
8.050405CA	8.050405CB	8.050405CC	8.050405CD	8.050405CE	8.050405CF	8.050405CG	8.050405CH	8.050405CI	8.050405CJ	8.050405CK	8.050405CL	8.050405CM
8.050405DA	8.050405DB	8.050405DC	8.050405DD	8.050405DE	8.050405DF	8.050405DG	8.050405DH	8.050405DI	8.050405DJ			
8.050405EA	8.050405EB	8.050405EC	8.050405ED	8.050405EE	8.050405EF	8.050405EG	8.050405EH	8.050405EI	8.050405EJ			

JE H 00 88.005004.005

MI40-030

88.05.05 69

001110 20000000

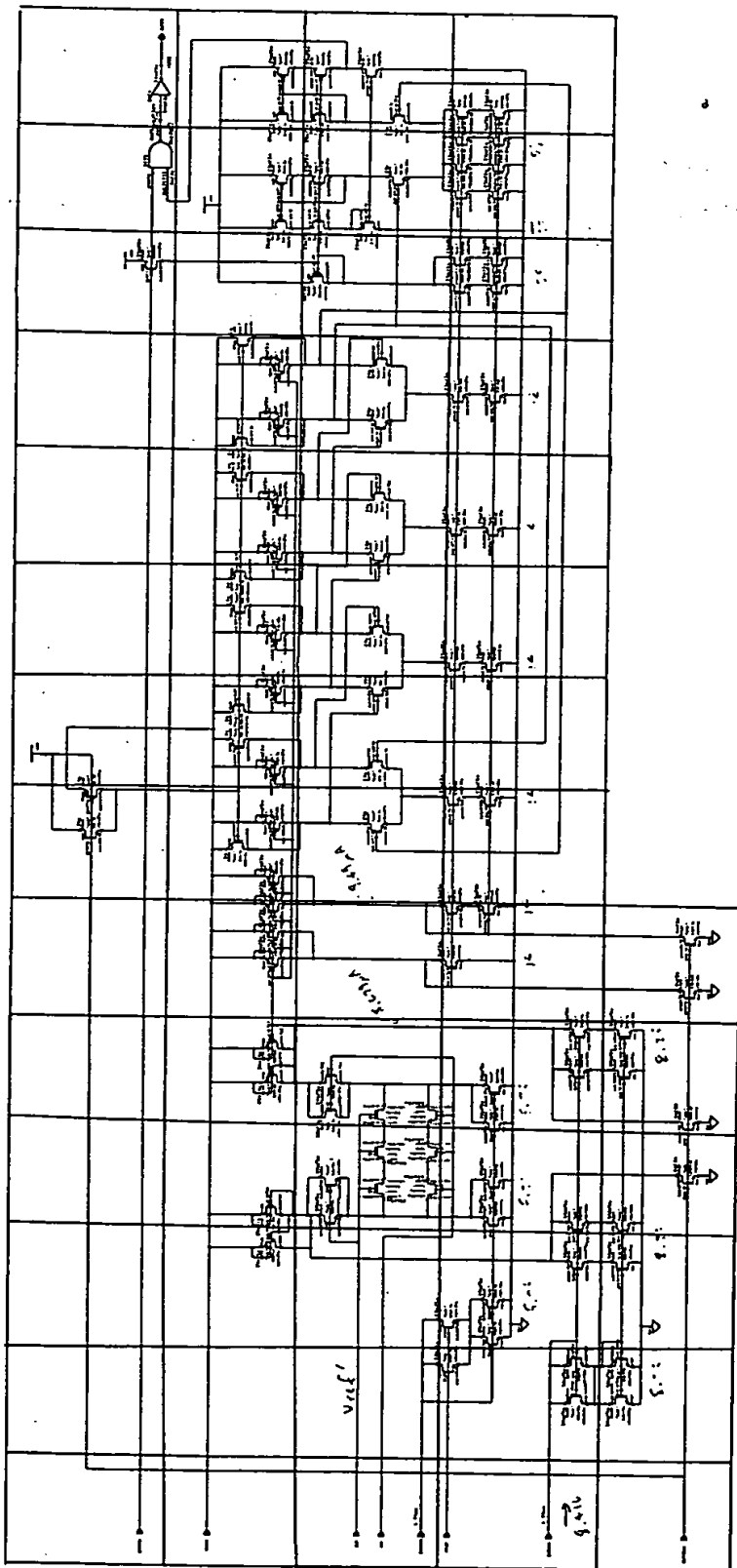


FIG. 8.0505

235A

004420 20000000

MI40-030

8.0506AA	8.0506AB
8.0506BA	8.0506BB

IL 10 88.05.05

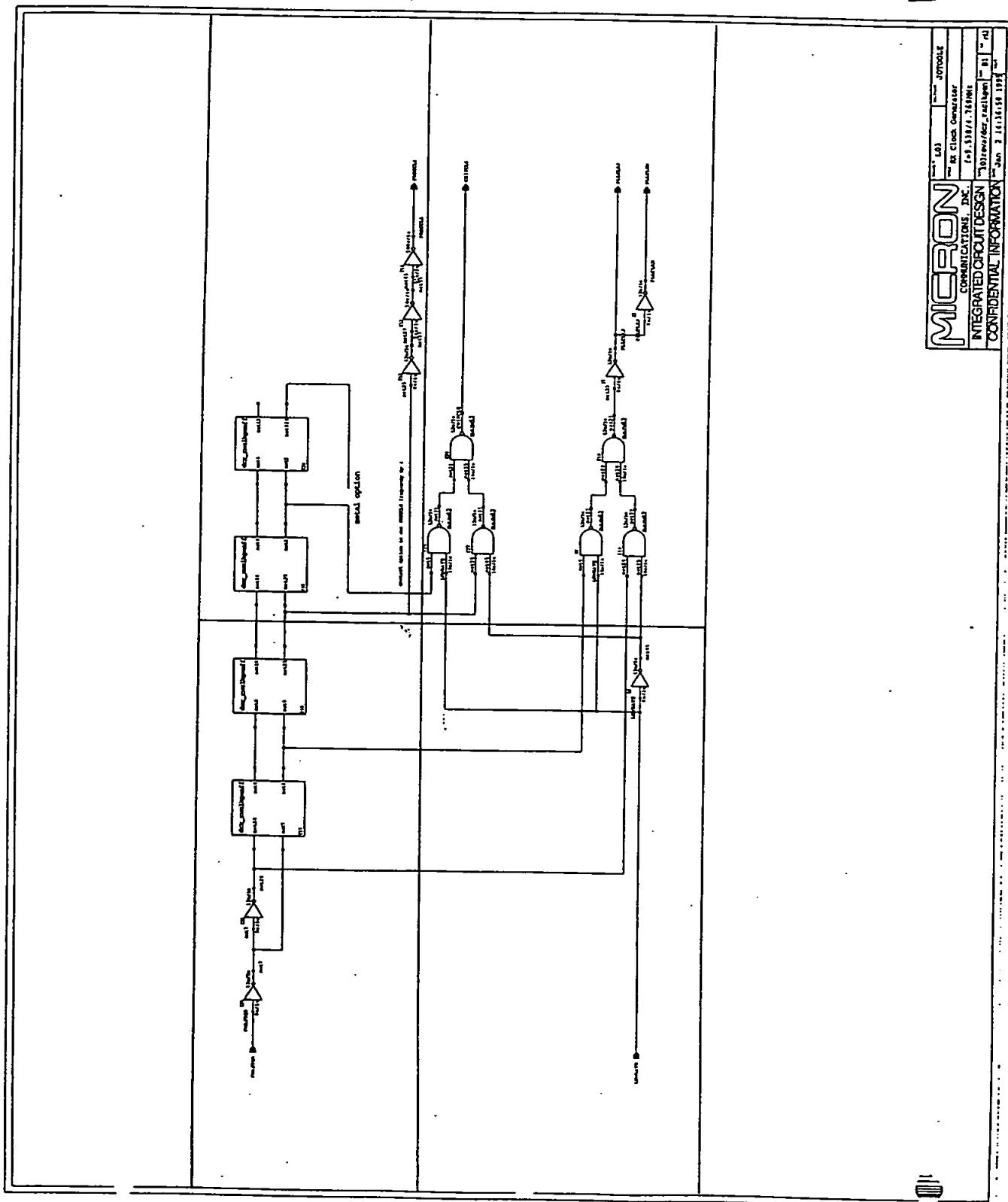
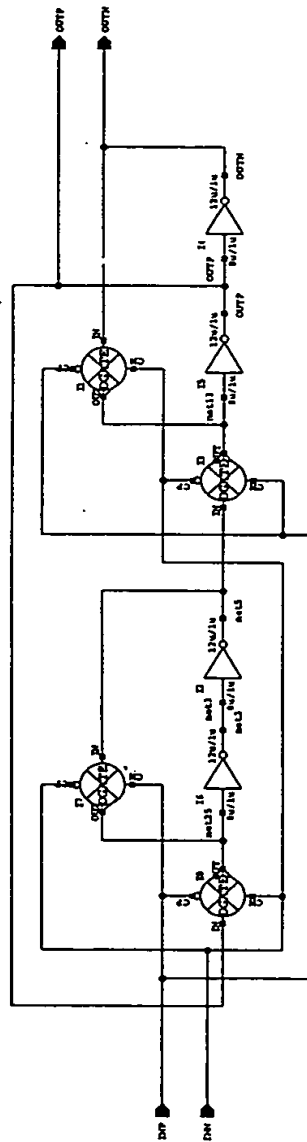
[illegible]

Fig. 8.0506

三



MICRON COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
PART#	U03
NAME	Rx Clock Generator
Flip-Flop	
THRESHOLD	0.81
DATE	Sep 26 09:36:05 1994

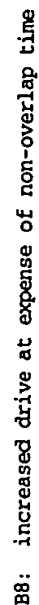
111
 112
 113
 114
 115
 116
 117
 118
 119
 120
 121
 122
 123
 124
 125
 126
 127
 128
 129
 130
 131
 132
 133
 134
 135
 136
 137
 138
 139
 140
 141
 142
 143
 144
 145
 146
 147
 148
 149
 150
 151
 152
 153
 154
 155
 156
 157
 158
 159
 160
 161
 162
 163
 164
 165
 166
 167
 168
 169
 170
 171
 172
 173
 174
 175
 176
 177
 178
 179
 180
 181
 182
 183
 184
 185
 186
 187
 188
 189
 190
 191
 192
 193
 194
 195
 196
 197
 198
 199
 200
 201
 202
 203
 204
 205
 206
 207
 208
 209
 210
 211
 212
 213
 214
 215
 216
 217
 218
 219
 220
 221
 222
 223
 224
 225
 226
 227
 228
 229
 230
 231
 232
 233
 234
 235
 236
 237
 238
 239
 240
 241
 242
 243
 244
 245
 246
 247
 248
 249
 250
 251
 252
 253
 254
 255
 256
 257
 258
 259
 260
 261
 262
 263
 264
 265
 266
 267
 268
 269
 270
 271
 272
 273
 274
 275
 276
 277
 278
 279
 280
 281
 282
 283
 284
 285
 286
 287
 288
 289
 290
 291
 292
 293
 294
 295
 296
 297
 298
 299
 300
 301
 302
 303
 304
 305
 306
 307
 308
 309
 310
 311
 312
 313
 314
 315
 316
 317
 318
 319
 320
 321
 322
 323
 324
 325
 326
 327
 328
 329
 330
 331
 332
 333
 334
 335
 336
 337
 338
 339
 340
 341
 342
 343
 344
 345
 346
 347
 348
 349
 350
 351
 352
 353
 354
 355
 356
 357
 358
 359
 360
 361
 362
 363
 364
 365
 366
 367
 368
 369
 370
 371
 372
 373
 374
 375
 376
 377
 378
 379
 380
 381
 382
 383
 384
 385
 386
 387
 388
 389
 390
 391
 392
 393
 394
 395
 396
 397
 398
 399
 400
 401
 402
 403
 404
 405
 406
 407
 408
 409
 410
 411
 412
 413
 414
 415
 416
 417
 418
 419
 420
 421
 422
 423
 424
 425
 426
 427
 428
 429
 430
 431
 432
 433
 434
 435
 436
 437
 438
 439
 440
 441
 442
 443
 444
 445
 446
 447
 448
 449
 450
 451
 452
 453
 454
 455
 456
 457
 458
 459
 460
 461
 462
 463
 464
 465
 466
 467
 468
 469
 470
 471
 472
 473
 474
 475
 476
 477
 478
 479
 480
 481
 482
 483
 484
 485
 486
 487
 488
 489
 490
 491
 492
 493
 494
 495
 496
 497
 498
 499
 500
 501
 502
 503
 504
 505
 506
 507
 508
 509
 510
 511
 512
 513
 514
 515
 516
 517
 518
 519
 520
 521
 522
 523
 524
 525
 526
 527
 528
 529
 530
 531
 532
 533
 534
 535
 536
 537
 538
 539
 540
 541
 542
 543
 544
 545
 546
 547
 548
 549
 550
 551
 552
 553
 554
 555
 556
 557
 558
 559
 560
 561
 562
 563
 564
 565
 566
 567
 568
 569
 570
 571
 572
 573
 574
 575
 576
 577
 578
 579
 580
 581
 582
 583
 584
 585
 586
 587
 588
 589
 590
 591
 592
 593
 594
 595
 596
 597
 598
 599
 600
 601
 602
 603
 604
 605
 606
 607
 608
 609
 610
 611
 612
 613
 614
 615
 616
 617
 618
 619
 620
 621
 622

001100 00000000

8.0507AA	8.0507AB
----------	----------

LE 88.0507

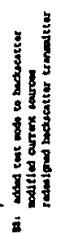
Fig. 8.0507



8.06AA	8.06AB	8.06AC	8.06AD
8.06BA	8.06BB	8.06BC	8.06BD
8.06CA	8.06CB	8.06CC	8.06CD
8.06DA	8.06DB	8.06DC	8.06DD
8.06EA	8.06EB	8.06EC	8.06ED

IL IL BB.0015

F16.8.06

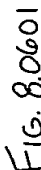


004720" 20320540

MI40-030

8.0601AA	8.0601AB
8.0601BA	8.0601BB

IL 11 05 88.01500 II



[illegible]

MI40-030

8.060101AA	8.060101AB	8.060101AC
8.060101BA	8.060101BB	8.060101BC
8.060101CA	8.060101CB	8.060101CC

8.060101

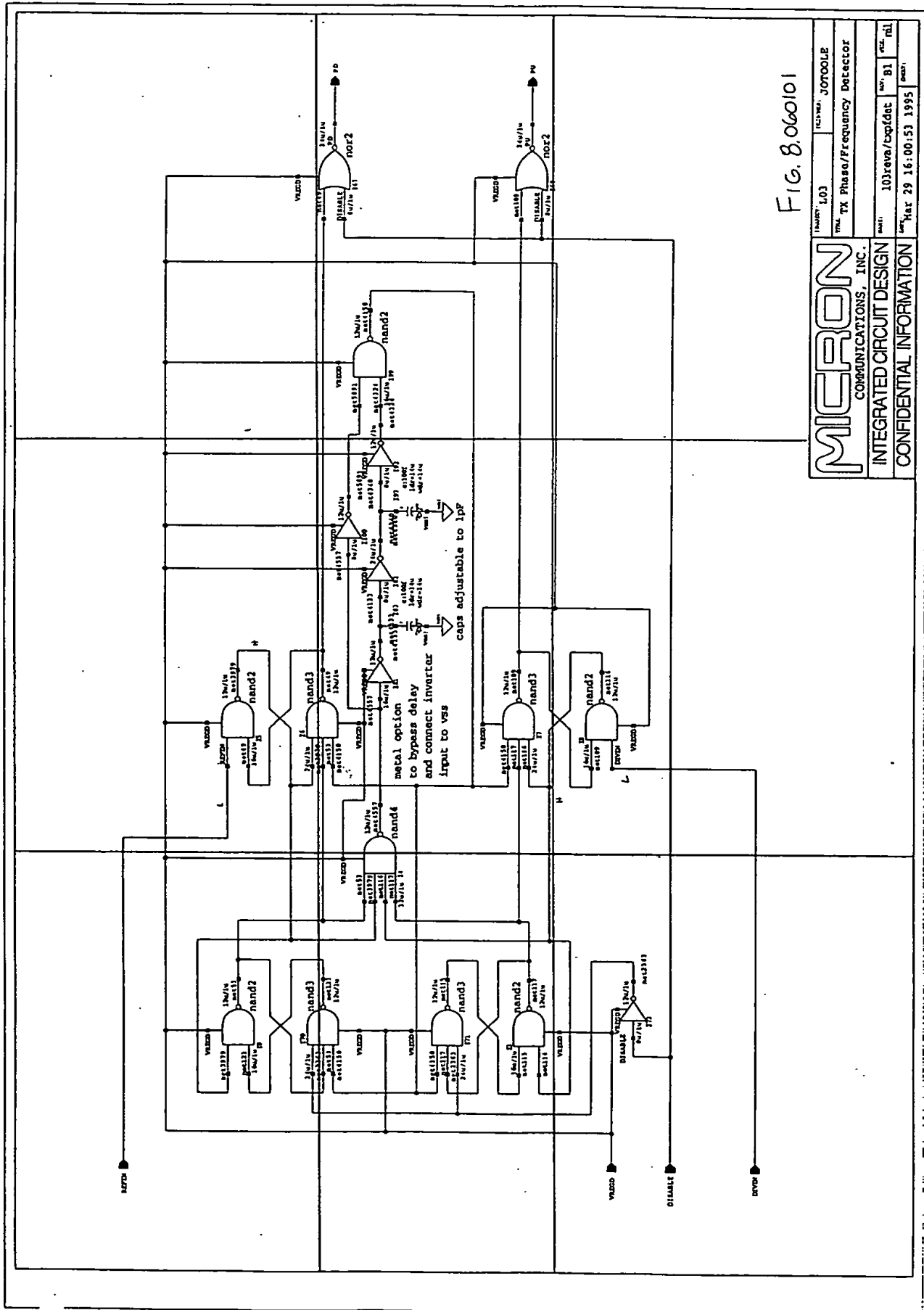


Fig. 8.060101

MICRON		PROPERTY: L03	FILE: J07000L8
COMMUNICATIONS, INC.		TX Phase/Frequency Detector	
INTEGRATED CIRCUIT DESIGN		103revs/eqidat	REV: B1
CONFIDENTIAL INFORMATION		DATE: Mar 29 16:00:53 1995	DESIGN: nll

00120 20200000

MI40-030

8.060102AA	8.060102AB
8.060102BA	8.060102BB

8.060102

0014100 20000000

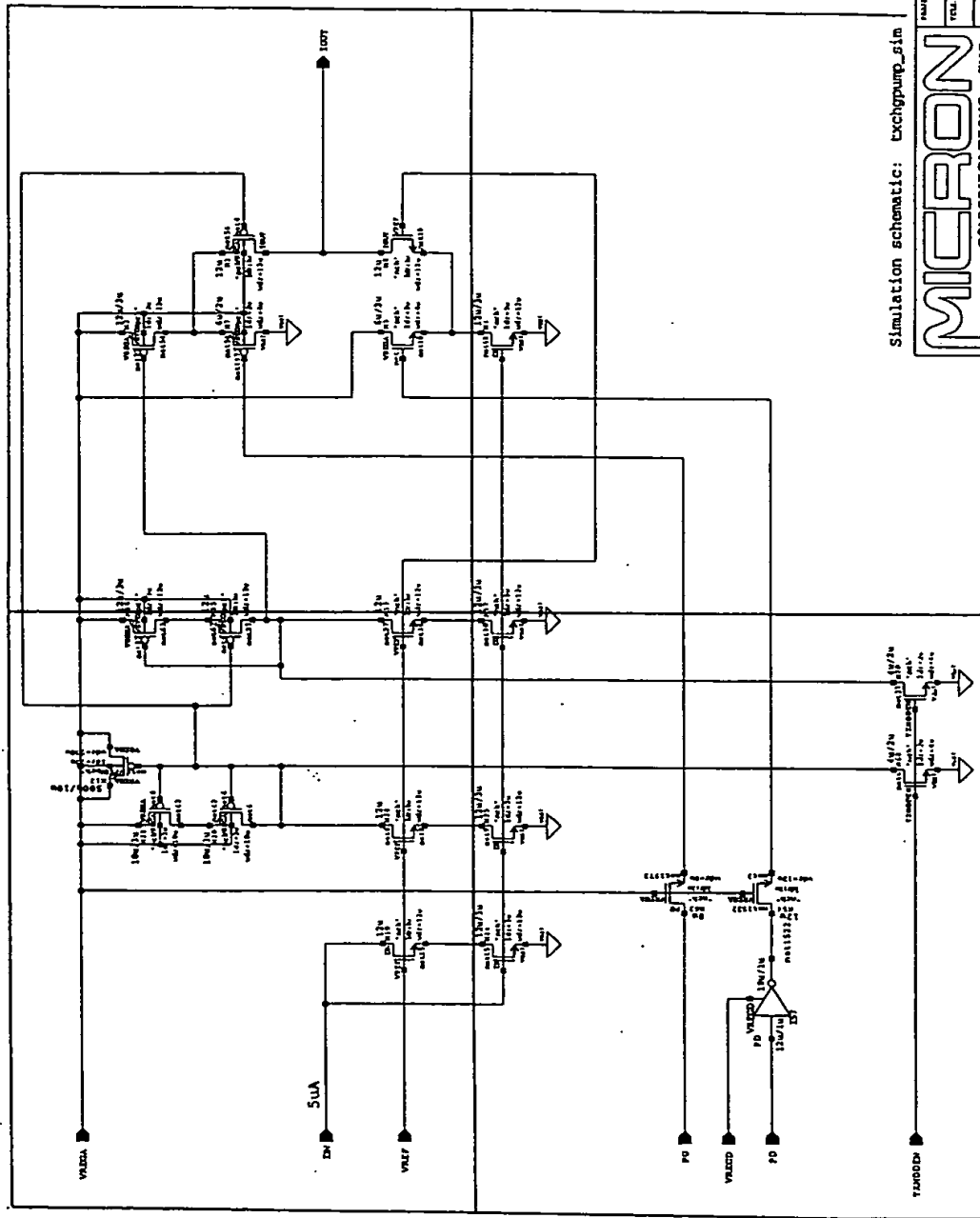


FIG. 8.060102

MICRON		DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		REV: TX PLL Charge Pump
INTEGRATED CIRCUIT DESIGN		DATE: 1030000/txchgump
CONFIDENTIAL INFORMATION		DATE: Feb 28 09:55:50 1995

001123 00000000

MI40-030

8.060103AA	8.060103AB
8.060103BA	8.060103BB
8.060103CA	8.060103CB

MI 001123 00000000

Total cap available = 55pF

tie to isolated V+

metal option

1204 1205 1206 1207 1208 1209 1210 1211 1212 1213 1214 1215 1216 1217 1218 1219 1220 1221 1222 1223 1224 1225 1226 1227 1228 1229 1230 1231 1232 1233 1234 1235 1236 1237 1238 1239 1240 1241 1242 1243 1244 1245 1246 1247 1248 1249 1250 1251 1252 1253 1254 1255 1256 1257 1258 1259 1260 1261 1262 1263 1264 1265 1266 1267 1268 1269 1270 1271 1272 1273 1274 1275 1276 1277 1278 1279 1280 1281 1282 1283 1284 1285 1286 1287 1288 1289 1290 1291 1292 1293 1294 1295 1296 1297 1298 1299 1300 1301 1302 1303 1304 1305 1306 1307 1308 1309 1310 1311 1312 1313 1314 1315 1316 1317 1318 1319 1320 1321 1322 1323 1324 1325 1326 1327 1328 1329 1330 1331 1332 1333 1334 1335 1336 1337 1338 1339 1340 1341 1342 1343 1344 1345 1346 1347 1348 1349 1350 1351 1352 1353 1354 1355 1356 1357 1358 1359 1360 1361 1362 1363 1364 1365 1366 1367 1368 1369 1370 1371 1372 1373 1374 1375 1376 1377 1378 1379 1380 1381 1382 1383 1384 1385 1386 1387 1388 1389 1390 1391 1392 1393 1394 1395 1396 1397 1398 1399 1400 1401 1402 1403 1404 1405 1406 1407 1408 1409 1410 1411 1412 1413 1414 1415 1416 1417 1418 1419 1420 1421 1422 1423 1424 1425 1426 1427 1428 1429 1430 1431 1432 1433 1434 1435 1436 1437 1438 1439 1440 1441 1442 1443 1444 1445 1446 1447 1448 1449 1450 1451 1452 1453 1454 1455 1456 1457 1458 1459 1460 1461 1462 1463 1464 1465 1466 1467 1468 1469 1470 1471 1472 1473 1474 1475 1476 1477 1478 1479 1480 1481 1482 1483 1484 1485 1486 1487 1488 1489 1490 1491 1492 1493 1494 1495 1496 1497 1498 1499 1500

MICRON		PROJECT: L03	REVISION: J07000LE
		TITLE: TX PLL Loop Filter	
COMMUNICATIONS, INC.		BW=700KHz PH=60deg	
INTEGRATED CIRCUIT DESIGN		DATE: 10/29/88	REV: B8
CONFIDENTIAL INFORMATION		DATE: Feb 5 14:40:11 1996	

B2: moved extra caps to biasok

B8: moved 2 2K registers to bombs

8.060104AA	8.060104AB	8.060104AC
8.060104BA	8.060104BB	8.060104BC
8.060104CA	8.060104CB	8.060104CC
8.060104DA	8.060104DB	8.060104DC

II II II II II II II II

004420-2090503

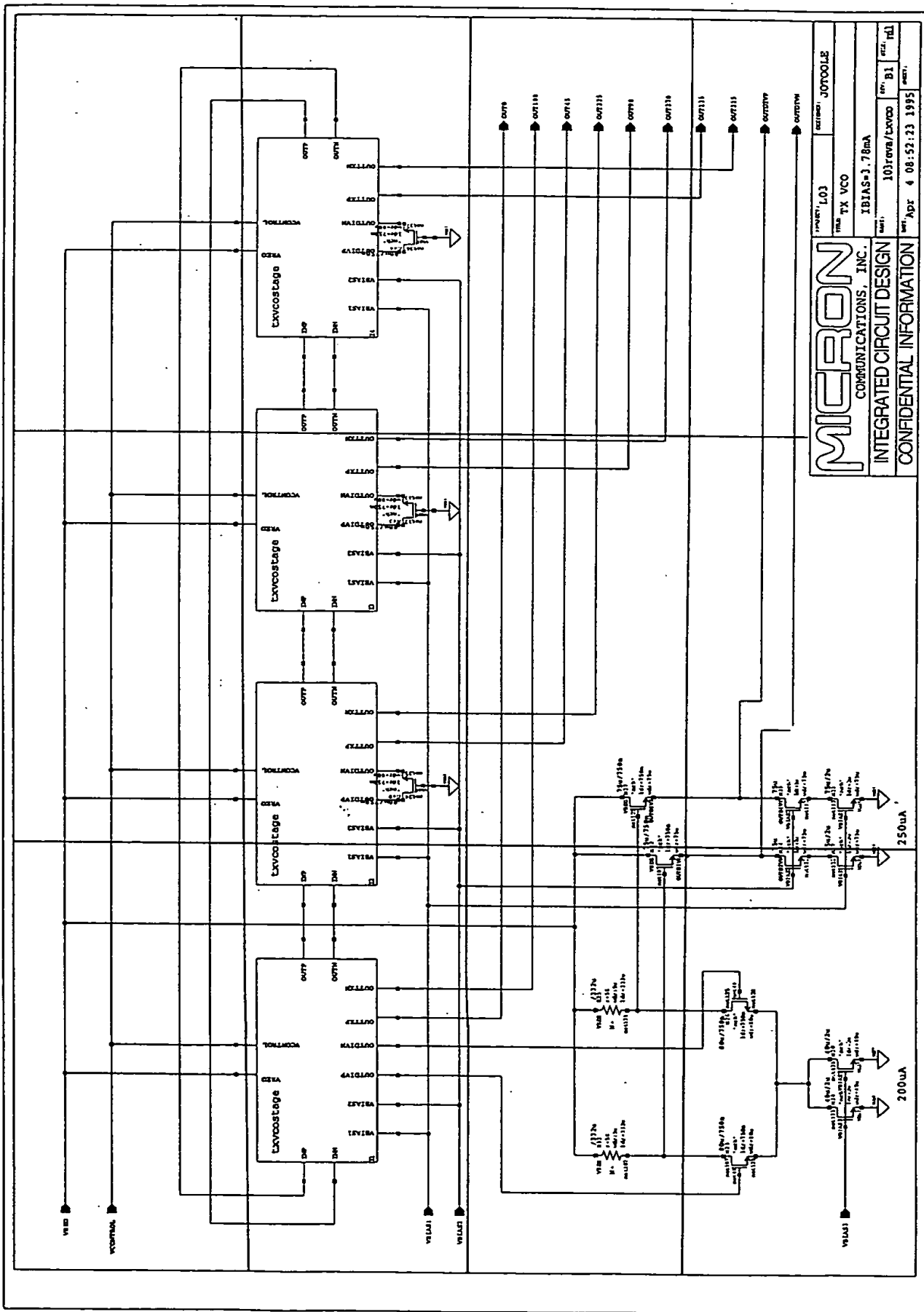
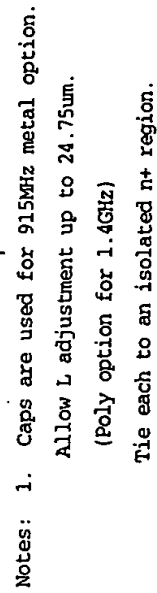


Fig. 8.060104

8.06010401AA	8.06010401AB	8.06010401AC	8.06010401AD
8.06010401BA	8.06010401BB	8.06010401BC	8.06010401BD

Fig. 8.06010901



Notes: 1. Caps are used for 915MHz metal option.
 Allow L adjustment up to 24.75um.
 (Poly option for 1.4GHz)
 Tie each to an isolated n+ region.

	1970	1971	1972	1973	1974	1975	1976	1977	1978	1979	1980	1981	1982	1983	1984	1985	1986	1987	1988	1989	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100	2101	2102	2103	2104	2105	2106	2107	2108	2109	2110	2111	2112	2113	2114	2115	2116	2117	2118	2119	2120	2121	2122	2123	2124	2125	2126	2127	2128	2129	2130	2131	2132	2133	2134	2135	2136	2137	2138	2139	2140	2141	2142	2143	2144	2145	2146	2147	2148	2149	2150	2151	2152	2153	2154	2155	2156	2157	2158	2159	2160	2161	2162	2163	2164	2165	2166	2167	2168	2169	2170	2171	2172	2173	2174	2175	2176	2177	2178	2179	2180	2181	2182	2183	2184	2185	2186	2187	2188	2189	2190	2191	2192	2193	2194	2195	2196	2197	2198	2199	2200	2201	2202	2203	2204	2205	2206	2207	2208	2209	2210	2211	2212	2213	2214	2215	2216	2217	2218	2219	2220	2221	2222	2223	2224	2225	2226	2227	2228	2229	2230	2231	2232	2233	2234	2235	2236	2237	2238	2239	2240	2241	2242	2243	2244	2245	2246	2247	2248	2249	2250	2251	2252	2253	2254	2255	2256	2257	2258	2259	2260	2261	2262	2263	2264	2265	2266	2267	2268	2269	2270	2271	2272	2273	2274	2275	2276	2277	2278	2279	2280	2281	2282	2283	2284	2285	2286	2287	2288	2289	2290	2291	2292	2293	2294	2295	2296	2297	2298	2299	2300	2301	2302	2303	2304	2305	2306	2307	2308	2309	2310	2311	2312	2313	2314	2315	2316	2317	2318	2319	2320	2321	2322	2323	2324	2325	2326	2327	2328	2329	2330	2331	2332	2333	2334	2335	2336	2337	2338	2339	2340	2341	2342	2343	2344	2345	2346	2347	2348	2349	2350	2351	2352	2353	2354	2355	2356	2357	2358	2359	2360	2361	2362	2363	2364	2365	2366	2367	2368	2369	2370	2371	2372	2373	2374	2375	2376	2377	2378	2379	2380	2381	2382	2383	2384	2385	2386	2387	2388	2389	2390	2391	2392	2393	2394	2395	2396	2397	2398	2399	2400	2401	2402	2403	2404	2405	2406	2407	2408	2409	2410	2411	2412	2413	2414	2415	2416	2417	2418	2419	2420	2421	2422	2
--	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	---

MI40-030

8.0601040101AA	8.0601040101AB	8.0601040101AC
8.0601040101BA	8.0601040101BB	8.0601040101BC

LE II 88.06501004010011

[illegible]

BEST AVAILABLE COPY

8.060105AA	8.060105AB	8.060105AC	8.060105AD
8.060105BA	8.060105BB	8.060105BC	8.060105BD
8.060105CA	8.060105CB	8.060105CC	8.060105CD
8.060105DA	8.060105DB	8.060105DC	8.060105DD

004720 24320500

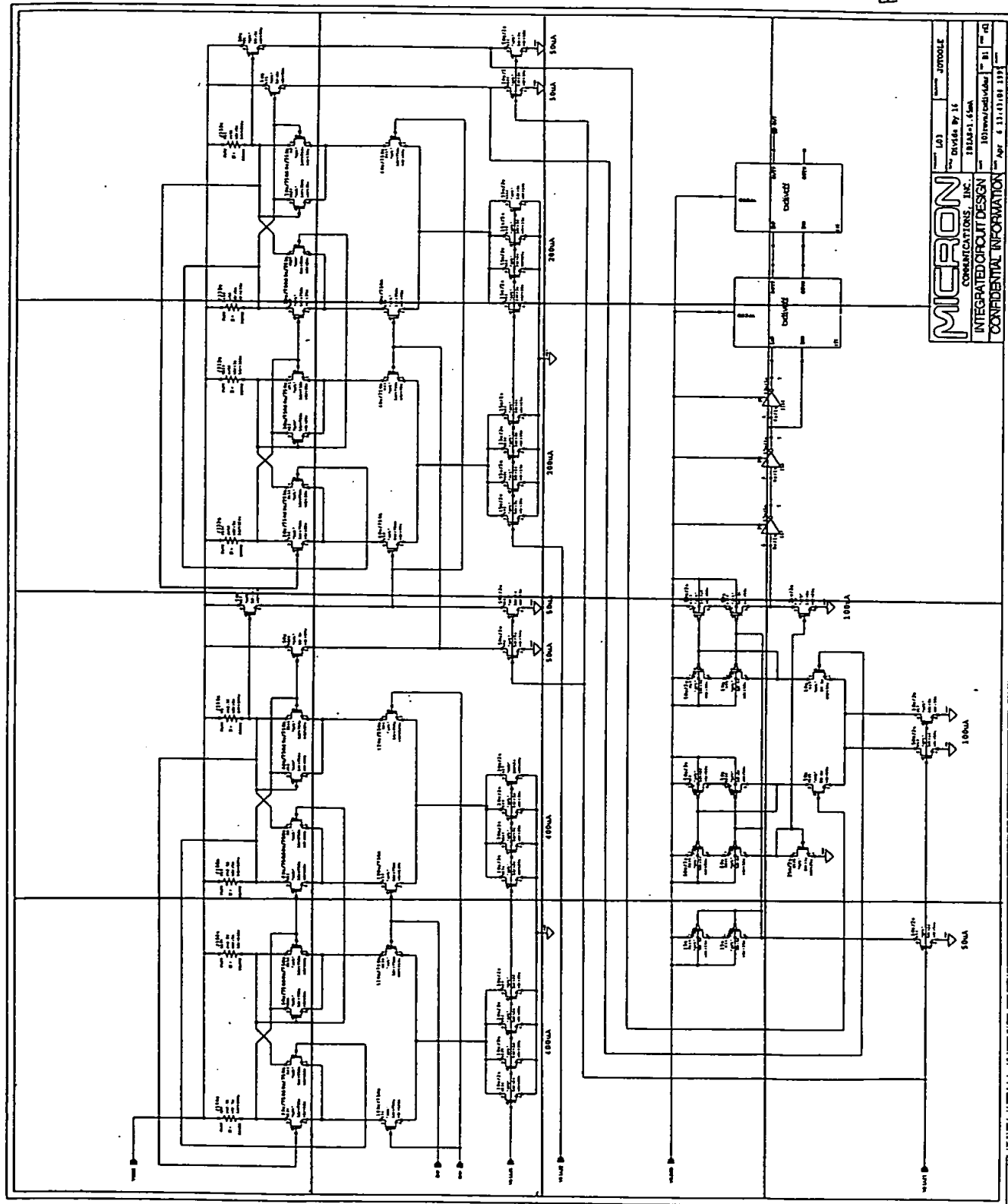


FIG. 8.060105

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
Part No.	1601
Divide By	16
16Bit	16Bit
100pA	100pA
Apr 8 11:01:00	1105

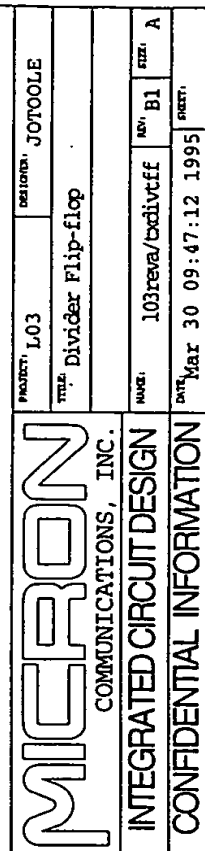
00770 2050000

MI40-030

8.06010501AA	8.06010501AB
--------------	--------------

II II 88.00500.00500.00

FIG. 8.06010501



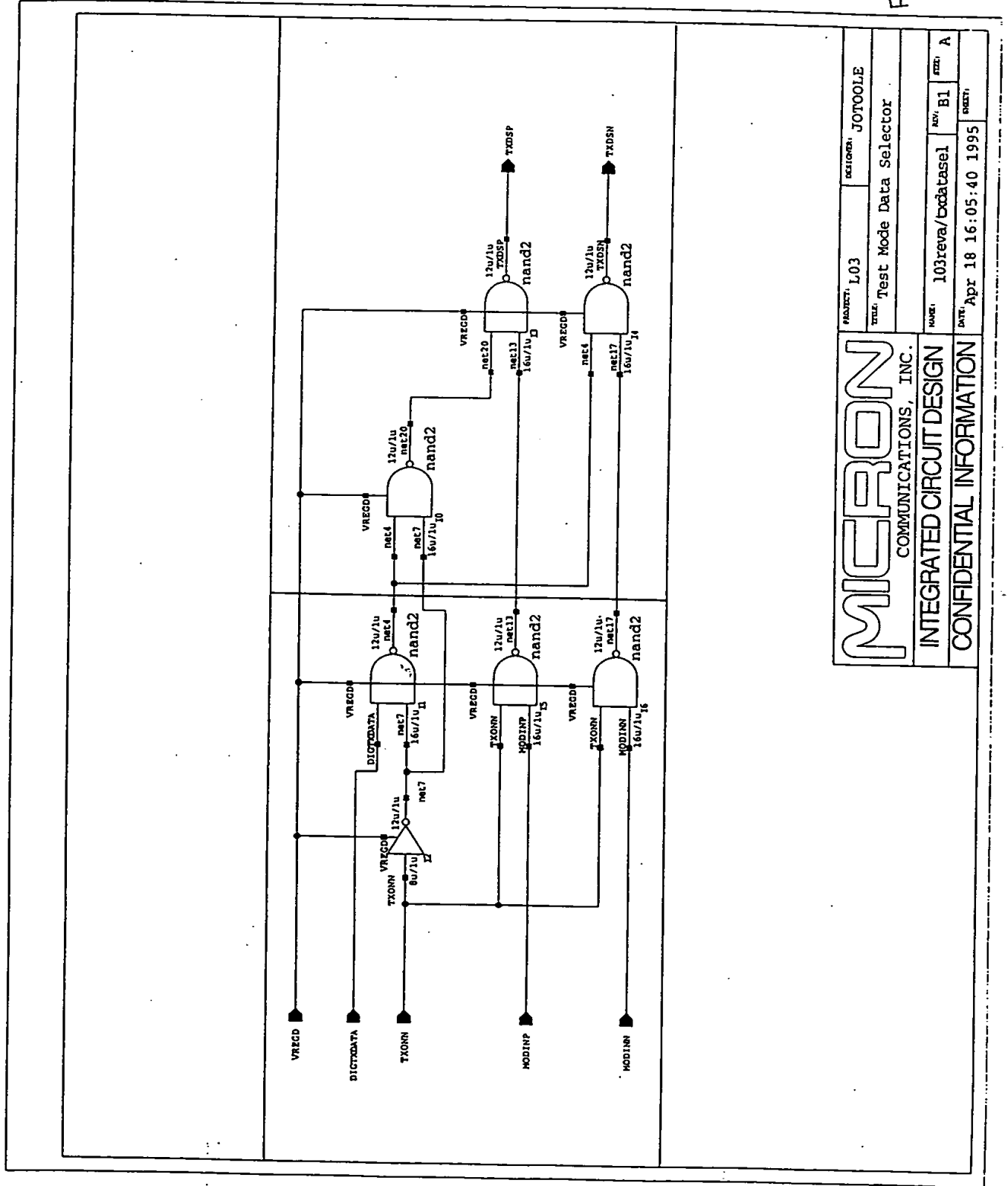
nicpro
COMMUNICATIONS, INC.

001420 00000000

8.0602AA	8.0602AB
----------	----------

EX. 8.0602

CONFIDENTIAL



MICRON		REVISION: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Test Mode Data Selector	
INTEGRATED CIRCUIT DESIGN		NAME: 103revA/Broadcast	REV: B1
CONFIDENTIAL INFORMATION		DATE: Apr 18 16:05:40 1995	FILE: A

FIG. 8.0602

007120 20200500

8.0603AA	8.0603AB
----------	----------

EE.006003

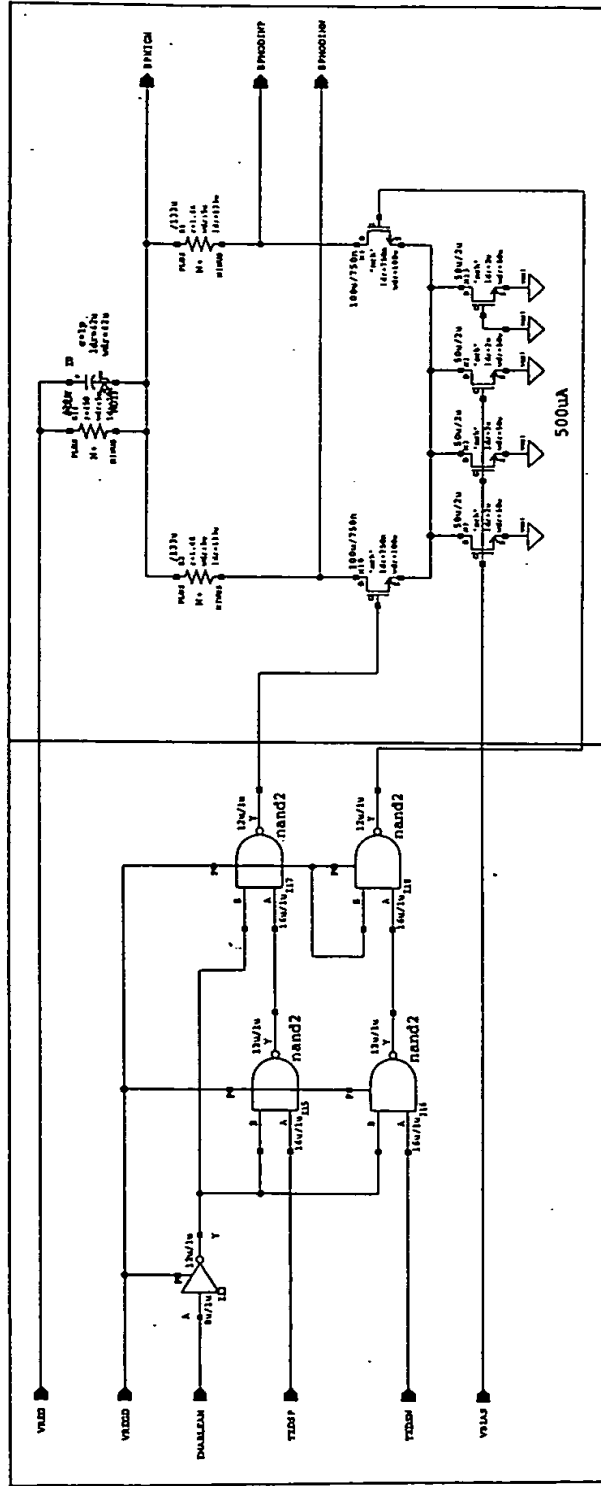


Fig. 8.0603

MICRON		DESIGN: L03	REVISION: J0700LE
COMMUNICATIONS, INC.		BPSS Modulation Driver	
INTEGRATED CIRCUIT DESIGN		IBIAS-500uA	
CONFIDENTIAL INFORMATION		103reva/cbcpk	B8
		Jan 18 10:28:46 1996	rd

B8: modified current source

007420 20920500

8.0604AA	8.0604AB
----------	----------

II 8.0604

Fig. 8.0604

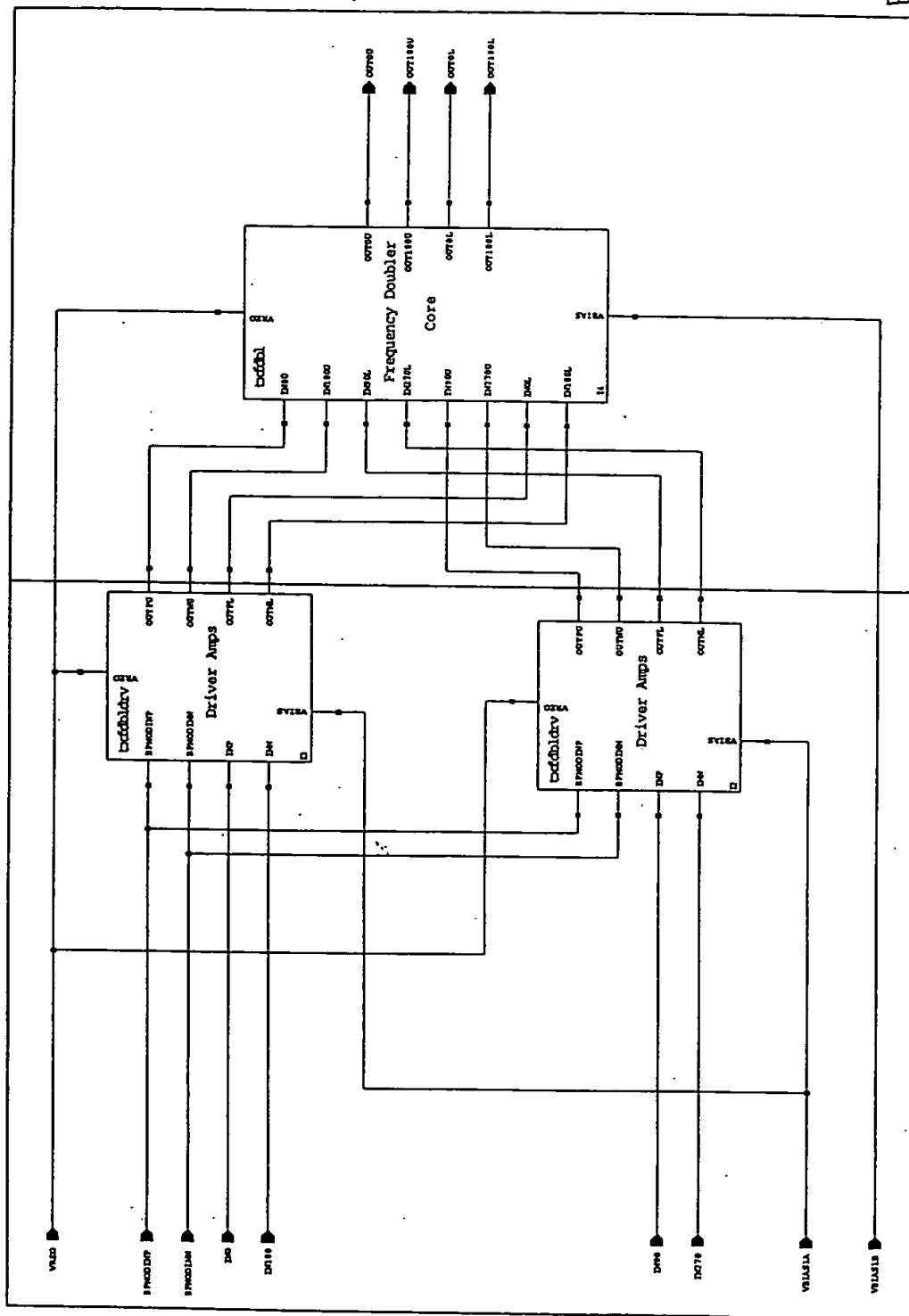


Fig. 8.0604

NOIR

COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

PROJECT: L03	DESCRIPTION: JOTOOLE
--------------	----------------------

Frequency Doubler	Control
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16
17	17
18	18
19	19
20	20
21	21
22	22
23	23
24	24
25	25
26	26
27	27
28	28
29	29
30	30
31	31
32	32
33	33
34	34
35	35
36	36
37	37
38	38
39	39
40	40
41	41
42	42
43	43
44	44
45	45
46	46
47	47
48	48
49	49
50	50
51	51
52	52
53	53
54	54
55	55
56	56
57	57
58	58
59	59
60	60
61	61
62	62
63	63
64	64
65	65
66	66
67	67
68	68
69	69
70	70
71	71
72	72
73	73
74	74
75	75
76	76
77	77
78	78
79	79
80	80
81	81
82	82
83	83
84	84
85	85
86	86
87	87
88	88
89	89
90	90
91	91
92	92
93	93
94	94
95	95
96	96
97	97
98	98
99	99
100	100

IBIAS=4mA

103 revs/todoubler	ser. B1	118
--------------------	---------	-----

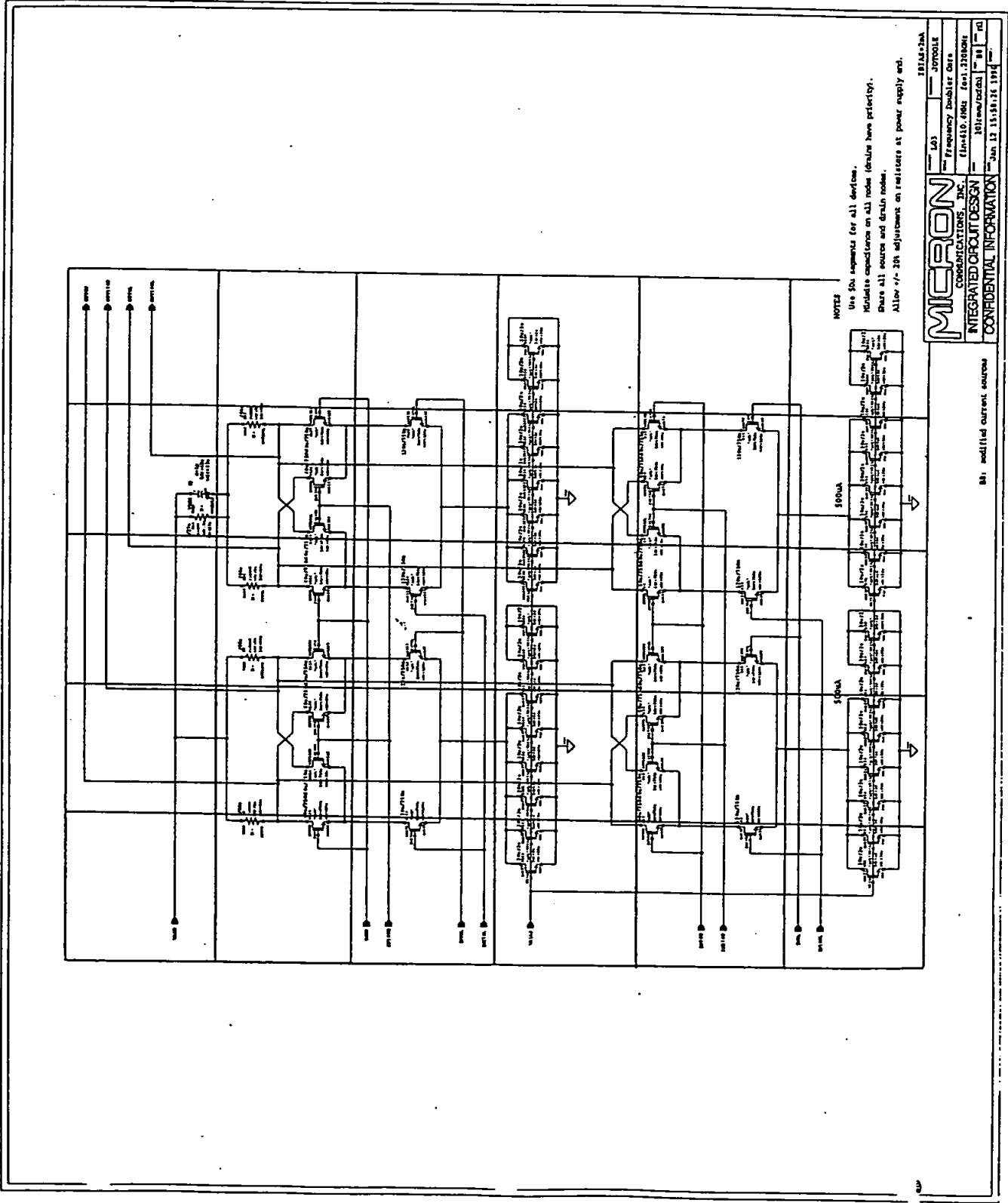
APR 5 10:17:13 1995

8.060401AA	8.060401AB	8.060401AC	8.060401AD	8.060401AE
8.060401BA	8.060401BB	8.060401BC	8.060401BD	8.060401BE
8.060401CA	8.060401CB	8.060401CC	8.060401CD	8.060401CE
8.060401DA	8.060401DB	8.060401DC	8.060401DD	8.060401DE
8.060401EA	8.060401EB	8.060401EC	8.060401ED	8.060401EE
8.060401FA	8.060401FB	8.060401FC	8.060401FD	8.060401FE

II II II 8.060401 II II

8.060401AA 8.060401AB 8.060401AC 8.060401AD 8.060401AE
8.060401BA 8.060401BB 8.060401BC 8.060401BD 8.060401BE
8.060401CA 8.060401CB 8.060401CC 8.060401CD 8.060401CE
8.060401DA 8.060401DB 8.060401DC 8.060401DD 8.060401DE
8.060401EA 8.060401EB 8.060401EC 8.060401ED 8.060401EE
8.060401FA 8.060401FB 8.060401FC 8.060401FD 8.060401FE

000000 20000000



004400 20500000

8.0605AA	8.0605AB
----------	----------

IL 11 88.016005

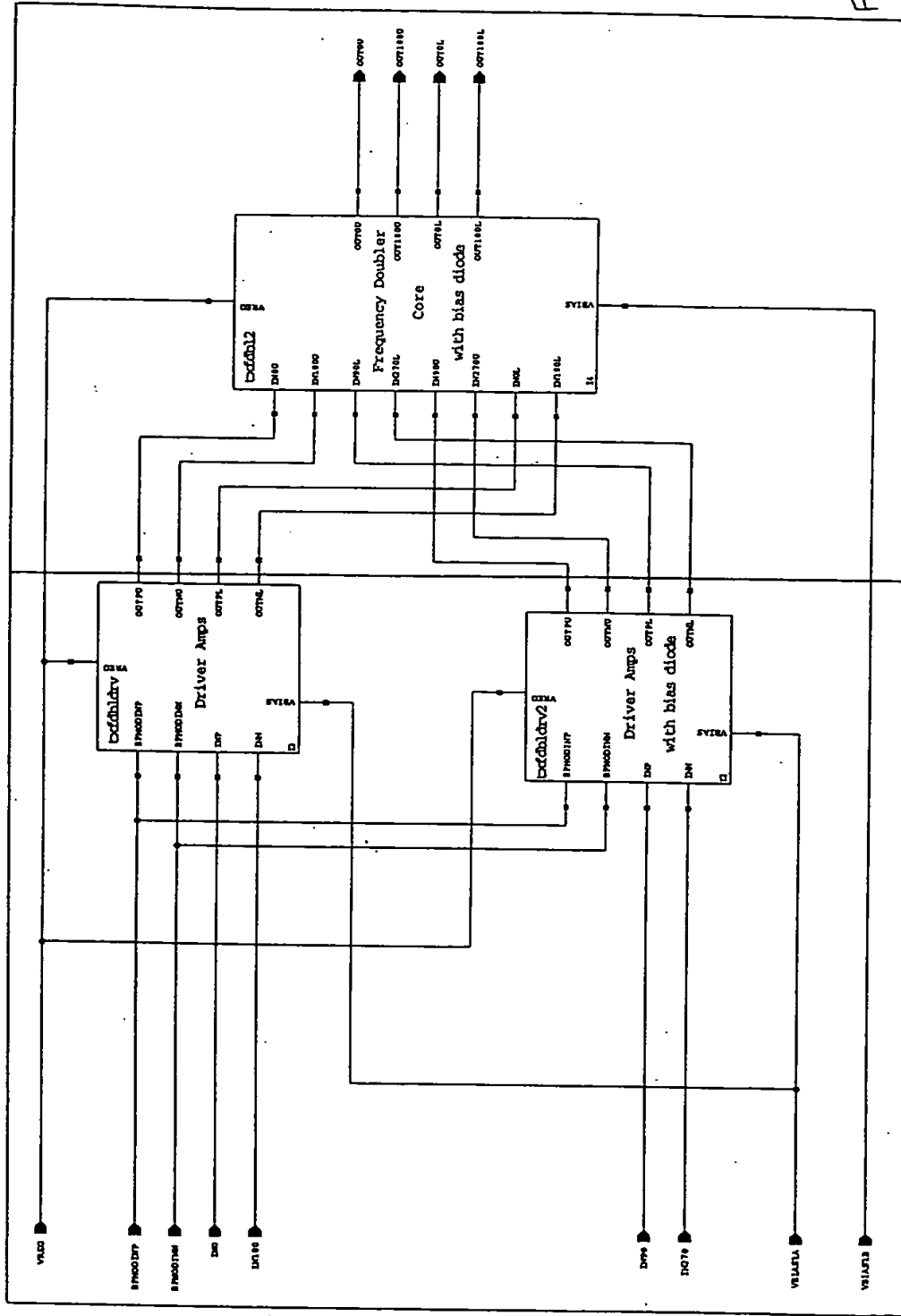


Fig. 8.0605

MICRON		PROJECT: L03	REVISION: J0700LE
COMMUNICATIONS, INC.		Title: Frequency Doubler	
INTEGRATED CIRCUIT DESIGN		IBIAS=4mA	
CONFIDENTIAL INFORMATION		DATE: 10/19/96	BY: B8
		DATE: Jan 12 17:22:51 1996	FILE: rd1

B8: current sources modified

И. И. Б. МОСОВИ

- Minimize capacitance on output nodes.
- Share all source/drain nodes.
- Allow +/- 20% adjustment on resistors at supply end.

Fig. 8.060501

POLICY, L03

Doubler Driver Atmos

$$\text{IBIAS} = 1 \text{ mA}$$

103reva/bcf0b1drv

Jan 13 15.37.26 1000	DEPT:
----------------------	-------

NOIR

COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

B8: modified current sources

И.И.И. 8.060502

- Minimize capacitance on output nodes.
- Share all source/drain nodes.
- Allow +/- 20% adjustment on resistors at supply end.

FIG. 8.060502

NOBIS

BB8: modified current sources

PROJECT: L03	REVISED: J0700LE
TITLE: Doubler Driver Amps	
IBIAS=1mA	

NAME	101eva/bxfdbldrv2	REV	B8	REV	ml
DATE		Jan 18 08:22:12 1996			
DATE		part.			

8.060503AA	8.060503AB	8.060503AC	8.060503AD	8.060503AE
8.060503BA	8.060503BB	8.060503BC	8.060503BD	8.060503BE
8.060503CA	8.060503CB	8.060503CC	8.060503CD	8.060503CE
8.060503DA	8.060503DB	8.060503DC	8.060503DD	8.060503DE
8.060503EA	8.060503EB	8.060503EC	8.060503ED	8.060503EE
8.060503FA	8.060503FB	8.060503FC	8.060503FD	8.060503FE

8.060503

8.060503AA 8.060503AB 8.060503AC 8.060503AD 8.060503AE
8.060503BA 8.060503BB 8.060503BC 8.060503BD 8.060503BE
8.060503CA 8.060503CB 8.060503CC 8.060503CD 8.060503CE
8.060503DA 8.060503DB 8.060503DC 8.060503DD 8.060503DE
8.060503EA 8.060503EB 8.060503EC 8.060503ED 8.060503EE
8.060503FA 8.060503FB 8.060503FC 8.060503FD 8.060503FE

001120 2055300

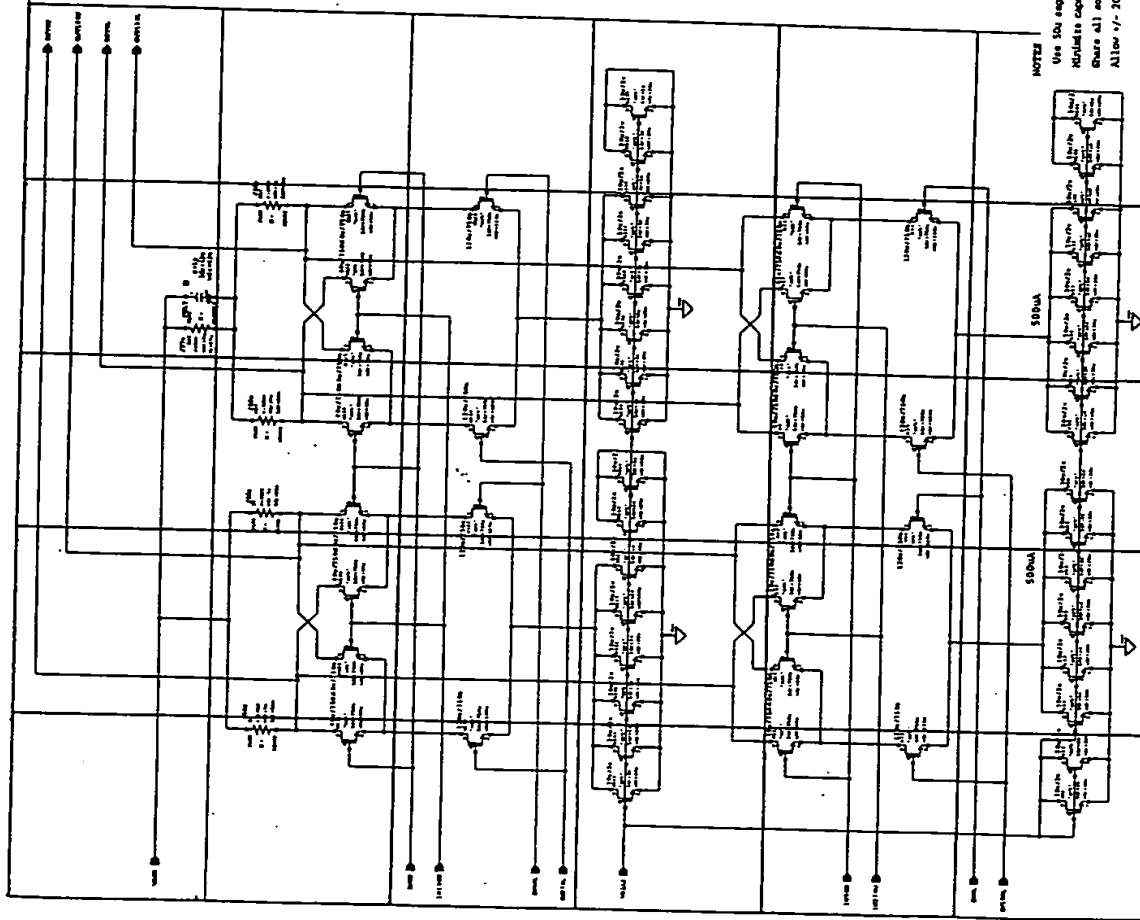


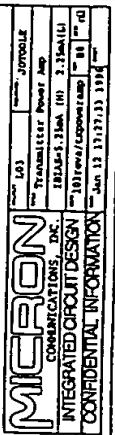
FIG. 8.060503

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
DESIGNER	LA3
DATE	JUN 18 1981
PROJECT	Frequency Doubler Core
FILE NO.	61-410-001
REV.	1
DATE	6-1-77
BY	JM
CHKD	JM
DATE	6-1-77

84, modified current source

8.0606AA	8.0606AB	8.0606AC	8.0606AD	8.0606AE	8.0606AF	8.0606AG	8.0606AH
8.0606BA	8.0606BB	8.0606BC	8.0606BD	8.0606BE	8.0606BF	8.0606BG	8.0606BH
8.0606CA	8.0606CB	8.0606CC	8.0606CD	8.0606CE	8.0606CF	8.0606CG	8.0606CH
8.0606DA	8.0606DB	8.0606DC	8.0606DD	8.0606DE	8.0606DF	8.0606DG	8.0606DH
8.0606EA	8.0606EB	8.0606EC	8.0606ED	8.0606EE	8.0606EF	8.0606EG	8.0606EH
8.0606FA	8.0606FB	8.0606FC	8.0606FD	8.0606FE	8.0606FF	8.0606FG	8.0606FH
		8.0606GC	8.0606GD	8.0606GE			
8.0606HA	8.0606HB	8.0606HC	8.0606HD	8.0606HE			
	8.0606IB	8.0606IC	8.0606ID	8.0606IE			

FIG. 8.0606



8.0607AA	8.0607AB	8.0607AC	8.0607AD	8.0607AE	8.0607AF	8.0607AG	8.0607AH	8.0607AI	8.0607AJ
8.0607BA	8.0607BB	8.0607BC	8.0607BD	8.0607BE	8.0607BF	8.0607BG	8.0607BH	8.0607BI	8.0607BJ
8.0607CA	8.0607CB	8.0607CC	8.0607CD	8.0607CE	8.0607CF	8.0607CG	8.0607CH	8.0607CI	8.0607CJ
8.0607DA	8.0607DB	8.0607DC	8.0607DD	8.0607DE	8.0607DF	8.0607DG	8.0607DH	8.0607DI	8.0607DJ
8.0607EA	8.0607EB	8.0607EC	8.0607ED	8.0607EE	8.0607EF	8.0607EG	8.0607EH	8.0607EI	8.0607EJ
8.0607FA	8.0607FB	8.0607FC	8.0607FD	8.0607FE	8.0607FF	8.0607FG	8.0607FH	8.0607FI	8.0607FJ
8.0607GA	8.0607GB	8.0607GC	8.0607GD	8.0607GE	8.0607GF	8.0607GG	8.0607GH	8.0607GI	8.0607GJ
8.0607HA	8.0607HB	8.0607HC	8.0607HD	8.0607HE	8.0607HF	8.0607HG	8.0607HH	8.0607HI	8.0607HJ
8.0607IA	8.0607IB	8.0607IC	8.0607ID	8.0607IE	8.0607IF	8.0607IG	8.0607IH	8.0607II	8.0607IJ
8.0607JA	8.0607JB	8.0607JC	8.0607JD	8.0607JE	8.0607JF	8.0607JG	8.0607JH	8.0607JI	8.0607JJ

И. П. 001420 20200506

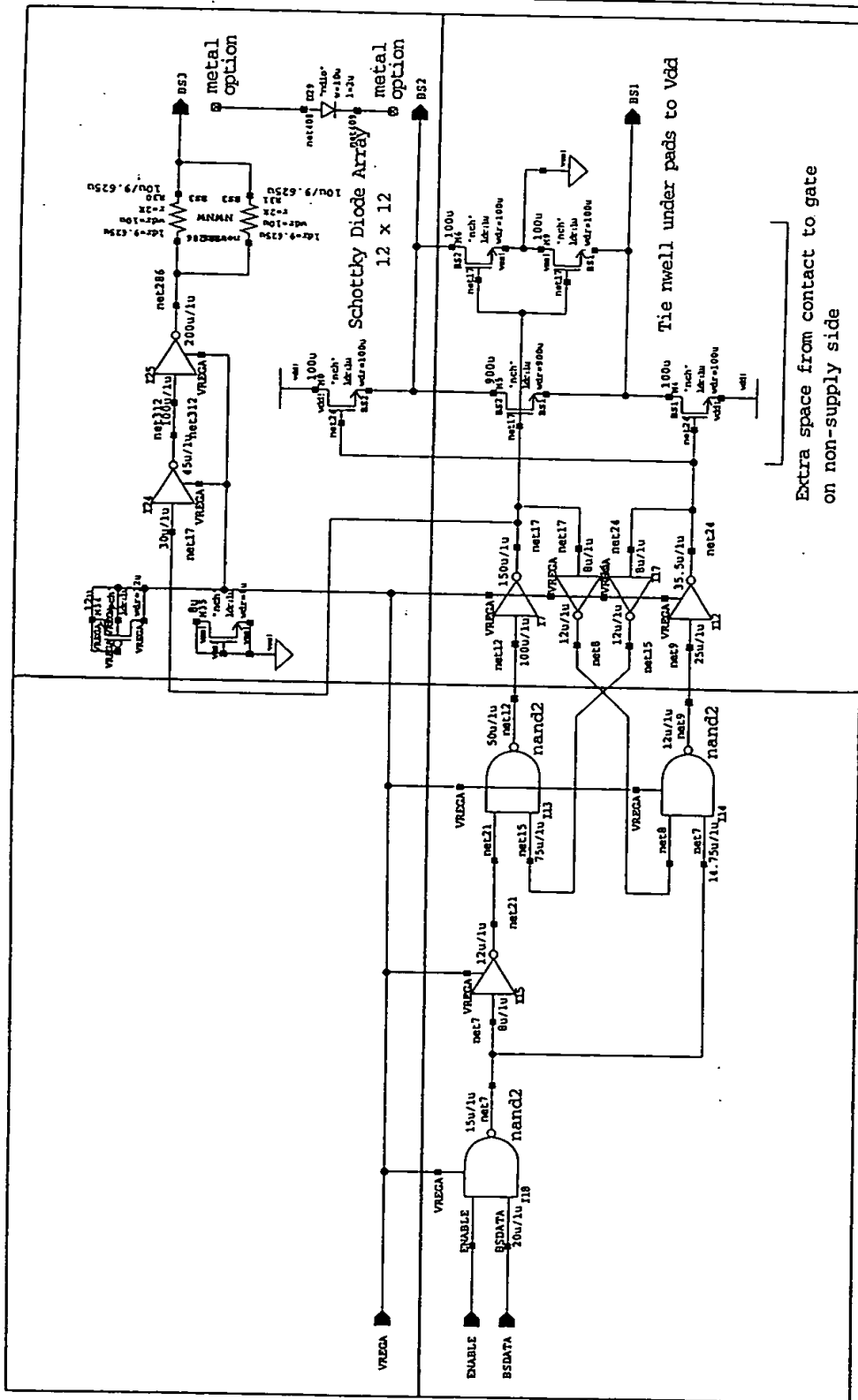
[illegible]

Fig. 8.0607

007420 20200500

8.0608AA	8.0608AB
8.0608BA	8.0608BB

II II BB.0608BB



MICRON		PROJECT: L03	DESIGN: J0700LE
COMMUNICATIONS, INC.		TITLE: Modulated Backscatter	
INTEGRATED CIRCUIT DESIGN		TRANSMITTER	
CONFIDENTIAL INFORMATION		NAME: 103reva/txmb	REV: B10
		DATE: Mar 26 11:07:42 1996	REV: A

B8: initial release of this design
B10: disconnected inverter

007420 20320500

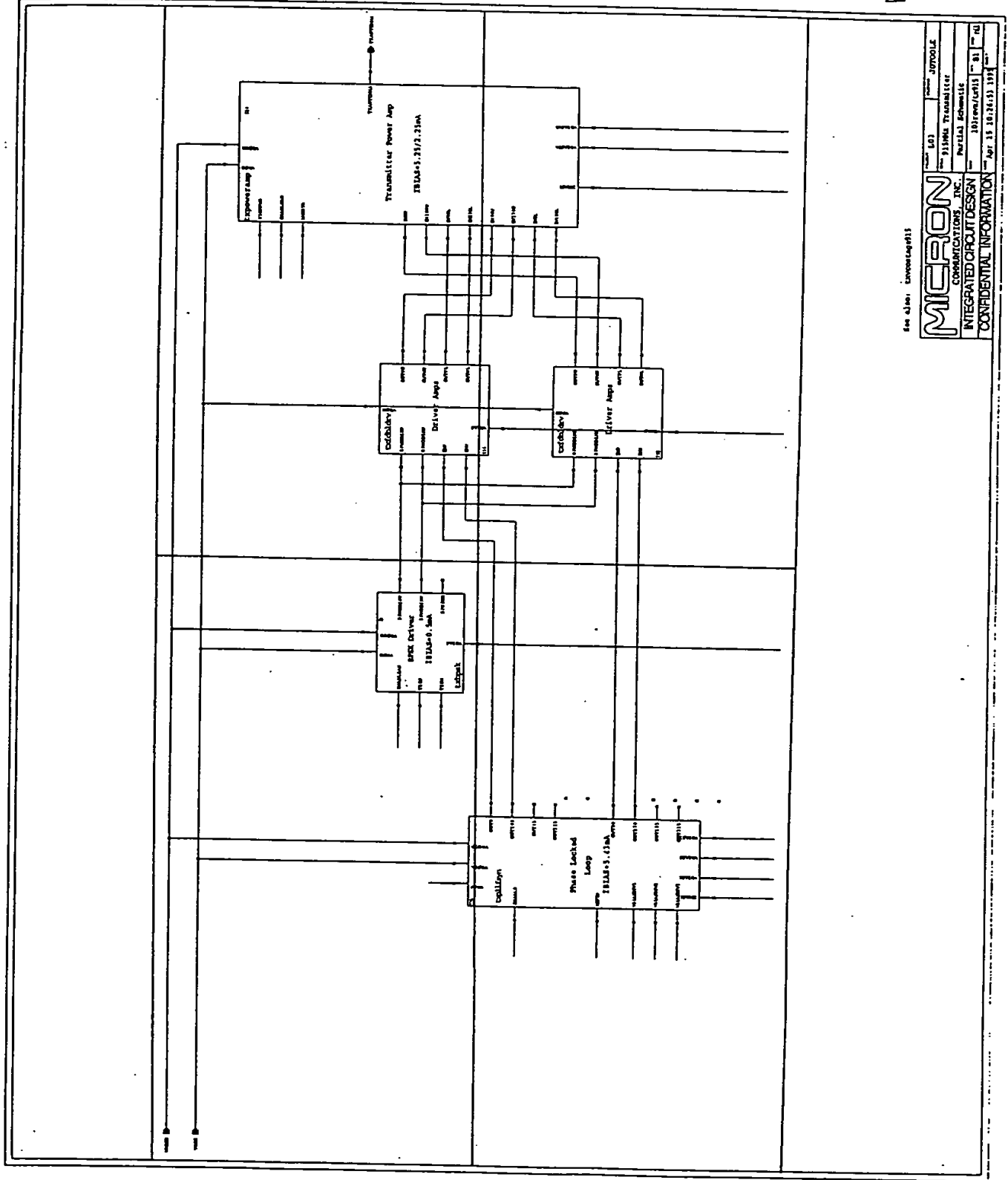
MI40-030

8.07AA	8.07AB
8.07BA	8.07BB

II II 88.007

007420 24320500

FIG. 8.07



See Also: Encapsulated

MICRON		LOG	JUTOLE
COMMUNICATIONS, INC.		9110A Transmitter	
INTEGRATED CIRCUIT DESIGN		Partial Schematic	
CONFIDENTIAL INFORMATION		107420/011	81
		Apr 13 1974	111

DD FORM 128 2-69

MI40-030

8.0701AA	8.0701AB
8.0701BA	8.0701BB
8.0701CA	8.0701CB

II II II II II II

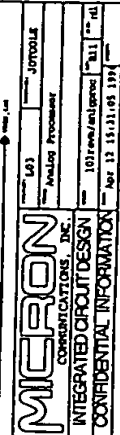
00140-030

MI40-030

9AA	9AB
9BA	9BB
9CA	9CB

1111

Fig. 9



9.01AA	9.01AB	9.01AC	9.01AD	9.01AE	9.01AF	9.01AG	9.01AH
9.01BA	9.01BB	9.01BC	9.01BD	9.01BE	9.01BF	9.01BG	9.01BH
9.01CA	9.01CB	9.01CC	9.01CD	9.01CE	9.01CF	9.01CG	9.01CH
9.01DA	9.01DB	9.01DC	9.01DD	9.01DE	9.01DF	9.01DG	9.01DH

\dot{q} 

WICBOON

106.913

9.0101AA	9.0101AB	9.0101AC	9.0101AD	9.0101AE	9.0101AF	9.0101AG	9.0101AH	9.0101AI	9.0101AJ	9.0101AK
9.0101BA	9.0101BB	9.0101BC	9.0101BD	9.0101BE	9.0101BF	9.0101BG	9.0101BH	9.0101BI	9.0101BJ	9.0101BK
9.0101CA	9.0101CB	9.0101CC	9.0101CD	9.0101CE	9.0101CF	9.0101CG	9.0101CH	9.0101CI	9.0101CJ	9.0101CK

00000000 00000000

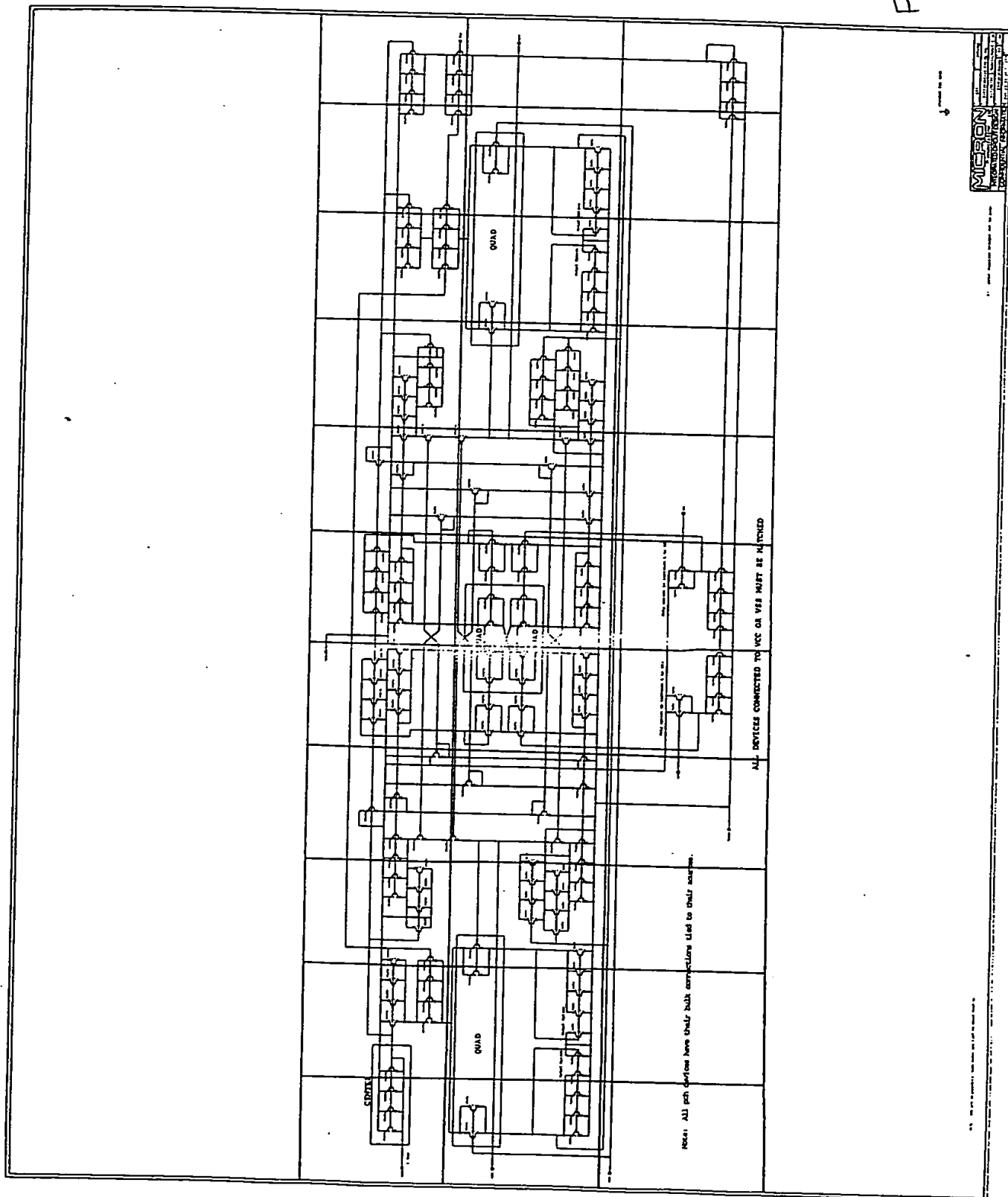
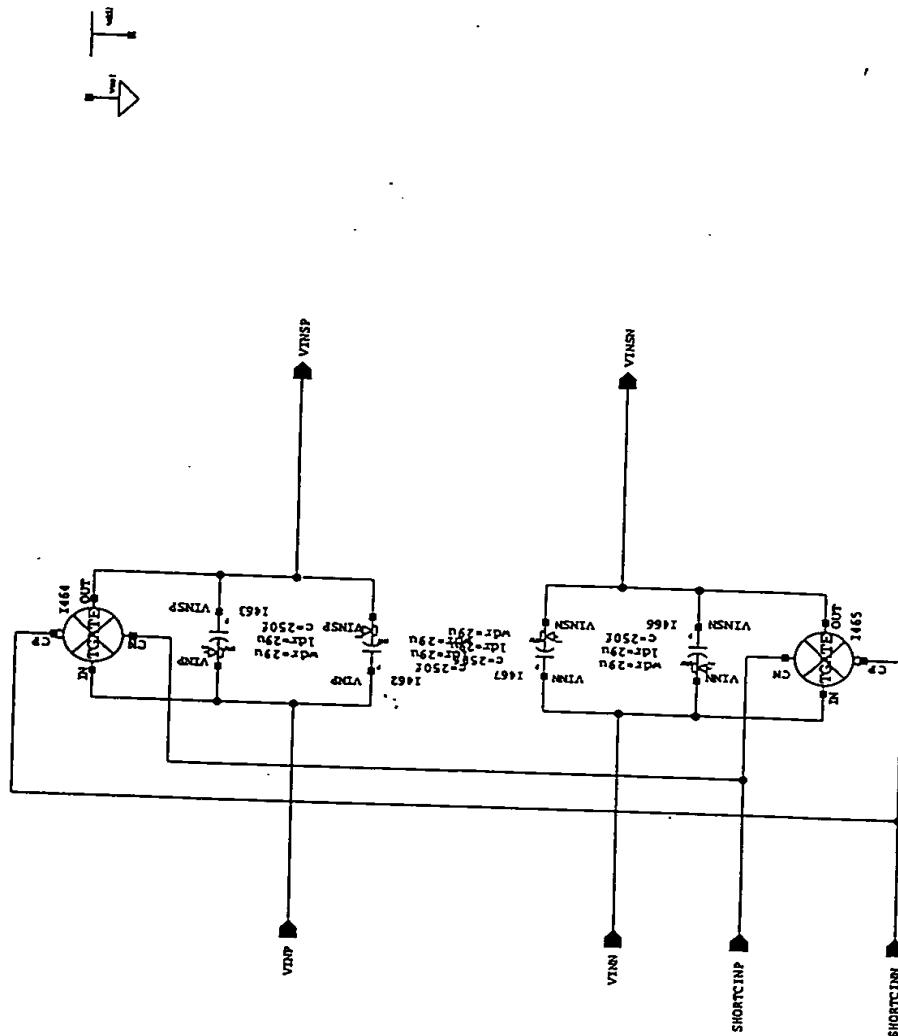


FIG. 9.0101

004420 20920500



MICRON				DESIGNER: JOTOOLE	
PROJECT: L03					
TITLE: Analog Divide by 2					
NAME: j03reva/adaprescale		REV: B1	SHEET: A		
DATE: May 19 16:34:53 1995		SHEET:			
MICRON COMMUNICATIONS, INC.					
INTEGRATED CIRCUIT DESIGN					
CONFIDENTIAL INFORMATION					

FIG. 9.0102

007420920543

MI40-030

9.0103BA	9.0103BB	9.0103BC	9.0103BD	9.0103BE	9.0103BF	9.0103BG	9.0103BH	9.0103AJ	9.0103AK	9.0103AL	9.0103AM	9.0103AN	9.0103AO	9.0103AP
								9.0103BJ	9.0103BK	9.0103BL	9.0103BM	9.0103BN	9.0103BO	9.0103BP
9.0103CA	9.0103CB	9.0103CC	9.0103CD	9.0103CE	9.0103CF	9.0103CG	9.0103CH	9.0103CI	9.0103CJ	9.0103CL	9.0103CM	9.0103CN	9.0103CO	9.0103CP
9.0103DA	9.0103DB	9.0103DC	9.0103DD	9.0103DE	9.0103DF	9.0103DG	9.0103DH	9.0103DI	9.0103DJ	9.0103DL	9.0103DM	9.0103DN	9.0103DO	9.0103DP
9.0103EA	9.0103EB	9.0103EC	9.0103ED	9.0103EE	9.0103EF	9.0103EG	9.0103EH	9.0103EI	9.0103EJ	9.0103EL	9.0103EM	9.0103EN	9.0103EO	9.0103EP
9.0103FA	9.0103FB	9.0103FC	9.0103FD	9.0103FE	9.0103FF	9.0103FG	9.0103FH	9.0103FI	9.0103FJ	9.0103FL	9.0103FM	9.0103FN	9.0103FO	9.0103FP

MI 40-030

007420 20520500

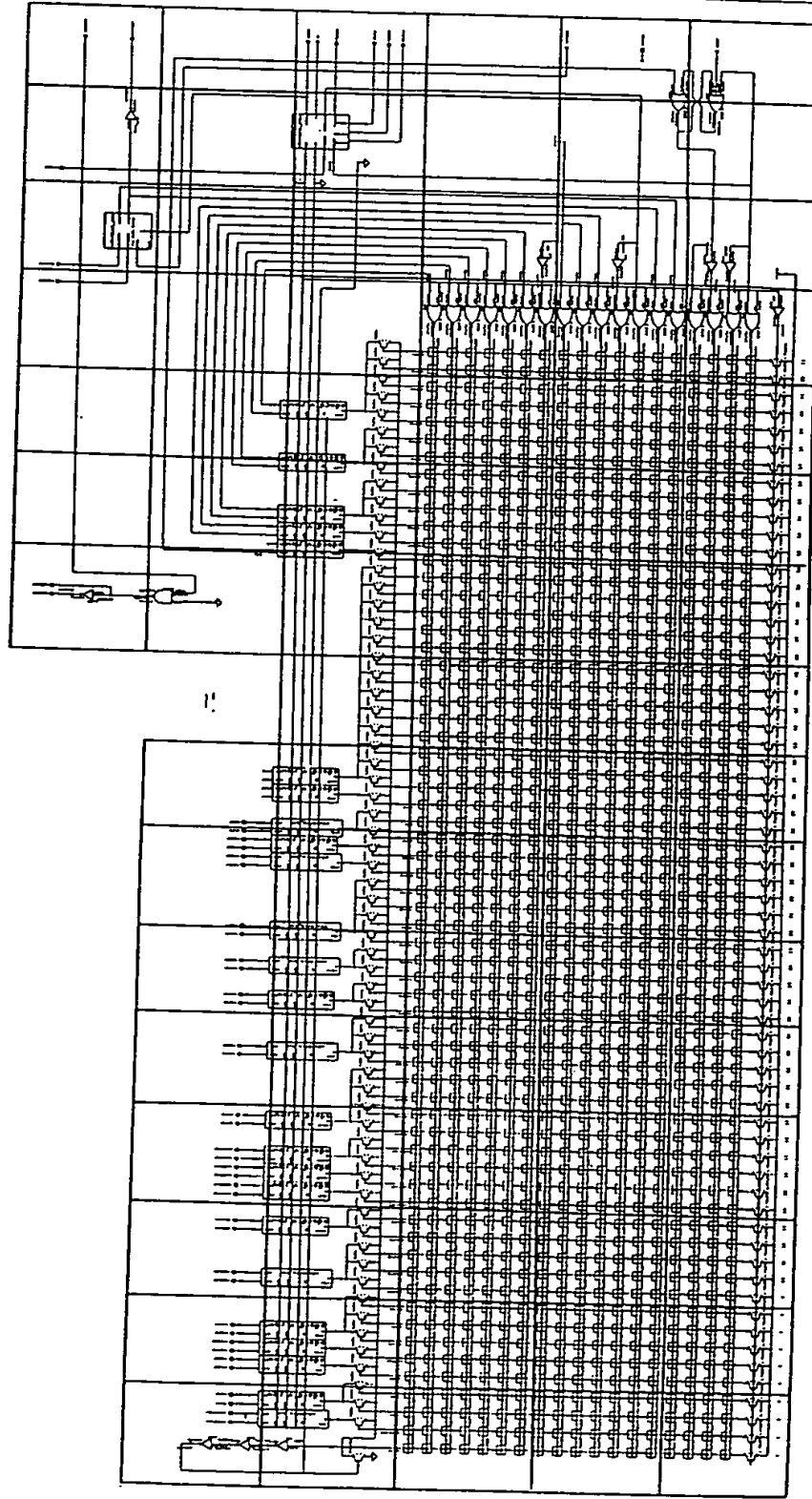


FIG. 9,0103

MICRON
MICROELECTRONICS
CORPORATION
SANTA CLARA, CALIF. 95050

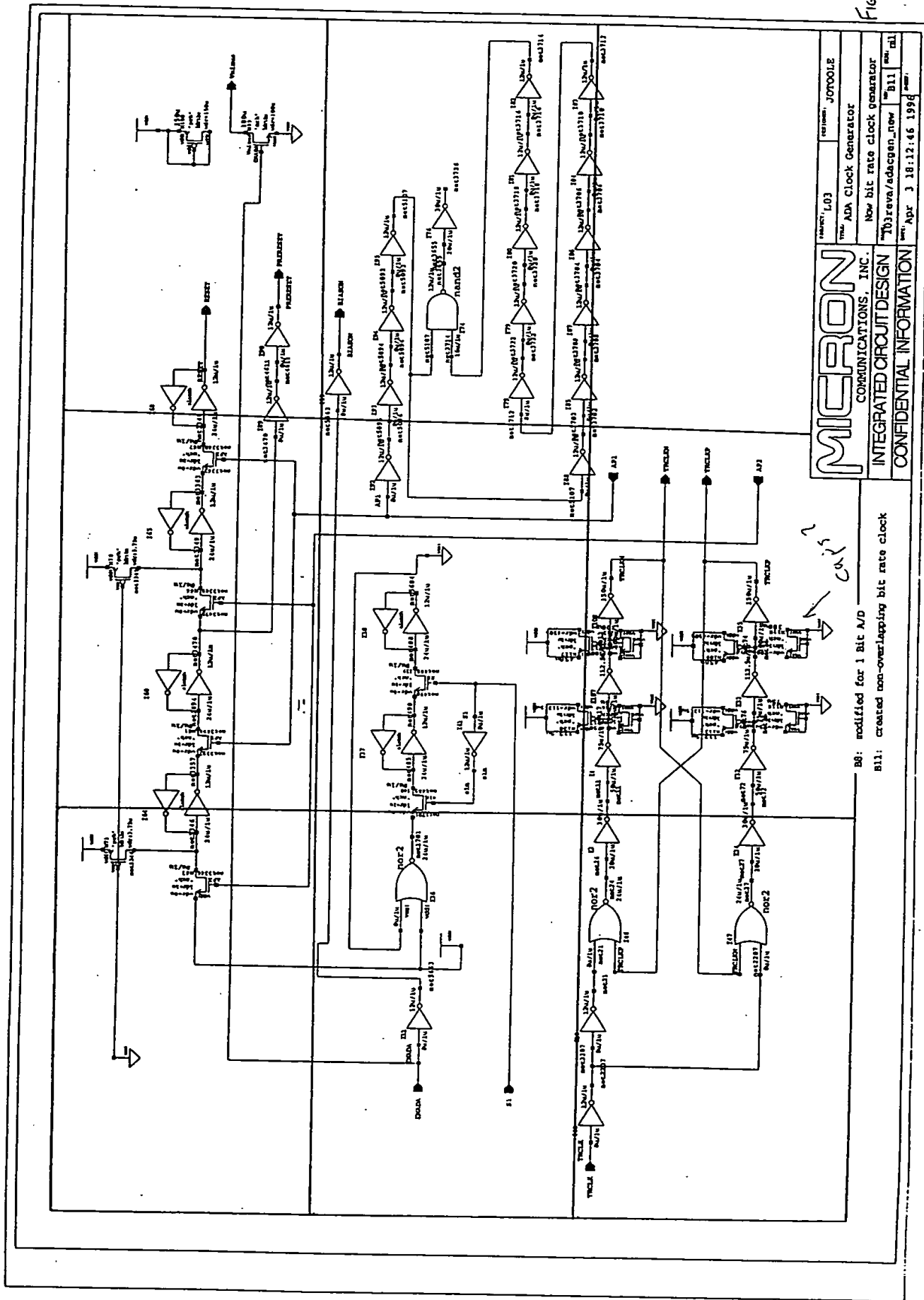
007420-20320500

MI40-030

9.010301AA	9.010301AB	9.010301AC
9.010301BA	9.010301BB	9.010301BC
9.010301CA	9.010301CB	9.010301CC

II II II III III II

001100 20920560

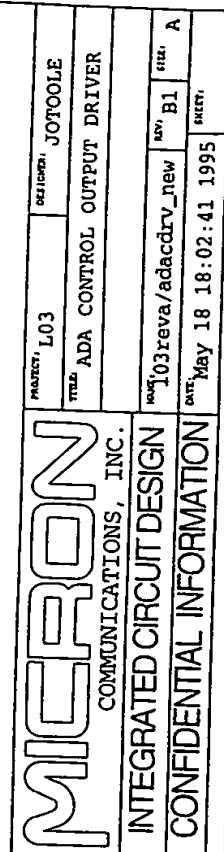


001470-6650563

9.010302AA	9.010302AB
------------	------------

IX 9.010302

FIG. 9.010302



INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

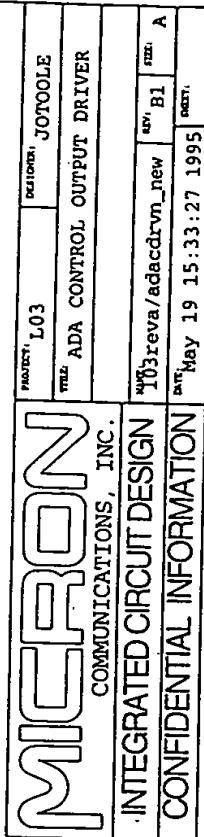
0014700 24920300

MI40-030

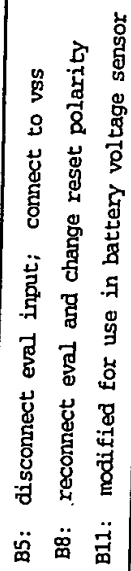
9.010303AA	9.010303AB
------------	------------

И.И.С. 9.01.03.03

FIG. 9.010303



9.010304AA	9.010304AB
9.010304BA	9.010304BB



MICRON		PROJECT: L03		SECTION: J07000LE	
		TITLE: ADA Data Latch			
INTEGRATED CIRCUIT DESIGN		NAME: l03reva/adadlat_new		REV: Bill	A
CONFIDENTIAL INFORMATION		DATE: Apr 8 10:39:12 1996		SHEET:	

150000 500000

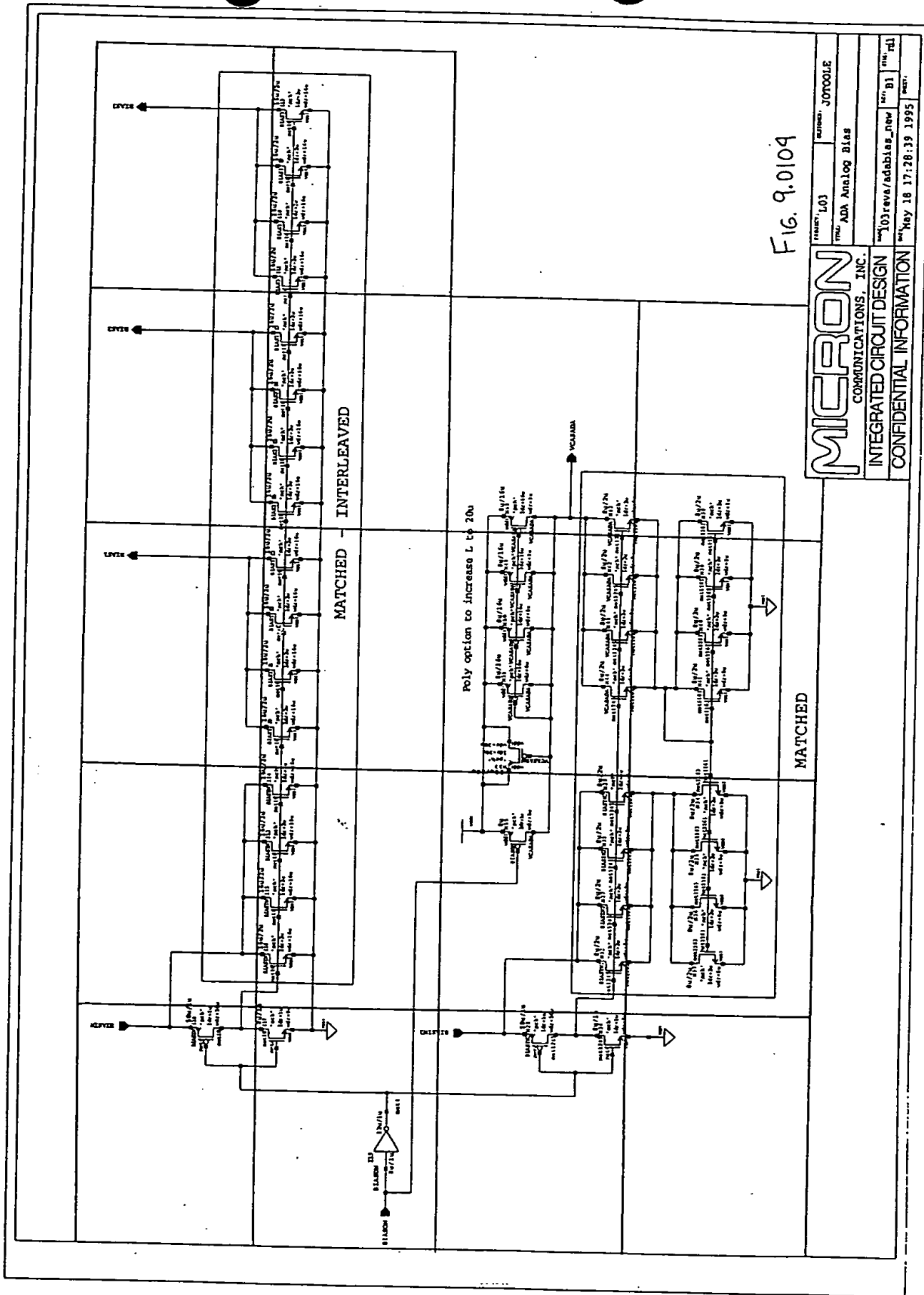


Fig. 9.0104

MICRON		PROJECT: L03	DATE: J07000LE
COMMUNICATIONS, INC.		FILE: ADA Analog Bias	
INTEGRATED CIRCUIT DESIGN		REV: 103.reva/ada/bias_new	REV: B1
CONFIDENTIAL INFORMATION		DATE: May 18 17:28:39 1995	DESIGNER:

[illegible]

MI40-030

9.02AA	9.02AB	9.02AC	9.02AD	9.02AE	9.02AF	9.02AG	9.02AH	9.02AI	9.02AJ	9.02AK
9.02BA	9.02BB	9.02BC	9.02BD	9.02BE	9.02BF	9.02BG	9.02BH	9.02BI	9.02BJ	9.02BK
9.02CA		9.02CC	9.02CD	9.02CE	9.02CF	9.02CG	9.02CH	9.02CI	9.02CJ	9.02CK
9.02DA	9.02DB	9.02DC	9.02DD		9.02DF	9.02DG	9.02DH	9.02DI	9.02DJ	9.02DK

20.6 6.11

001720" 20920900

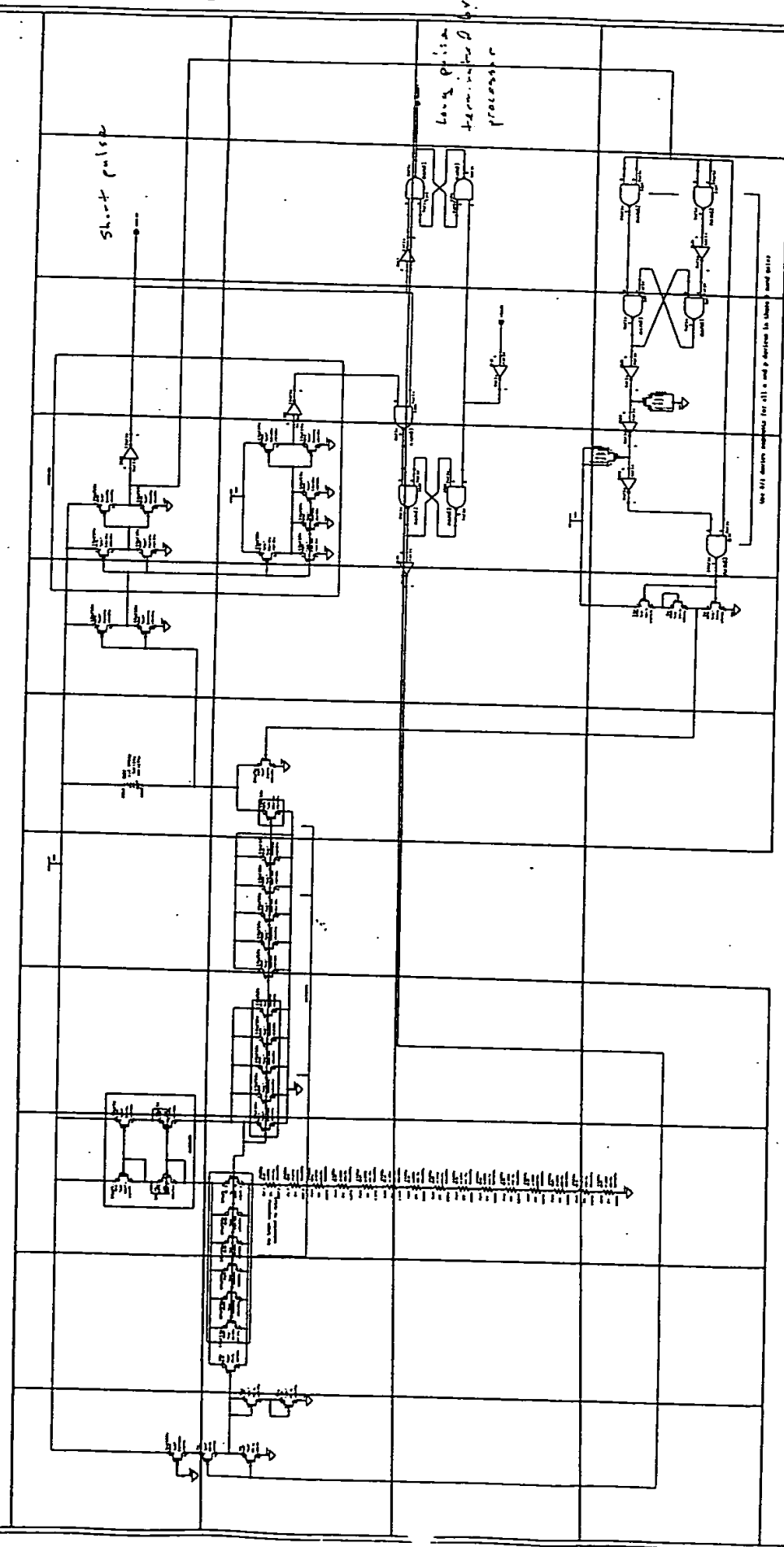
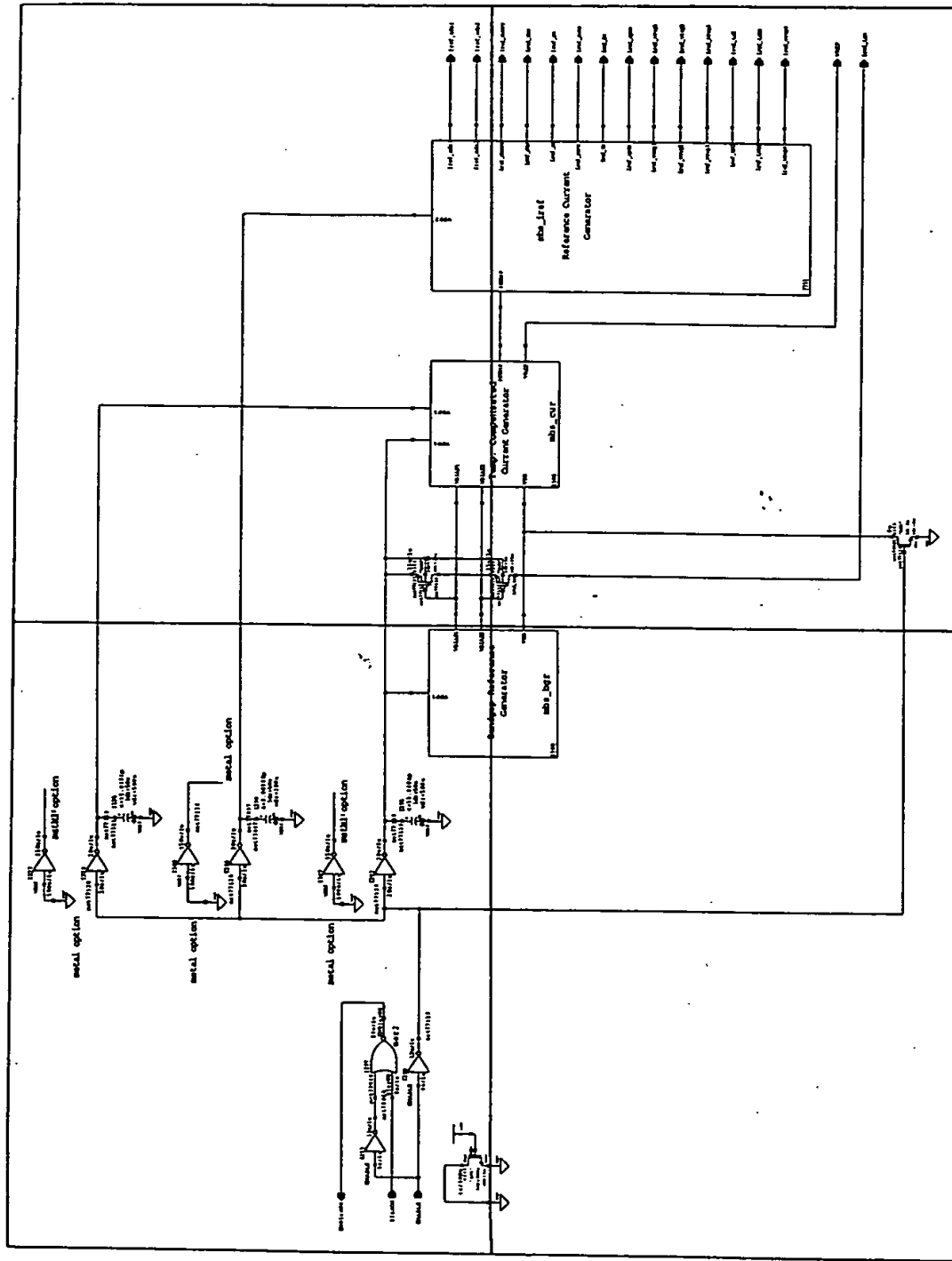


FIG. 9.02

MICRON	
COMMUNICATIONS, INC.	INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION	CONFIDENTIAL INFORMATION

9.03AA	9.03AB
9.03BA	9.03BB

[illegible]

82: deleted TESTNO function
added DISBOX logic
created buffered VLDY

micron
COMMUNICATIONS, INC.

U COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN

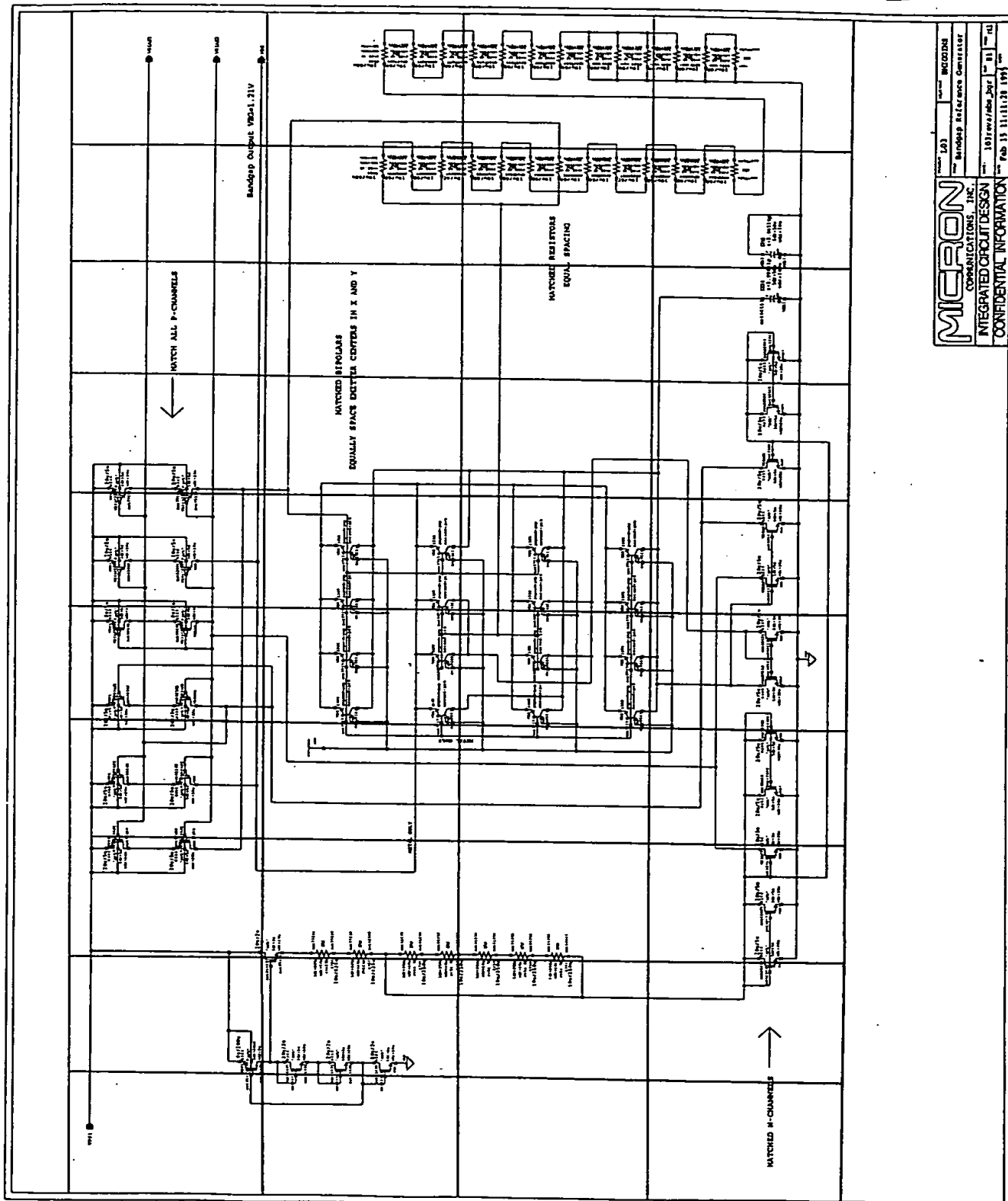
INTEGRATED CIRCUIT DESIGN

~~CONFIDENTIAL INFORMATION~~

DATE	TIME	LOCATION
01	01	01
Master Map Source		
DATE	TIME	LOCATION
01	01	01
Master Map Source		

9.0301AA	9.0301AB	9.0301AC	9.0301AD	9.0301AE	9.0301AF	9.0301AG	9.0301AH	9.0301AI	9.0301AJ
9.0301BA	9.0301BB	9.0301BC	9.0301BD	9.0301BE	9.0301BF	9.0301BG	9.0301BH	9.0301BI	9.0301BJ
	9.0301CB	9.0301CC	9.0301CD	9.0301CE	9.0301CF	9.0301CG	9.0301CH	9.0301CI	9.0301CJ
	9.0301DB	9.0301DC	9.0301DD	9.0301DE	9.0301DF	9.0301DG	9.0301DH	9.0301DI	9.0301DJ

FIG. 9.0301



9.030200 9.030201 9.030202

MI40-030

9.0302AA	9.0302AB	9.0302AC	9.0302AD	9.0302AE	9.0302AF	9.0302AG	9.0302AH	9.0302AI	9.0302AJ
9.0302BA	9.0302BB	9.0302BC	9.0302BD	9.0302BE	9.0302BF	9.0302BG	9.0302BH	9.0302BI	9.0302BJ
		9.0302CC	9.0302CD	9.0302CE	9.0302CF	9.0302CG	9.0302CH	9.0302CI	9.0302CJ
						9.0302DG	9.0302DH	9.0302DI	

9.030200 9.030201 9.030202

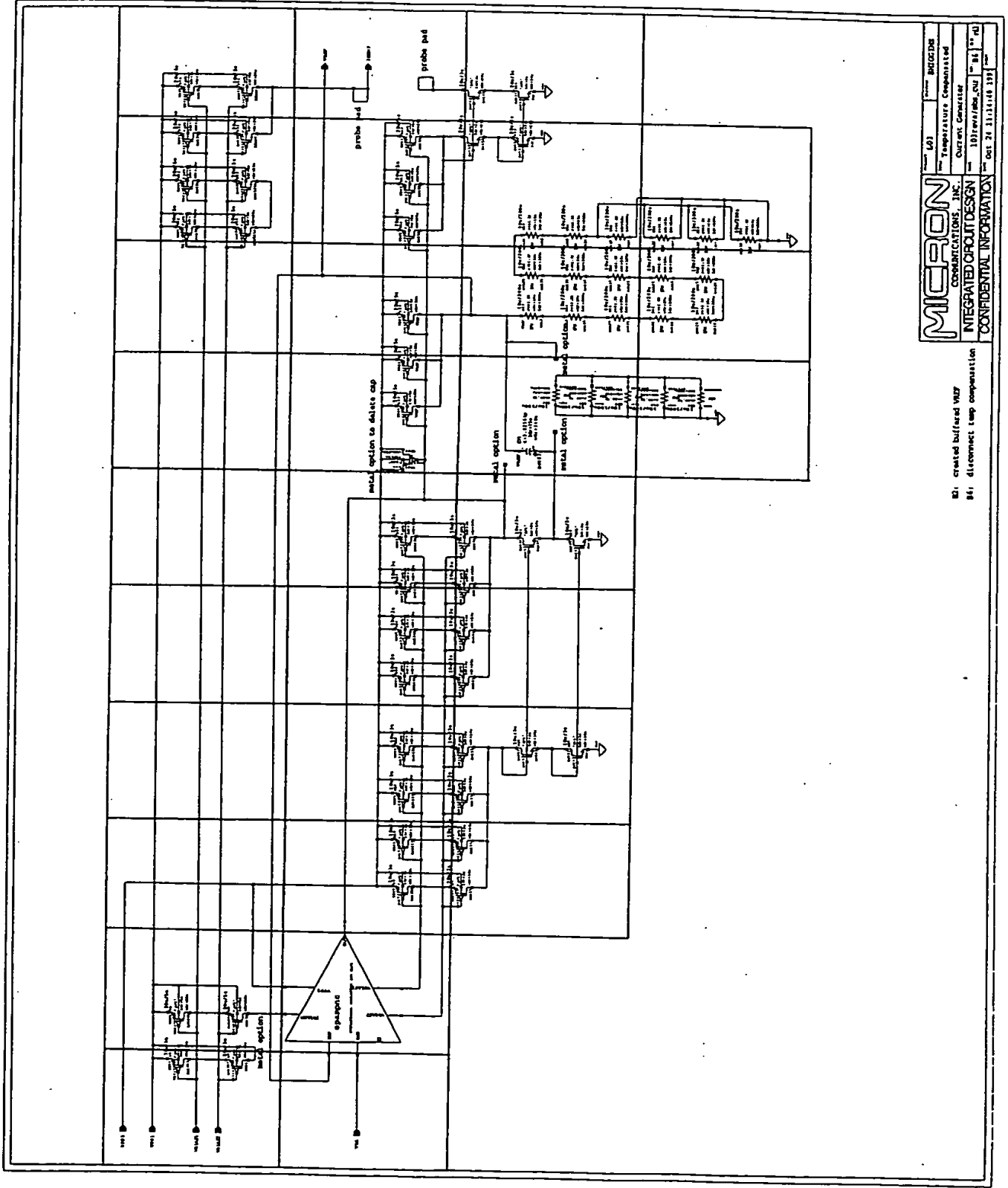


FIG. 9.030a

MICRON	
COMMUNICATIONS, INC.	TEMPERATURE COMPENSATED
INTEGRATED CIRCUIT DESIGN	CURRENT COMPENSATOR
CONFIDENTIAL INFORMATION	10/19/80, Rev. 1.0
	OUT 24 1114148 198

82: created buffered VDD
84: disconnect temp compensation

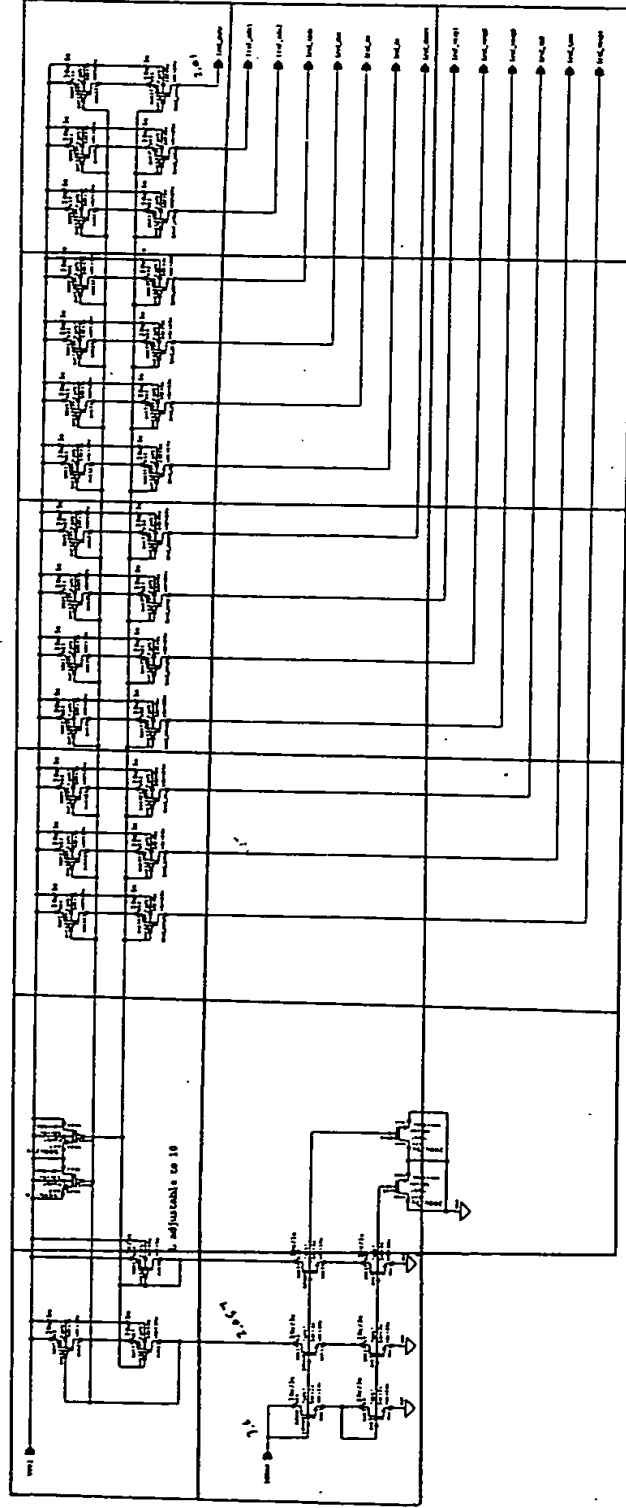


9.0303AA	9.0303AB	9.0303AC	9.0303AD	9.0303AE	9.0303AF
9.0303BA	9.0303BB	9.0303BC	9.0303BD	9.0303BE	9.0303BF
	9.0303CB	9.0303CC	9.0303CD	9.0303CE	9.0303CF

LEWIS & CLARK

001120" 20920500

FIG. 9.0303



MICRON		LA3	BUILDING
INTEGRATED CIRCUIT DESIGN		Reference Circuit	
CONFIDENTIAL INFORMATION		Copyright	
		3017000/000_1001	Rev. B
		Apr 21 11:15:21 1995	

007720" 20920300

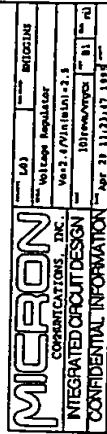
MI40-030

9.04AA	9.04AB	9.04AC	9.04AD	9.04AE
9.04BA	9.04BB	9.04BC	9.04BD	9.04BE
9.04CA	9.04CB	9.04CC	9.04CD	9.04CE

11 11 11 9.0400

9.05AA	9.05AB	9.05AC	9.05AD	9.05AE
9.05BA	9.05BB	9.05BC	9.05BD	9.05BE
9.05CA	9.05CB	9.05CC	9.05CD	9.05CE
9.05DA	9.05DB	9.05DC	9.05DD	9.05DE
9.05EA	9.05EB	9.05EC	9.05ED	9.05EE
9.05FA	9.05FB	9.05FC	9.05FD	9.05FE

Fig. 9.05



9.0501AA	9.0501AB	9.0501AC	9.0501AD
9.0501BA	9.0501BB	9.0501BC	9.0501BD
9.0501CA	9.0501CB	9.0501CC	9.0501CD

REF ID: A66050

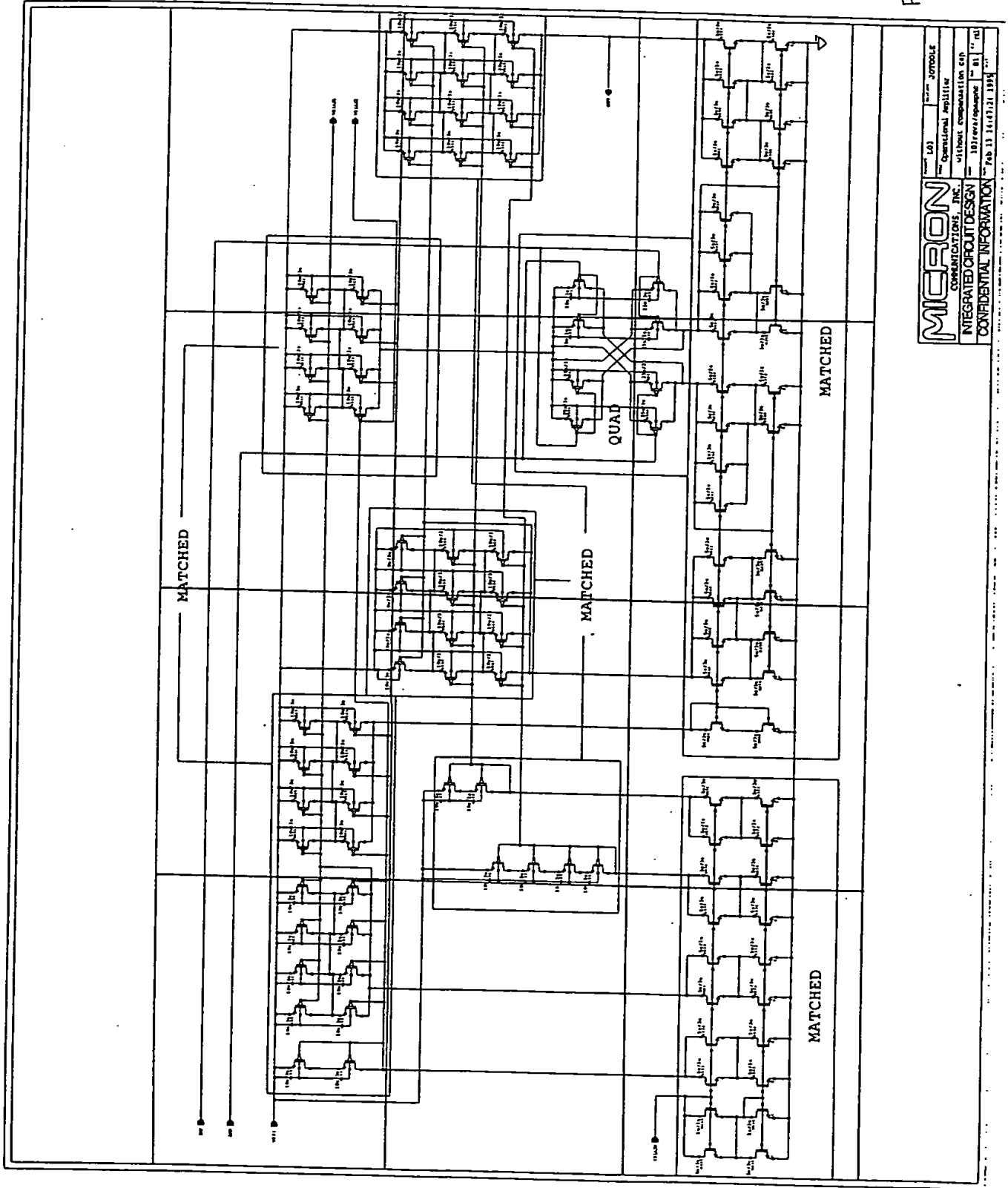
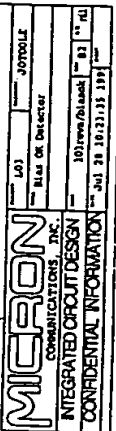


FIG. 9.0501

MICRON		L01	CMOS	4000
CORPORATION, INC.		Operational Amplifier		
INTEGRATED CIRCUIT DESIGN		Without compensation cap		
CONFIDENTIAL INFORMATION		10/rev 1/80		
		Feb 13 11:47:31 1980		

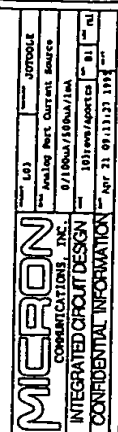
9.06AA	9.06AB	9.06AC	9.06AD	9.06AE
9.06BA	9.06BB	9.06BC	9.06BD	9.06BE
9.06CA	9.06CB	9.06CC	9.06CD	
9.06DA	9.06DB	9.06DC	9.06DD	

Fig. 9.06



9.07AA	9.07AB	9.07AC	9.07AD	9.07AE	9.07AF	9.07AG	9.07AH	9.07AI
9.07BA	9.07BB	9.07BC	9.07BD	9.07BE	9.07BF	9.07BG	9.07BH	9.07BI
9.07CA	9.07CB	9.07CC	9.07CD	9.07CE	9.07CF	9.07CG	9.07CH	
9.07DA	9.07DB	9.07DC	9.07DD	9.07DE	9.07DF	9.07DG		
9.07EA	9.07EB	9.07EC	9.07ED	9.07EE	9.07EF	9.07EG		

Fig. 9.07



<i>9.08AA</i>	<i>9.08AB</i>	<i>9.08AC</i>
<i>9.08BA</i>	<i>9.08BB</i>	<i>9.08BC</i>
<i>9.08CA</i>	<i>9.08CB</i>	<i>9.08CC</i>

II 9.08

F16.9.08



10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	-----

7760101	7760101
---------	---------

Major Multiples Decoder

and logic

101-200-1001

19 10-06-91 1007

STAY COOL IN THE

1

00000000000000000000

MI40-030

9.09AA	9.09AB
9.09BA	9.09BB

IF 11 9.09

Fig. 9.09

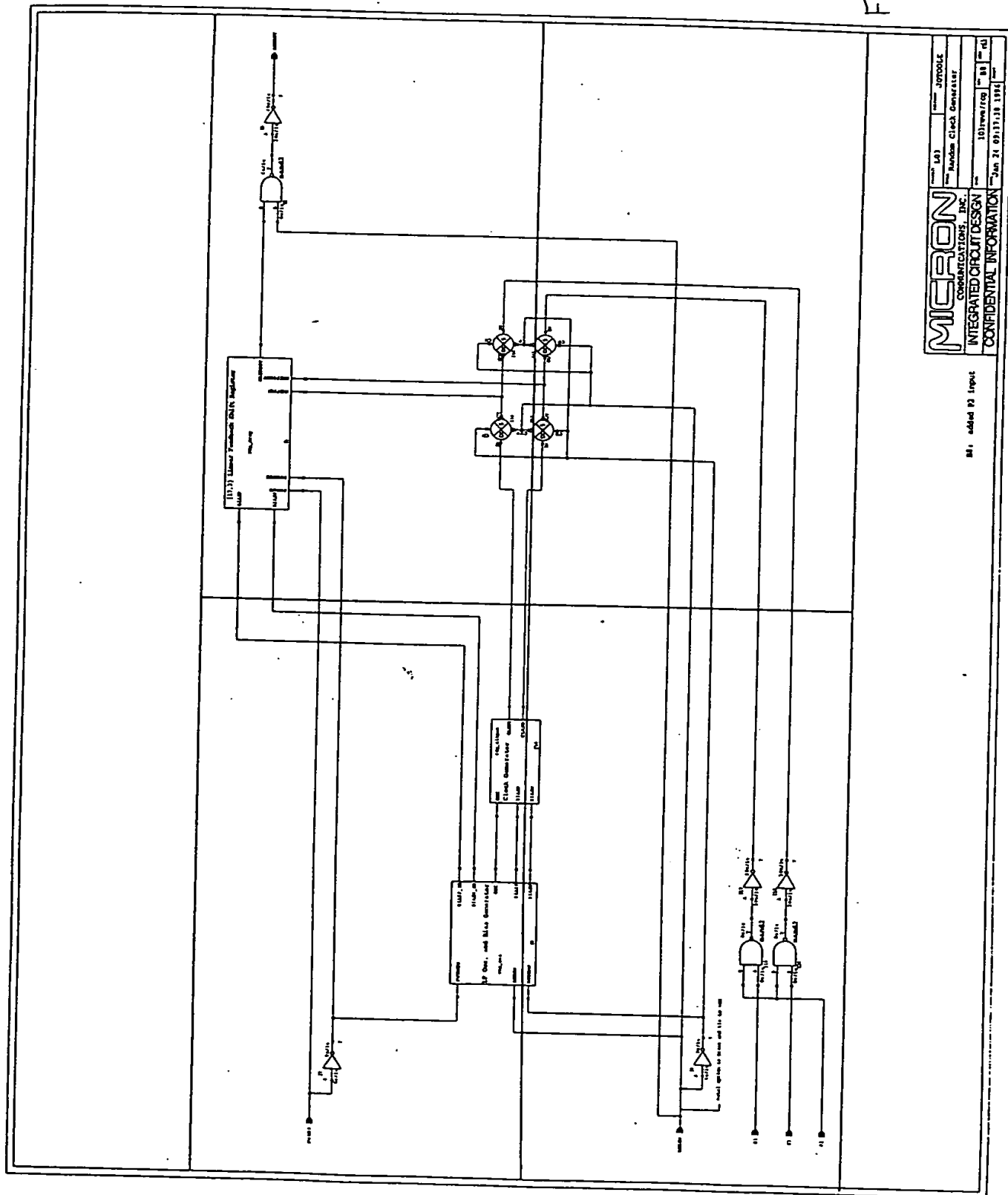


Fig. 9.09

001123 26920500

MI40-030

9.0901AA	9.0901AB	9.0901AC	9.0901AD	9.0901AE	9.0901AF	9.0901AG	9.0901AH
9.0901BA	9.0901BB	9.0901BC	9.0901BD	9.0901BE	9.0901BF	9.0901BG	9.0901BH
9.0901CA	9.0901CB	9.0901CC	9.0901CD	9.0901CE	9.0901CF	9.0901CG	9.0901CH

11 11 11 11 11 11

00141200 20900500

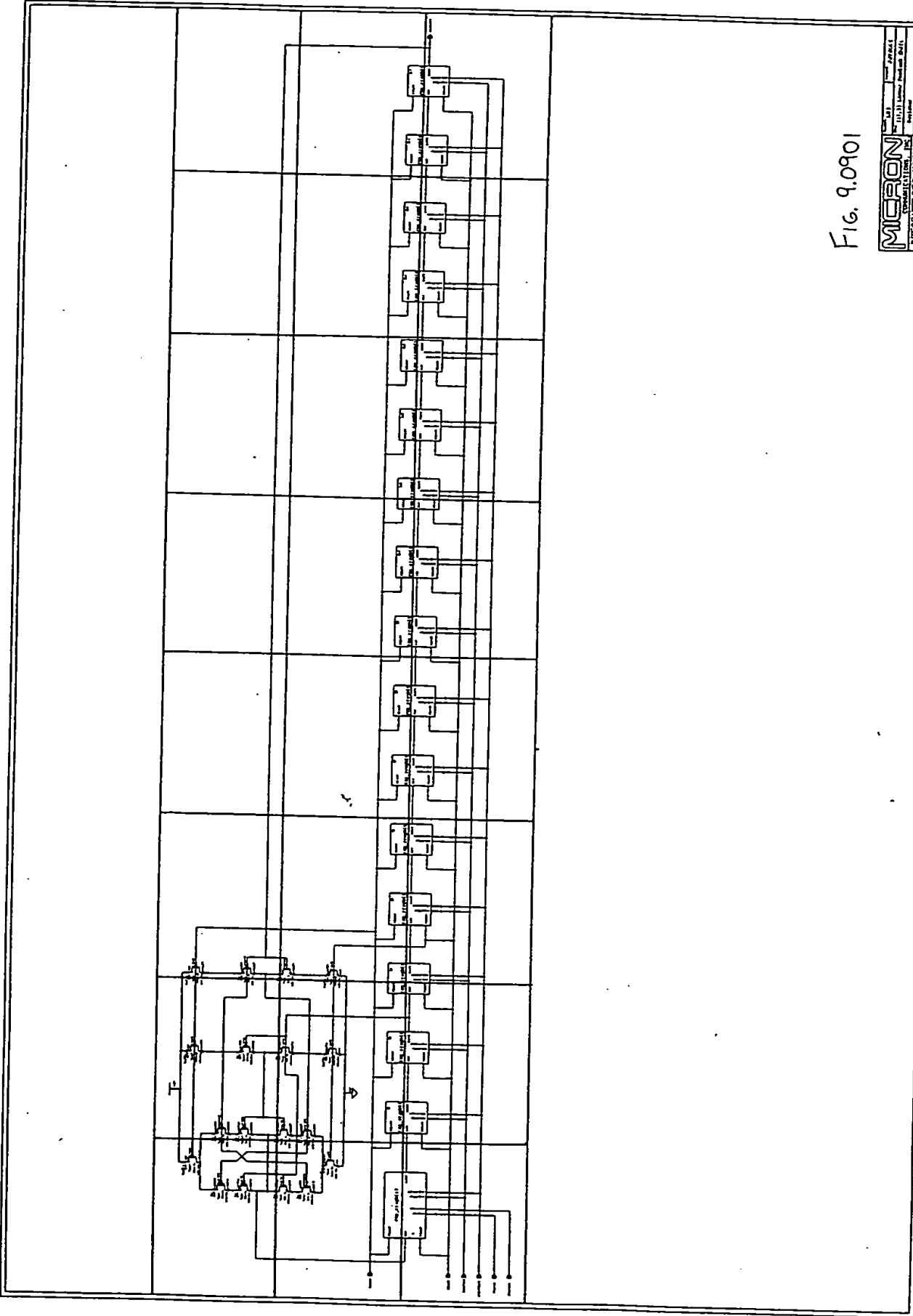


Fig. 9.0901

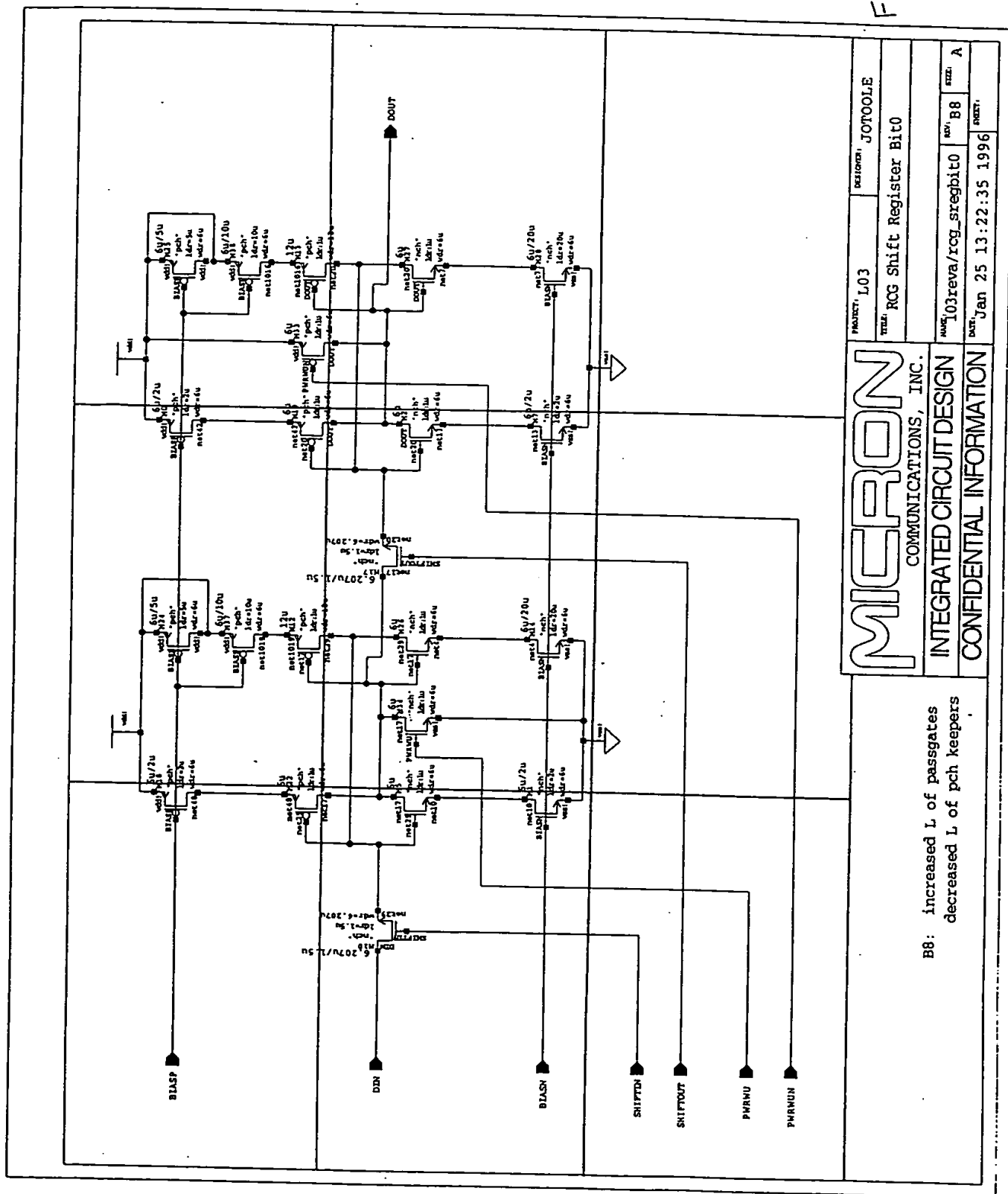
MICRON	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
DATE	APPROVAL
10/11/80	10/11/80
DESIGNED BY	DESIGNED BY
10/11/80	10/11/80
CHECKED BY	CHECKED BY
10/11/80	10/11/80

001123 26520300

MI40-030

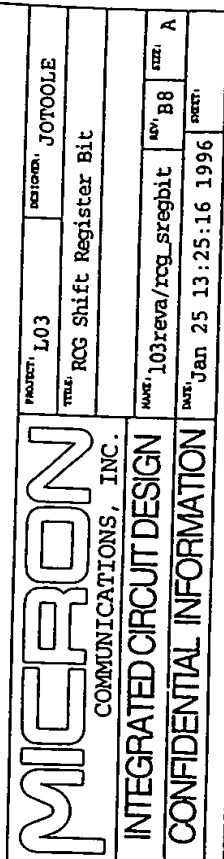
9.090101AA	9.090101AB	9.090101AC
9.090101BA	9.090101BB	9.090101BC
9.090101CA	9.090101CB	9.090101CC

11 11 11 11 11 11 11 11 11 11



9.090102AA	9.090102AB
9.090102BA	9.090102BB

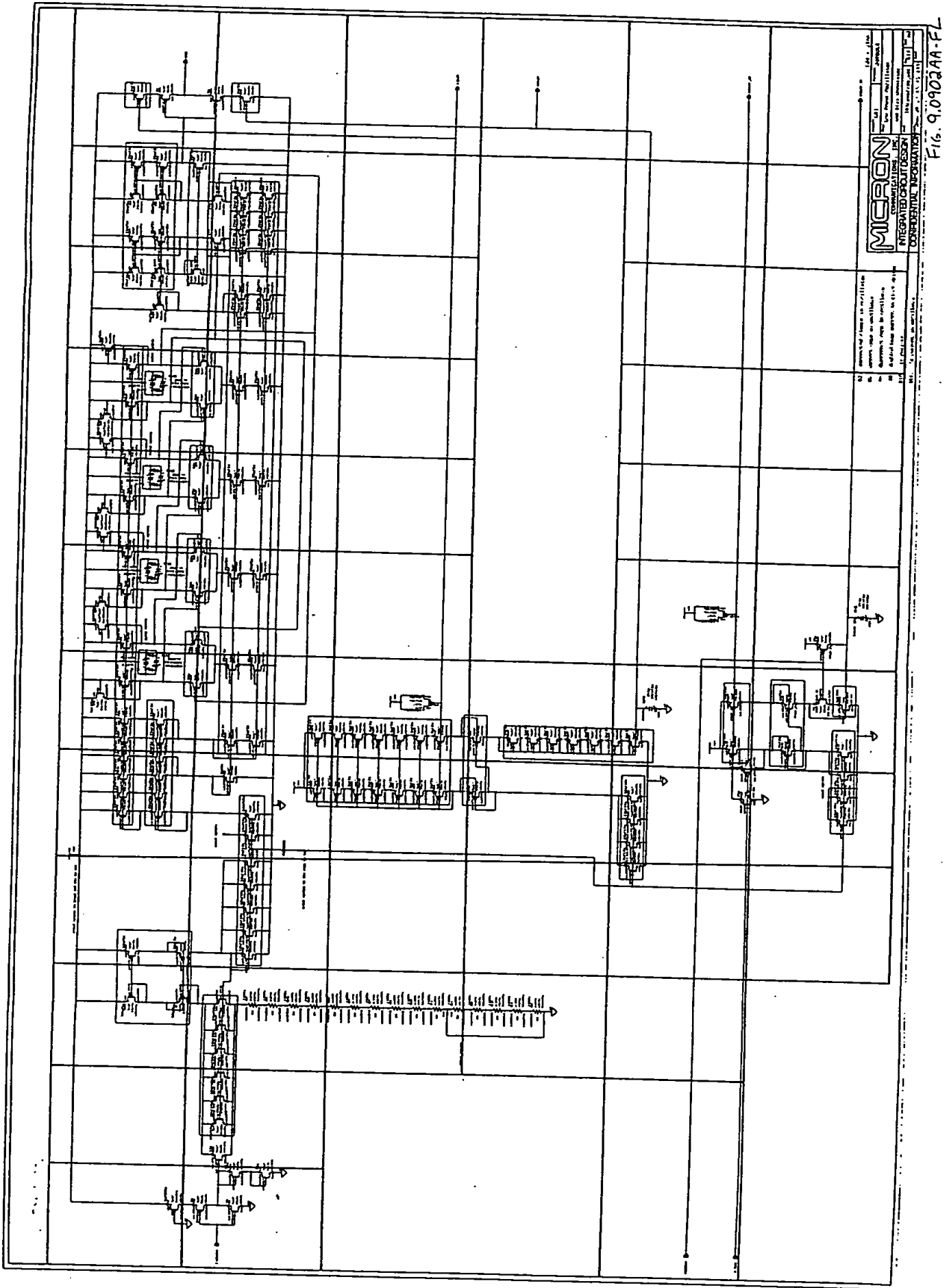
FIG. 9.090102.



**B8: increased L of passgates
decreased L of pch keepers**

9.0902AA	9.0902AB	9.0902AC	9.0902AD	9.0902AE	9.0902F	9.0902AG	9.0902AH	9.0902AI	9.0902AJ	9.0902AK	9.0902AL
9.0902BA	9.0902BB	9.0902BC	9.0902BD	9.0902BE	9.0902F	9.0902BG	9.0902BH	9.0902BI	9.0902BJ	9.0902BK	9.0902BL
		9.0902CC	9.0902CD	9.0902CE	9.0902F	9.0902CG	9.0902CH	9.0902CI	9.0902CJ	9.0902CK	9.0902CL
		9.0902DC	9.0902DD	9.0902DE	9.0902F						9.0902DL
9.0902EA	9.0902EB	9.0902EC	9.0902ED	9.0902EE	9.0902F	9.0902EG	9.0902EH	9.0902EI	9.0902EJ	9.0902EK	9.0902EL
			9.0902FD	9.0902FE	9.0902F	9.0902FG	9.0902FH	9.0902FI	9.0902FJ	9.0902FK	9.0902FL

001420 20920900



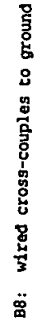
007720 20320000

MI40-030

9.0903AA	9.0903AB	9.0903AC
9.0903BA	9.0903BB	9.0903BC
9.0903CA	9.0903CB	9.0903CC

IF II 9.0903

Fig. 9.0903

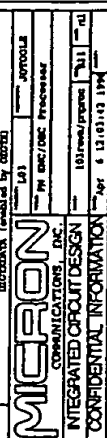


PROJECT, L03	STATUS, JOTOOLE
--------------	-----------------

Serial:	103revA/rcg_clkgen	APR.	B8	FILA.	nil
Date:	Jan 24 09:56:43 1996				

10AA	10AB	10AC	10AD
10BA	10BB	10BC	10BD
10CA	10CB	10CC	10CD
10DA	10DB	10DC	10DD

Fig. 10



ED: delayed OFFER connection to pilotboat

added non-overlapping clocks TCLK and TCLK added MIOCLK input to fpga

10.01AA	10.01AB	10.01AC	10.01AD	10.01AE	10.01AF	10.01AG			
10.01BA	10.01BB	10.01BC	10.01BD	10.01BE	10.01BF	10.01BG	10.01BH	10.01BI	10.01BJ
10.01CA	10.01CB	10.01CC	10.01CD	10.01CE	10.01CF	10.01CG	10.01CH	10.01CI	10.01CJ
	10.01DB	10.01DC	10.01DD	10.01DE	10.01DF	10.01DG	10.01DH	10.01DI	10.01DJ

三

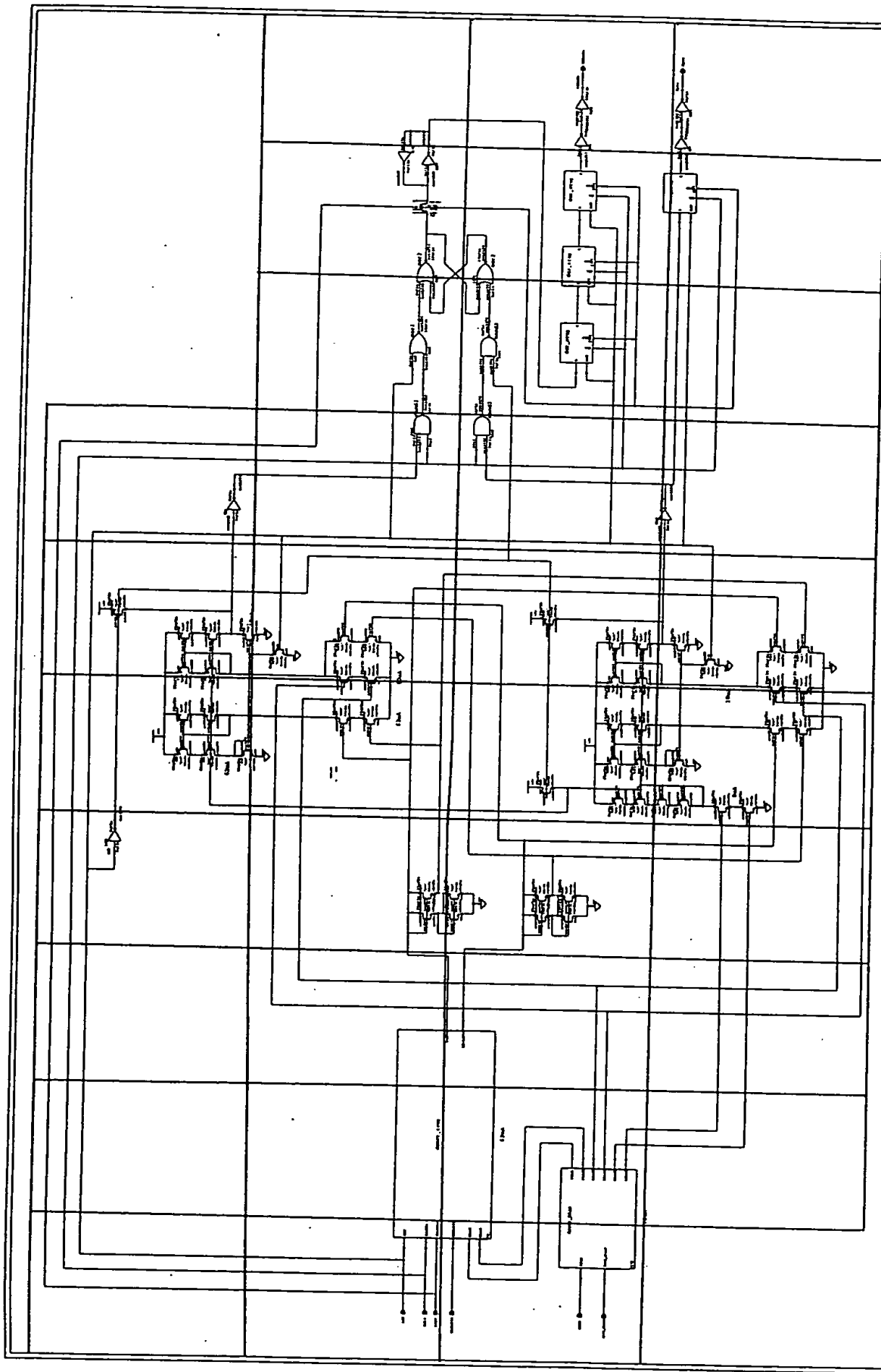


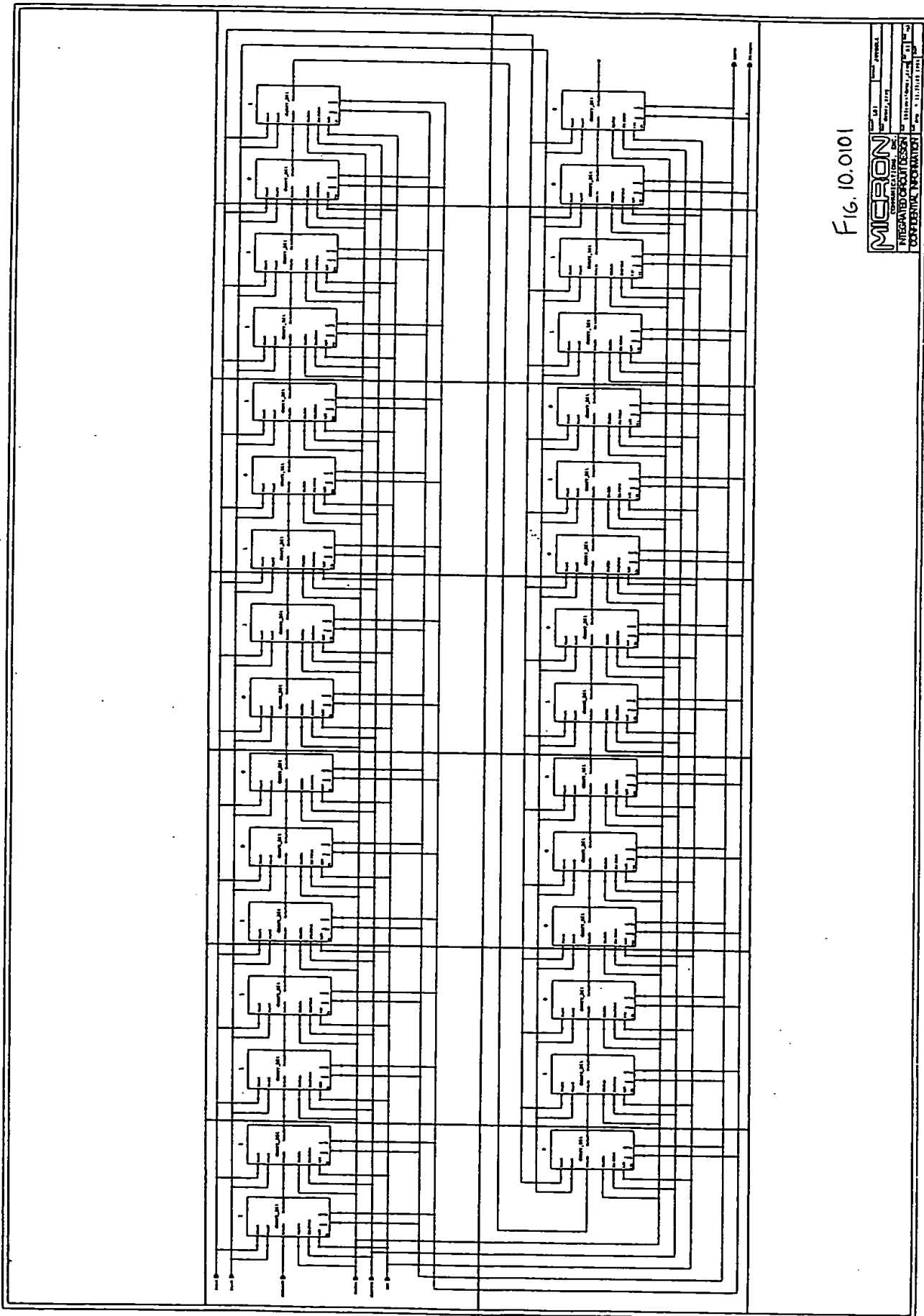
Fig. 10.01

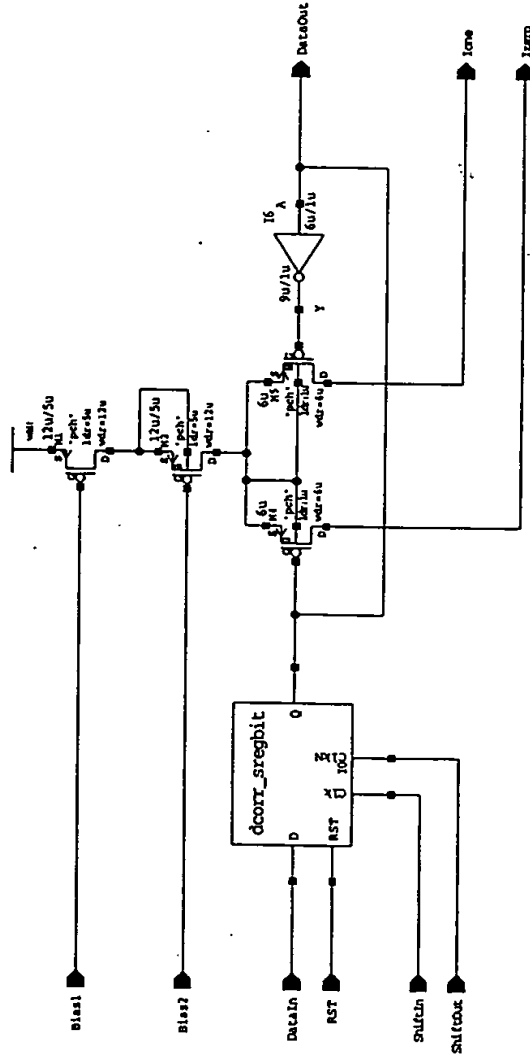
MICRON
CONSULTANTS, INC.
INTEGRATED PROFIT DESIGN
CONFIDENTIAL INFORMATION

10.0101AA	10.0101AB	10.0101AC	10.0101AD	10.0101AE	10.0101AF	10.0101AG
10.0101BA	10.0101BB	10.0101BC	10.0101BD	10.0101BE	10.0101BF	10.0101BG

II II III III III III

001420 20320500





MICRON		PROJECT: L03	REVISION: J0700LE
COMMUNICATIONS, INC.		TITLE: Correlator Bit	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/dcorr_bit	SIZE: A
CONFIDENTIAL INFORMATION		DATE: Sep 9 11:37:26 1994	EXCER:

FIG. 10.010101

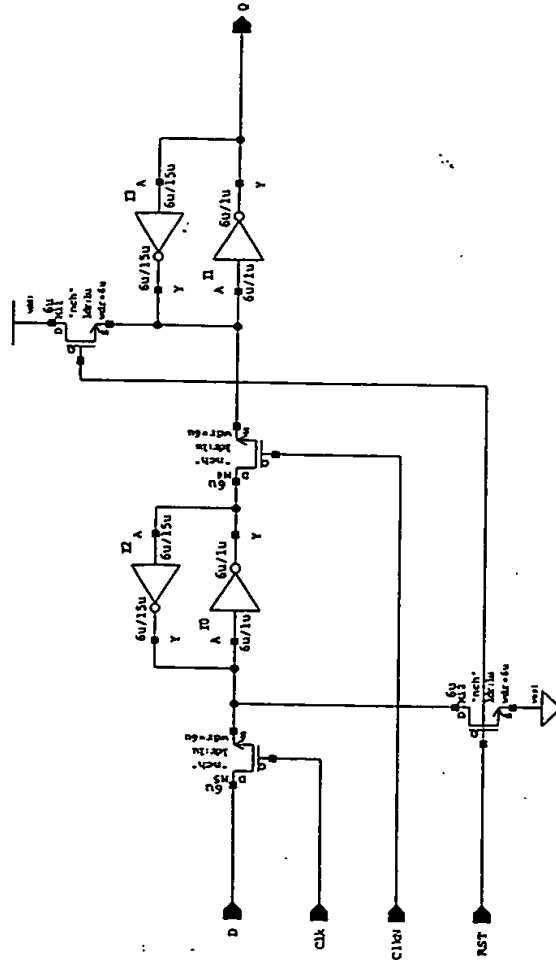


Fig. 10.01010101

MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Shift Register Cell	
INTEGRATED CIRCUIT DESIGN		REV: B1	SIZE: A
CONFIDENTIAL INFORMATION		DATE: Sep 9 14:08:50 1994	REVT:

11.001.002

004420 20320500

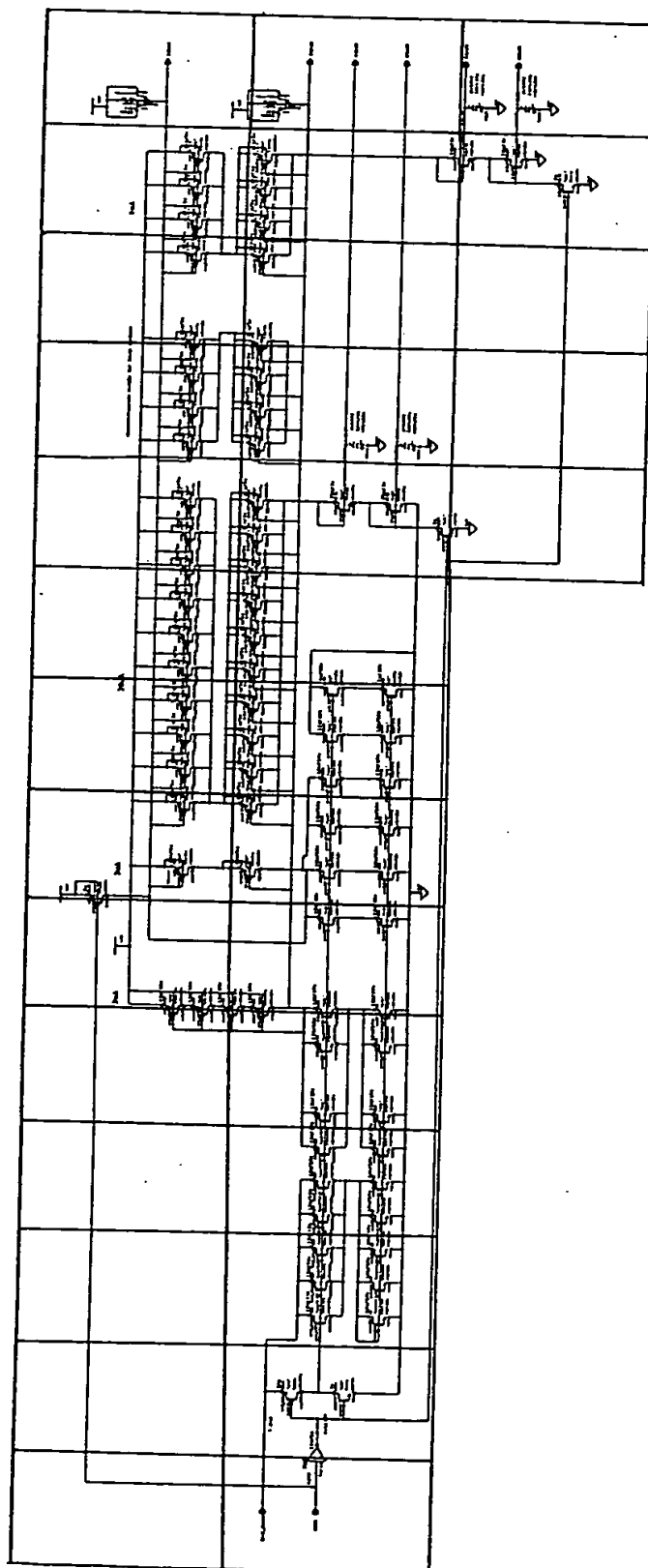


Fig. 10.0102

10.02AA	10.02AB	10.02AC	10.02AD	10.02AE
10.02BA	10.02BB	10.02BC	10.02BD	10.02BE

11.01.002

004720 20920560

Detects preamble by counting 4 consecutive 0's

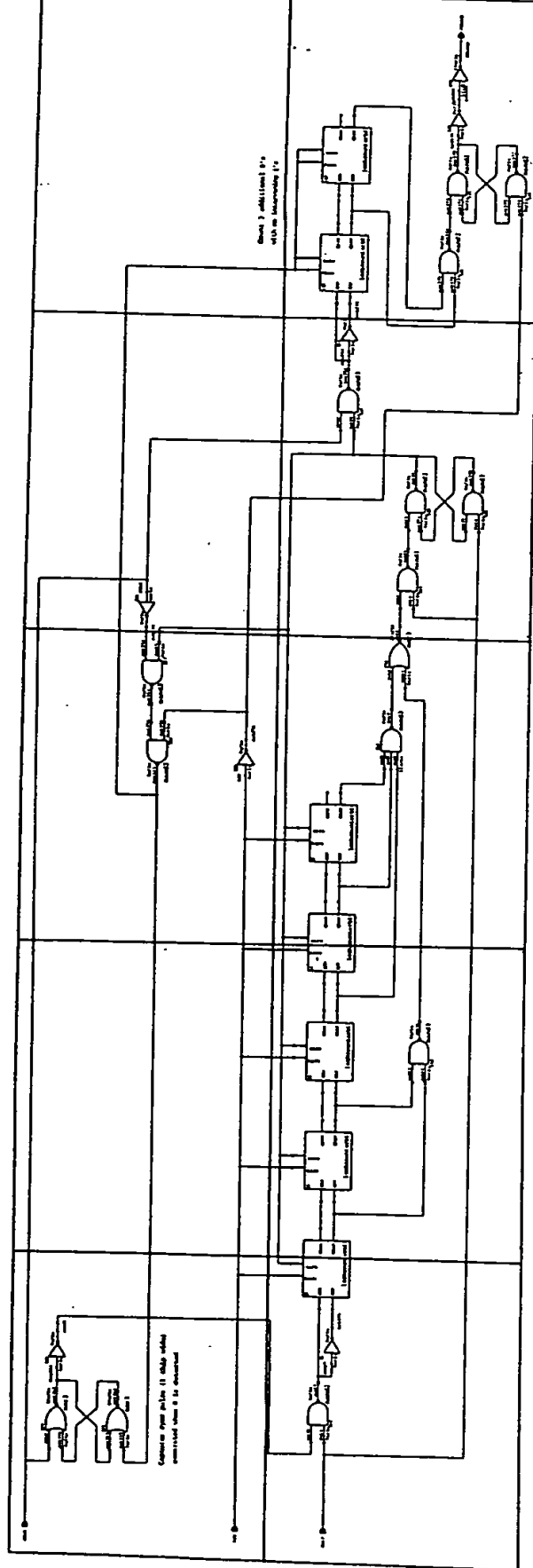


Fig. 10.02

21) Shows the state of the 2 bit counter
change of state to 00

MICRON	
INTEGRATED CIRCUIT	20920560
DATE	10/10/70
DESIGNER	10/10/70
TESTER	10/10/70
APPROVED	10/10/70

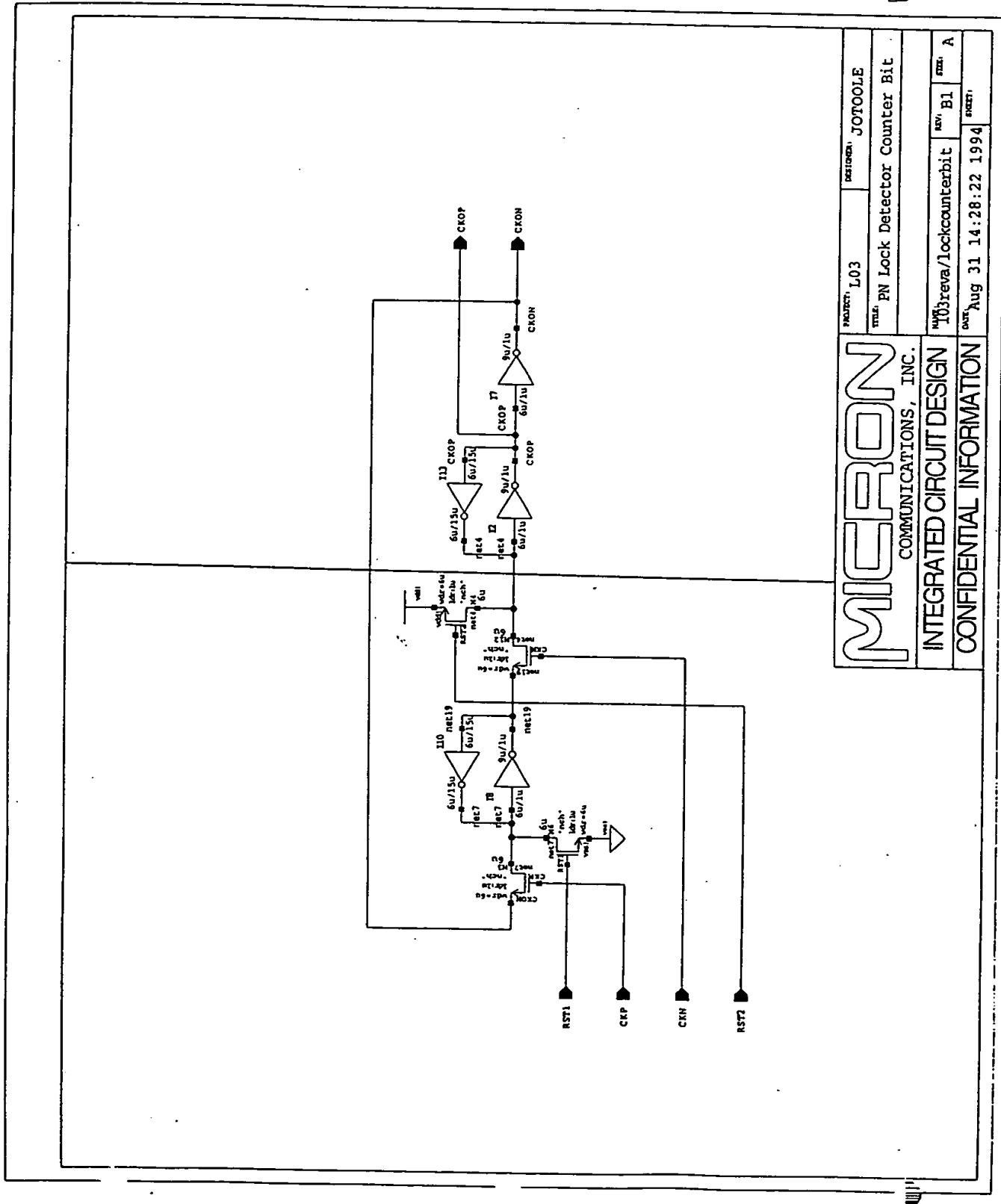
001120 20320500

10.0201AA	10.0201AB
-----------	-----------

11.000000

007720 20320369

FIG. 10.0201



007420 20320500

10.03AA	10.03AB
---------	---------

11.11.11.11.11

10.04AA	10.04AB	10.04AC	10.04AD	10.04AE
10.04BA	10.04BB	10.04BC	10.04BD	10.04BE
10.04CA	10.04CB	10.04CC	10.04CD	10.04CE

DATE: 200509

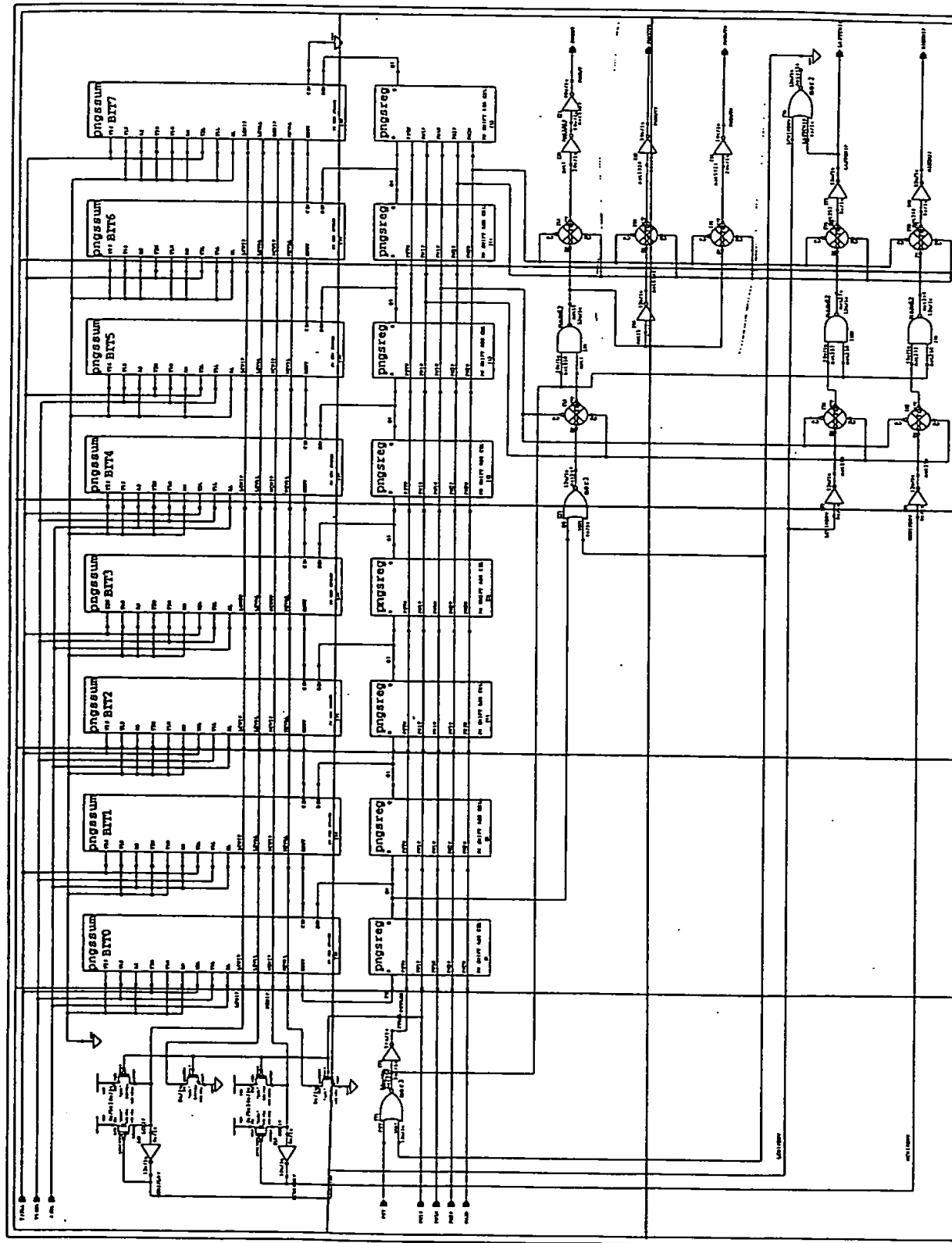
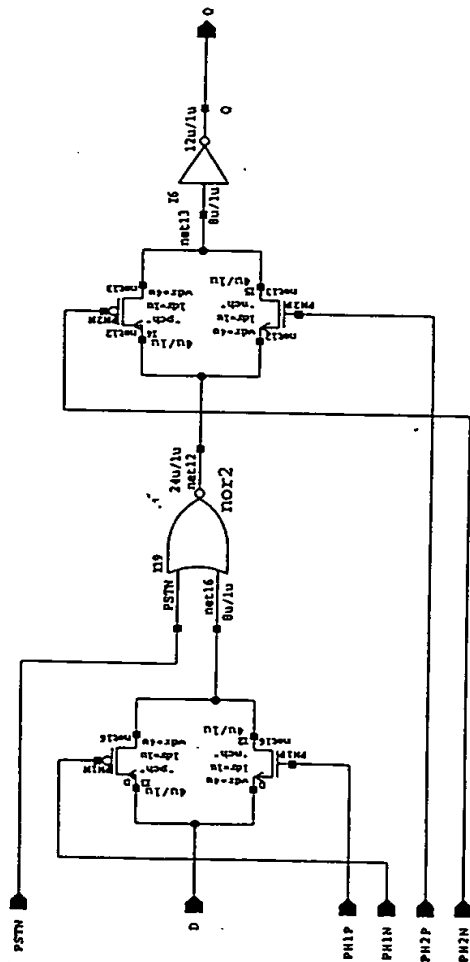


FIG. 10.04

MICRON	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
Part No.	100000000
Rev.	1.0
Date	11/10/83
By	100000000
Check	100000000
Appr.	100000000

00440 20920500



PROJECT: L03		DESIGNER: Rotzoll	
TITLE: PN Generator Shift Register Cell			
NAME:	103reva/pngsreg	REV:	A
DATE:	Nov 20 21:22:37 1993	DESIGN:	

FIG. 10.0401

10.0402AA	10.0402AB
10.0402BA	10.0402BB
10.0402CA	10.0402CB

FIG. 10.0902

MICRON COMMUNICATIONS, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

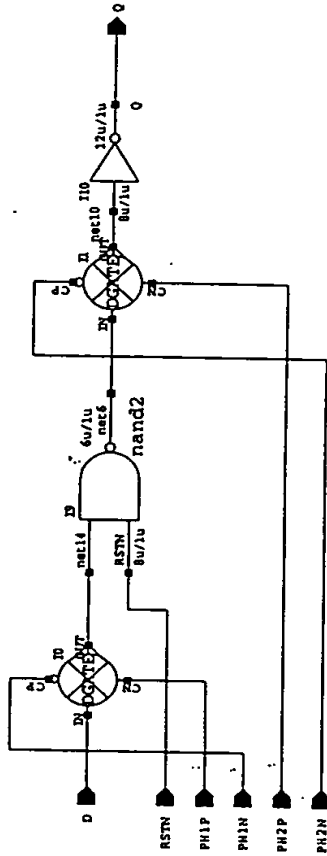
PROJECT: L03
 DESIGNED: Rotzoll
 TEST: PN Gen Shift Reg Summer

DATE: 103reva/pngssum
 DESIGNED: 103reva/pngssum
 TEST: 103reva/pngssum
 DATE: Nov 20 21:23:00 1993

The diagram illustrates a logic circuit for a PN Gen Shift Reg Summer. It features several input lines (V13, V14, S1, V15, V16, S2, V17, V18, S3) and output lines (COUT, LOUTP, LEVAL, HOUTP, HLEVAL). The circuit is composed of various logic gates, including NOT, AND, OR, and NAND gates, as well as inverters. The gates are labeled with their respective functions and pin numbers. The circuit is designed to process the input signals and generate the output signals based on the logic implemented in the gates.

MICRON
 COMMUNICATIONS, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

007420 60900000



MICRON		PRODUCT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: FN Controller D Flip-Flop	
INTEGRATED CIRCUIT DESIGN		DATE: 103reva/prndiff	REV: -
CONFIDENTIAL INFORMATION		DATE: Nov 26 18:12:59 1993	SHEET: A

Fig. 10.05

[illegible]

MI40-030

10.06AA	10.06AB	10.06AC	10.06AD							
10.06BA	10.06BB	10.06BC	10.06BD	10.06BE	10.06BF	10.06BG	10.06BH	10.06BI	10.06BJ	10.06BK
10.06CA	10.06CB	10.06CC	10.06CD	10.06CE	10.06CF	10.06CG	10.06CH	10.06CI	10.06CJ	10.06CK
10.06DA	10.06DB	10.06DC	10.06DD	10.06DE	10.06DF	10.06DG	10.06DH			

10.006 11.006

004420" 00900500

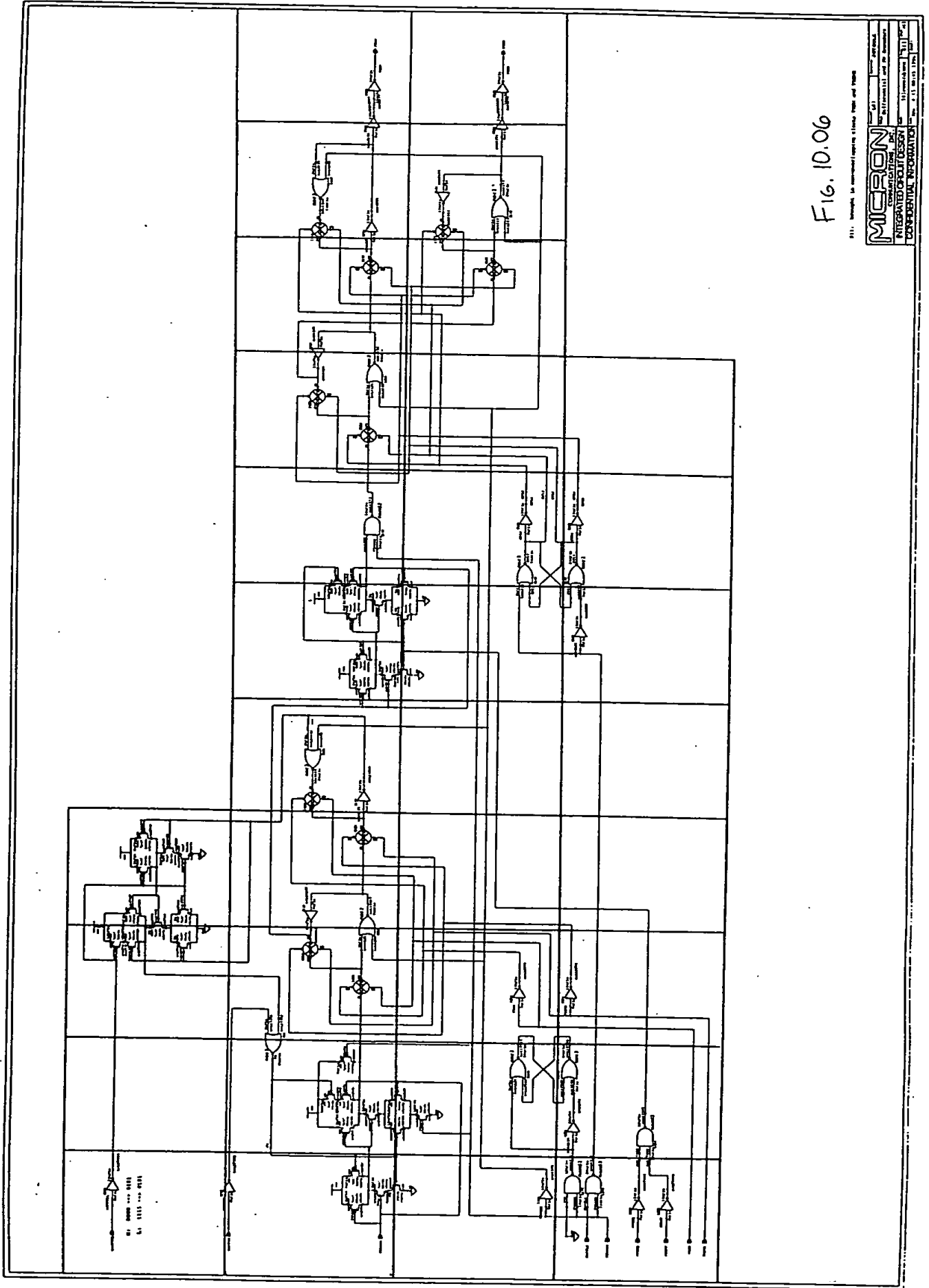
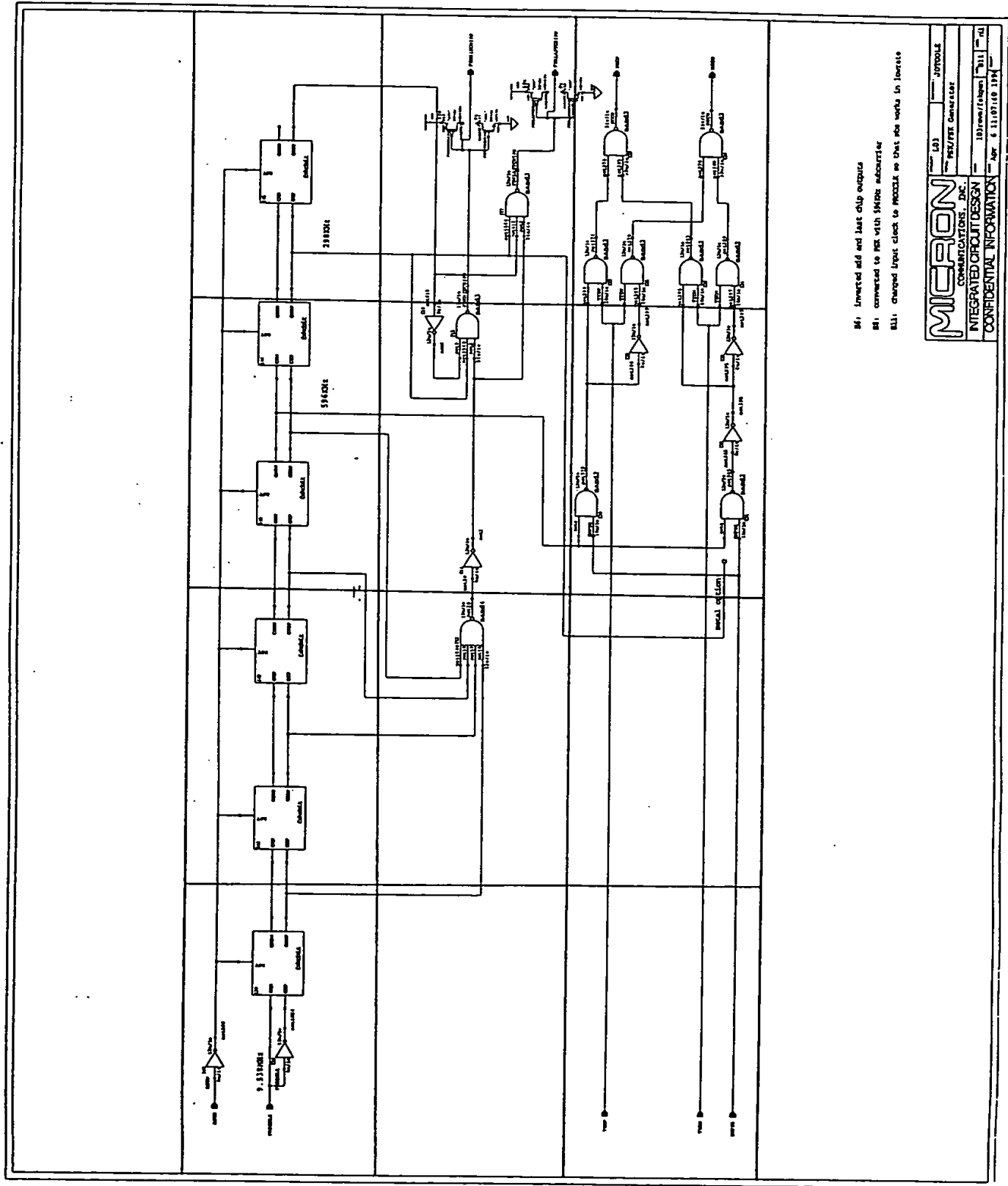


Fig. 10.06

10.07AA	10.07AB	10.07AC	10.07AD
10.07BA	10.07BB	10.07BC	10.07BD
10.07CA	10.07CB	10.07CC	10.07CD

11.11.11



M: Inverted and just chip outputs
 BE: converted to PE with 54000 microstar
 BL: changed input clock to PROCEK so that it works in librate

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
DATE: 6/11/07 (10:11:00)	DESIGNER: JUTTOLE
PROJECT: PEX/PEE GENERATOR	REVISION: 1010000/00000000
FILE: 1010000/00000000	DATE: 6/11/07 (10:11:00)

Fig. 10.07

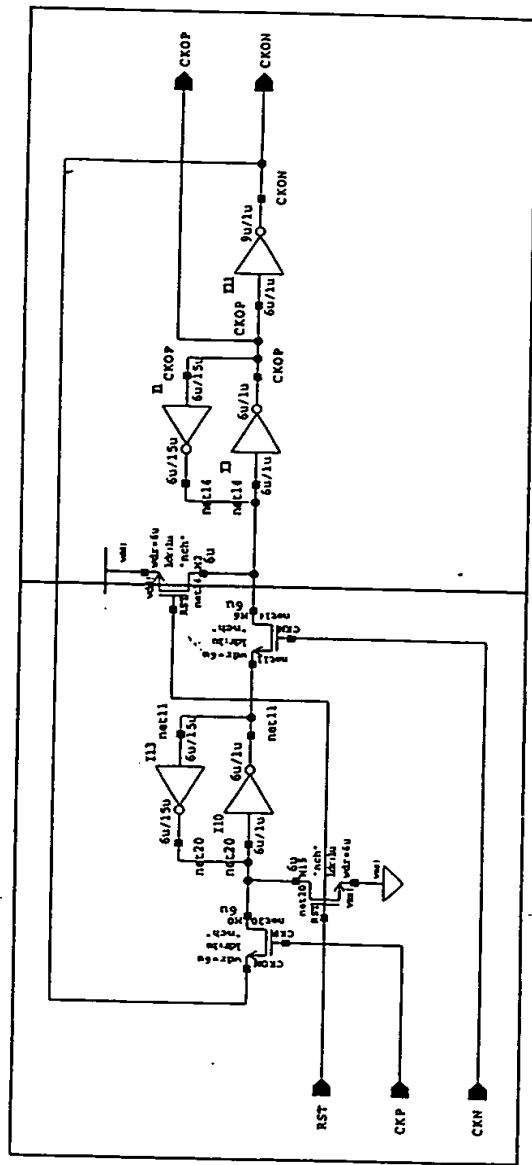
001120" 00000000

MI40-030

10.0701AA	10.0701AB
-----------	-----------

II II 10.0701

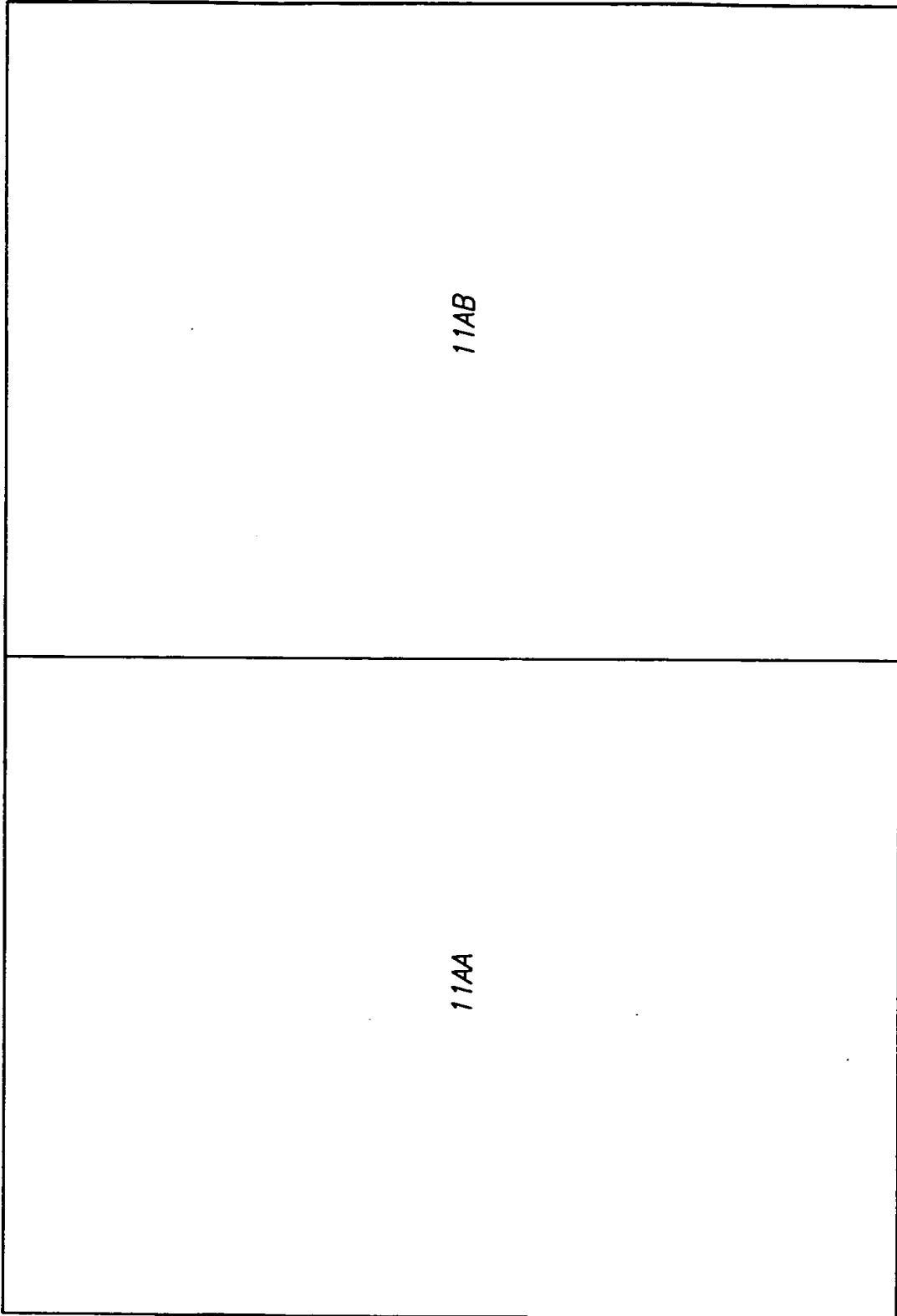
004420" 20920500



MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: FSK Counter Bit	
INTEGRATED CIRCUIT DESIGN		Same as wuabort_cbit	
CONFIDENTIAL INFORMATION		DATE: Apr 17 15:42:44 1995	REV: B1
			SHEET: A

Fig. 10.0701

001120 26920500



II II

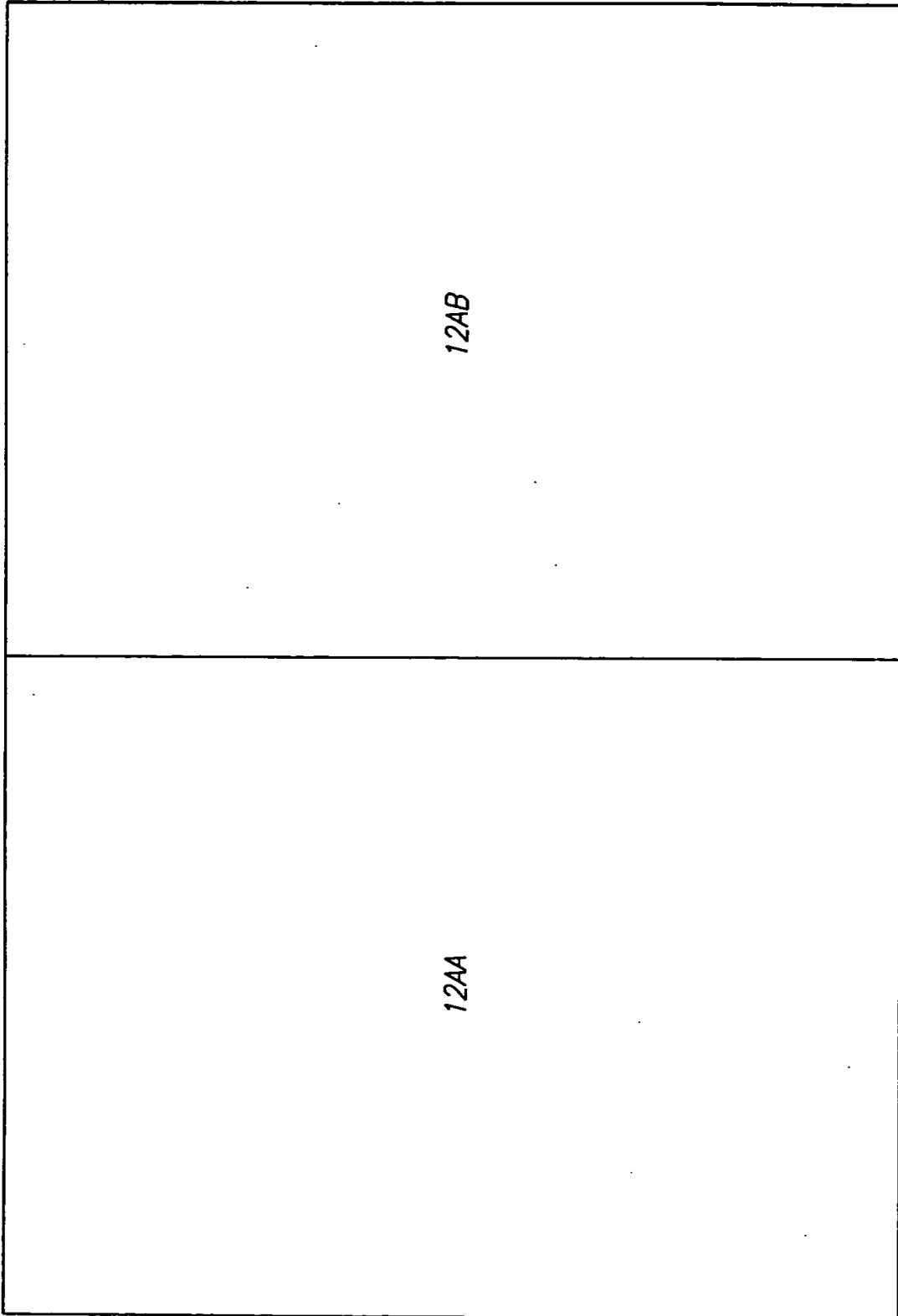
The diagram shows a complex electronic circuit labeled "B11: modified to create a buffer for". The circuit includes several integrated circuits (ICs) and passive components:

- Input Stage:** An input signal "IN" is connected to a network of resistors and capacitors. Key components include two $10\mu F$ electrolytic capacitors, three $1K$ resistors, and four $10nF$ capacitors. This network feeds into a buffer IC.
- Buffer ICs:** There are two buffer ICs, each labeled "ABUSP". Each has a gain of $\times 1$, a bandwidth of $f_{bw} = 100kHz$, and a slew rate of $slew = 10V/\mu s$. They are configured as voltage followers.
- Output Stage:** The output of the first buffer IC is connected to a second buffer IC, which also has a gain of $\times 1$, $f_{bw} = 100kHz$, and $slew = 10V/\mu s$. The final output is taken from the output of this second buffer.
- Power Supply:** A power supply section at the bottom left shows a "GND" connection, a $60/70$ transformer, and a bridge rectifier. The output of the rectifier is connected to a $100\mu F$ capacitor and a $100V$ rating, providing power to the circuit.
- Other Components:** Various other components are shown, including a $100\mu F$ capacitor, a $100V$ rating, and a $100\mu F$ capacitor, all contributing to the overall functionality of the buffer circuit.

MICRON		PROJECT: L03		DESIGNER: JOTOOLE	
COMMUNICATIONS, INC.		TITLE: Battery Analog I/O Buffer			
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/batalg		REV: B11	SER: A
CONFIDENTIAL INFORMATION		DATE: Apr 8 10:19:56 1996		SHEET:	

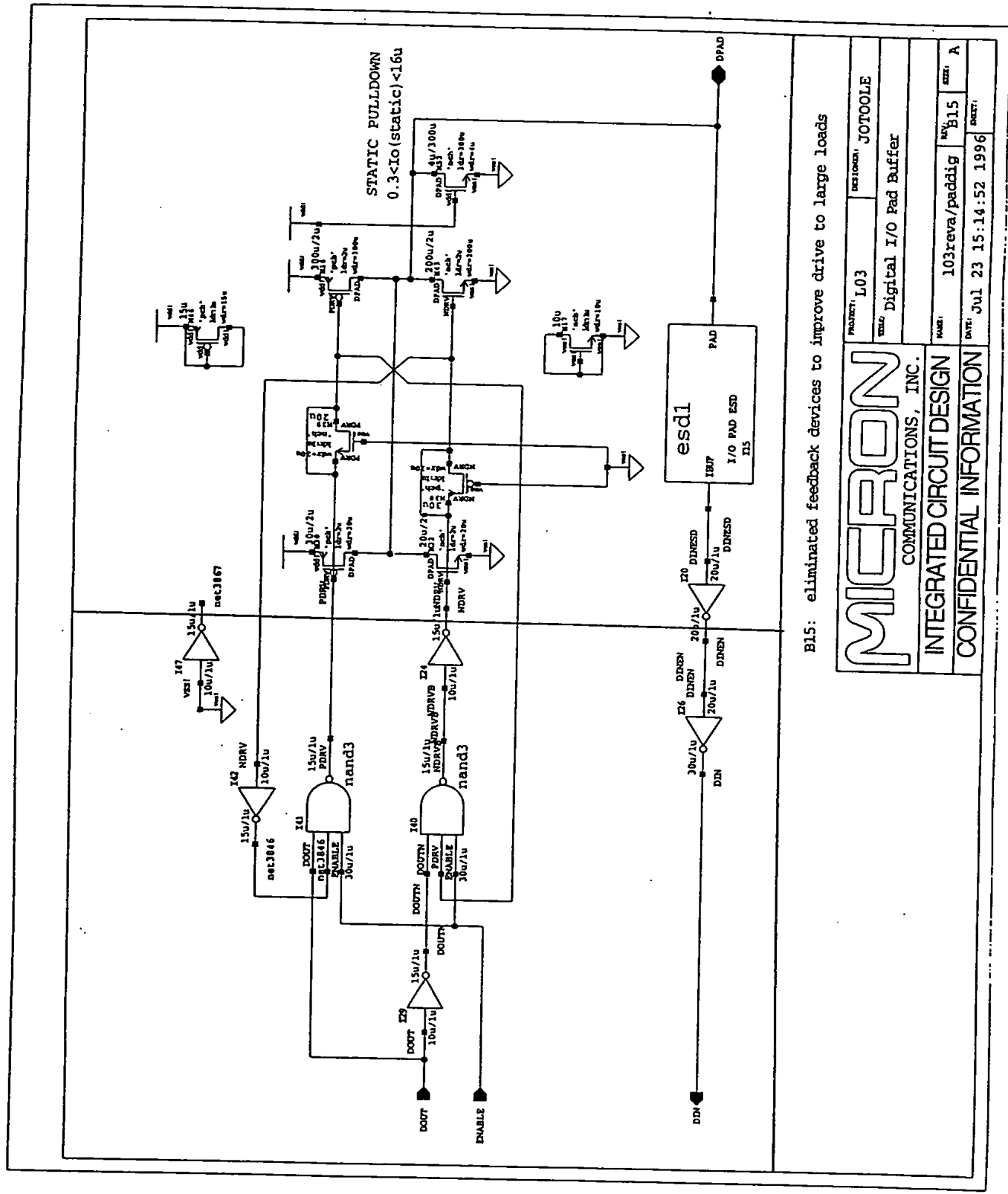
Fig. 11

0014100 00000000



12 12

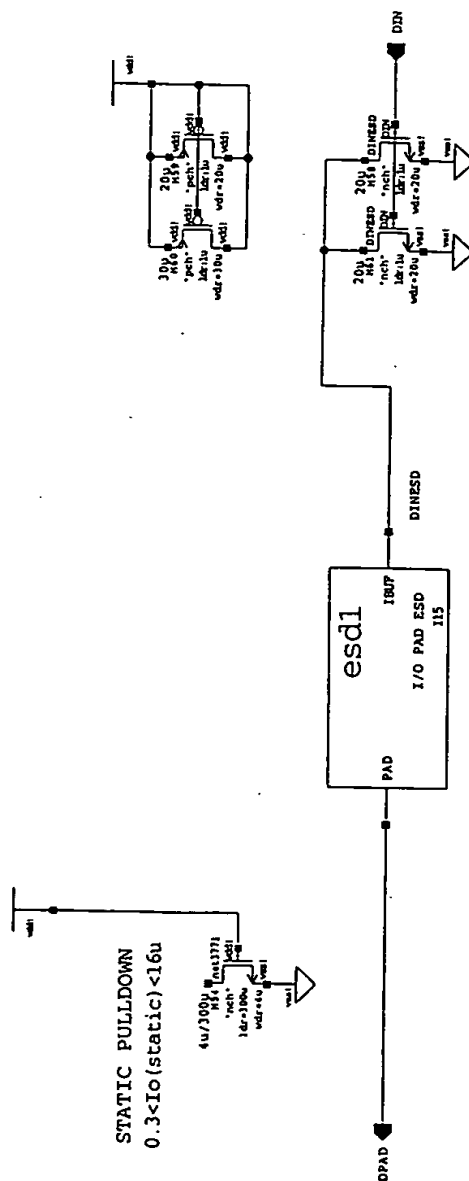
DATA 20-00000000



B15: eliminated feedback devices to improve drive to large loads

MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		NAME: Digital I/O Pad Buffer	
INTEGRATED CIRCUIT DESIGN		NAME: 103revc/paddig	REV: B15
CONFIDENTIAL INFORMATION		DATE: Jul 23 15:14:52 1996	REV: A

Fig 12AA-AB



B13: new cell for WAKEUP* output

2000

COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

PROJECT: L03	DESIGNER: JOTOOLE
--------------	-------------------

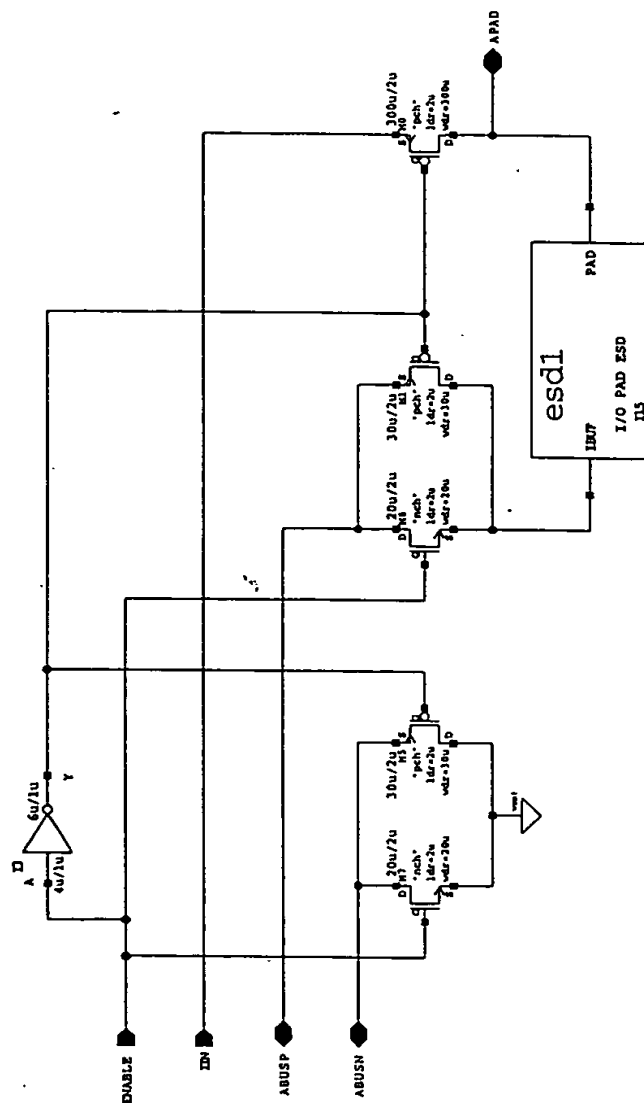
mm2. Digital Input Pad Buffer

NAME:	103reva/paddigin2	REV:	B13	SIZE:	A
-------	-------------------	------	-----	-------	---

A

DATE: May 24 18:28:29 1996 SHEET: 1

Fig 13.5

[illegible]

NOOR
COMMUNICATIONS INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

PROJECT: L03

Rotzoll

TIME	Analog I/O Pad Buffer
0000	0000
0001	0001
0002	0002
0003	0003
0004	0004
0005	0005
0006	0006
0007	0007
0008	0008
0009	0009
0010	0010
0011	0011
0012	0012
0013	0013
0014	0014
0015	0015
0016	0016
0017	0017
0018	0018
0019	0019
0020	0020
0021	0021
0022	0022
0023	0023
0024	0024
0025	0025
0026	0026
0027	0027
0028	0028
0029	0029
0030	0030
0031	0031
0032	0032
0033	0033
0034	0034
0035	0035
0036	0036
0037	0037
0038	0038
0039	0039
0040	0040
0041	0041
0042	0042
0043	0043
0044	0044
0045	0045
0046	0046
0047	0047
0048	0048
0049	0049
0050	0050
0051	0051
0052	0052
0053	0053
0054	0054
0055	0055
0056	0056
0057	0057
0058	0058
0059	0059
0060	0060
0061	0061
0062	0062
0063	0063
0064	0064
0065	0065
0066	0066
0067	0067
0068	0068
0069	0069
0070	0070
0071	0071
0072	0072
0073	0073
0074	0074
0075	0075
0076	0076
0077	0077
0078	0078
0079	0079
0080	0080
0081	0081
0082	0082
0083	0083
0084	0084
0085	0085
0086	0086
0087	0087
0088	0088
0089	0089
0090	0090
0091	0091
0092	0092
0093	0093
0094	0094
0095	0095
0096	0096
0097	0097
0098	0098
0099	0099

NAME:	REV:	SIZE:
103reva/padalg	-	A

DATE	Dec 12 21:55:41 1993	PAGE:
------	----------------------	-------

F16.19

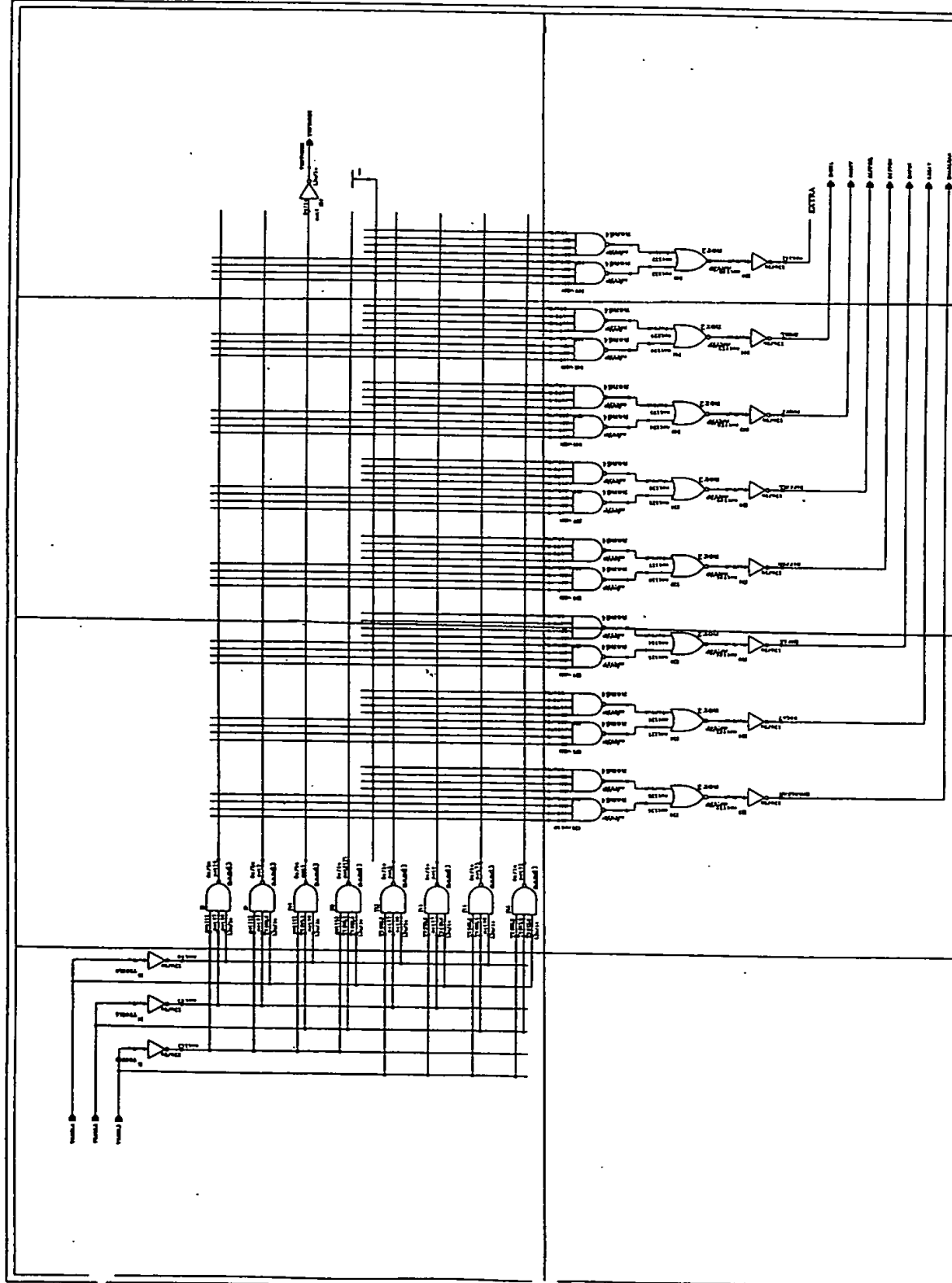
001120 20900000

MI40-030

15AA	15AB	15AC	15AD	15BC
	15BA	15BB		

11 11 11

004400 00000000



MICRON		DATA	ADDRESS
COMMUNICATIONS, INC.		Return Link Configuration	
INTEGRATED CIRCUIT DESIGN		Output Logic	
CONFIDENTIAL INFORMATION		Documentation - 88 - 01	
		Jan 21 10:01:02 1988	

88: added backscatter spread option in place of MUX

FIG. 15

001120 20920500

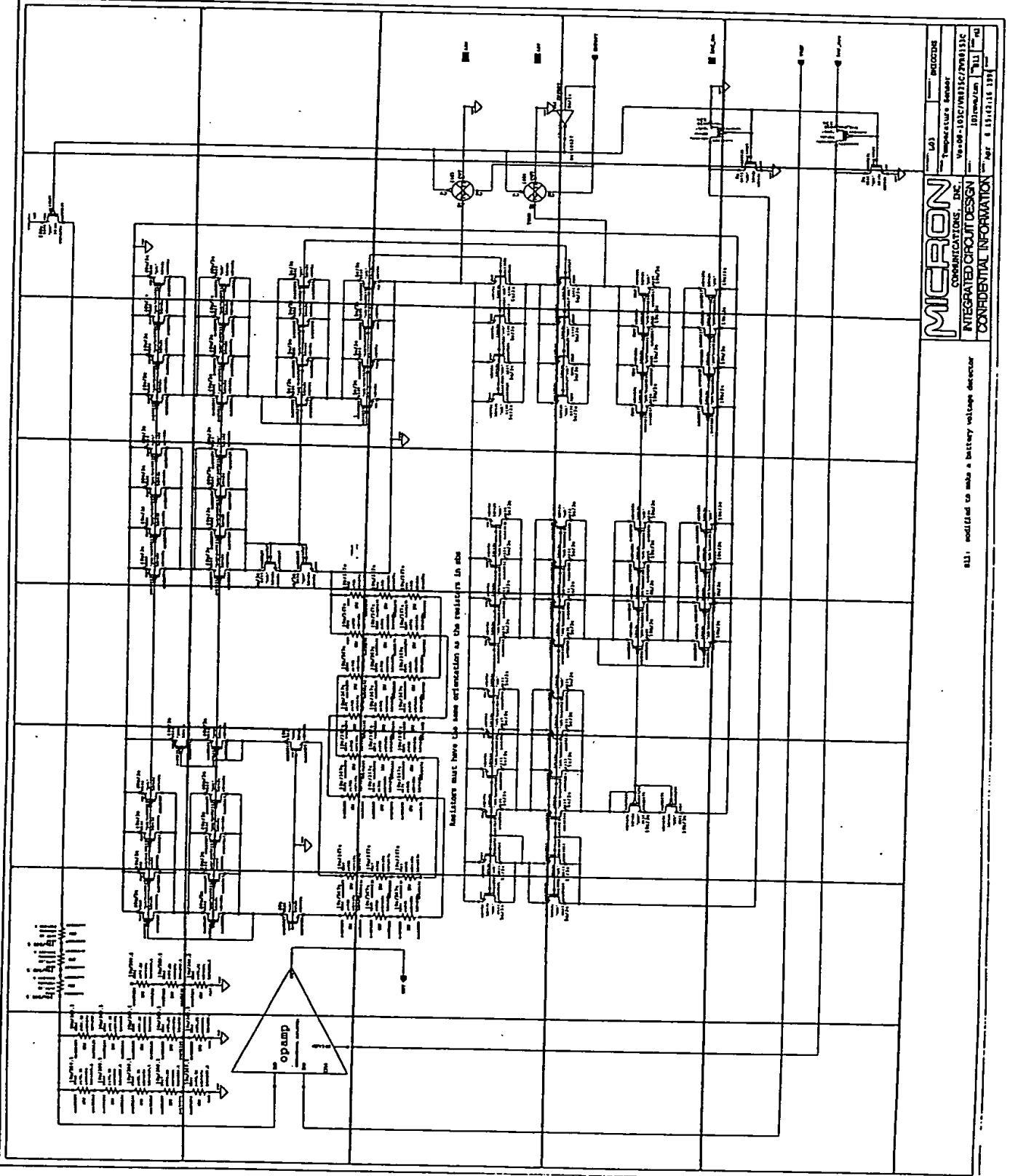
MI40-030

16AA	16AB	16AC	16AD	16AE	16AF	16AG	16AH
16BA	16BB	16BC	16BD	16BE	16BF	16BG	16BH
16CA	16CB	16CC	16CD	16CE	16CF	16CG	16CH
16DA	16DB	16DC	16DD	16DE	16DF	16DG	16DH
16EA	16EB	16EC	16ED	16EE	16EF	16EG	16EH

И. П. 116

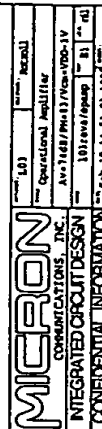
00000000000000000000

Fig. 16



16.01AA	16.01AB	16.01AC	16.01AD	16.01AE	16.01AF	16.01AG	
16.01BA	16.01BB	16.01BC	16.01BD	16.01BE	16.01BF	16.01BG	16.01BH
16.01CA	16.01CB	16.01CC	16.01CD	16.01CE	16.01CF	16.01CG	16.01CH
16.01DA	16.01DB	16.01DC	16.01DD	16.01DE	16.01DF	16.01DG	16.01DH
							16.01DI
							16.01CI
							16.01BI

Fig. 6.01



001120 203205G

MI40-030

17AA	17AB
17BA	17BB

II II

MICRON		PART NO. L03		REVISED		R020011	
COMMUNICATIONS, INC.		THE Magnetic Field Sensor					
INTEGRATED CIRCUIT DESIGN							
CONFIDENTIAL INFORMATION							
DATE:	103-reva/mag	REV:	B8	FILE:	rd		
DATE:	9 08-58-18 1962	REV:		FILE:			

B8: fixed control logic

007720 20920500

18AA	18AB
------	------

18 18

001123 20320300

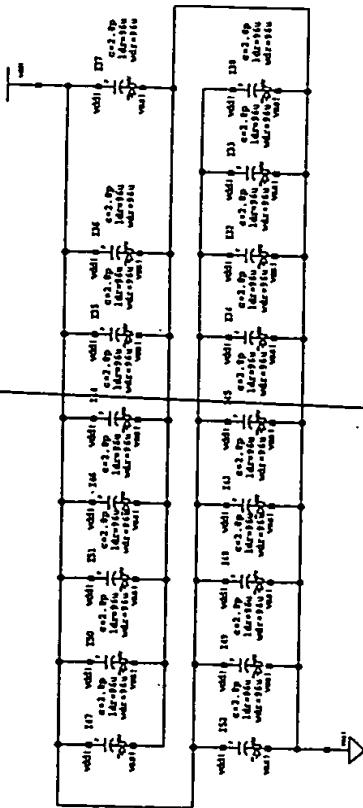


FIG. 18

MICRON		INSTRUMENT, L03	INTERNAL, J0700LE
COMMUNICATIONS, INC.		Chip Bypass Capacitor	
INTEGRATED CIRCUIT DESIGN		Ct=pf	
CONFIDENTIAL INFORMATION		DATE: 103reva/bypcap3	REV: B2
		DATE: Jul 28 17:43:25 1995	FILE: n1

B2: deleted one cap

00120 2690900

MI40-030

19AA	19AB	19AC	19AD	19AE	19AF	19AG	19AH	19AI	19AJ	19AK
19BA	19BB	19BC	19BD	19BE	19BF	19BG	19BH	19BI	19BJ	19BK
19CA	19CB	19CC	19CD	19CE	19CF	19CG	19CH	19CI	19CJ	19CK
19DA	19DB	19DC	19DD	19DE	19DF	19DG	19DH	19DI	19DJ	19DK
19EA	19EB	19EC	19ED	19EE	19EF	19EG	19EH	19EI	19EJ	19EK

II II II II

000000000000

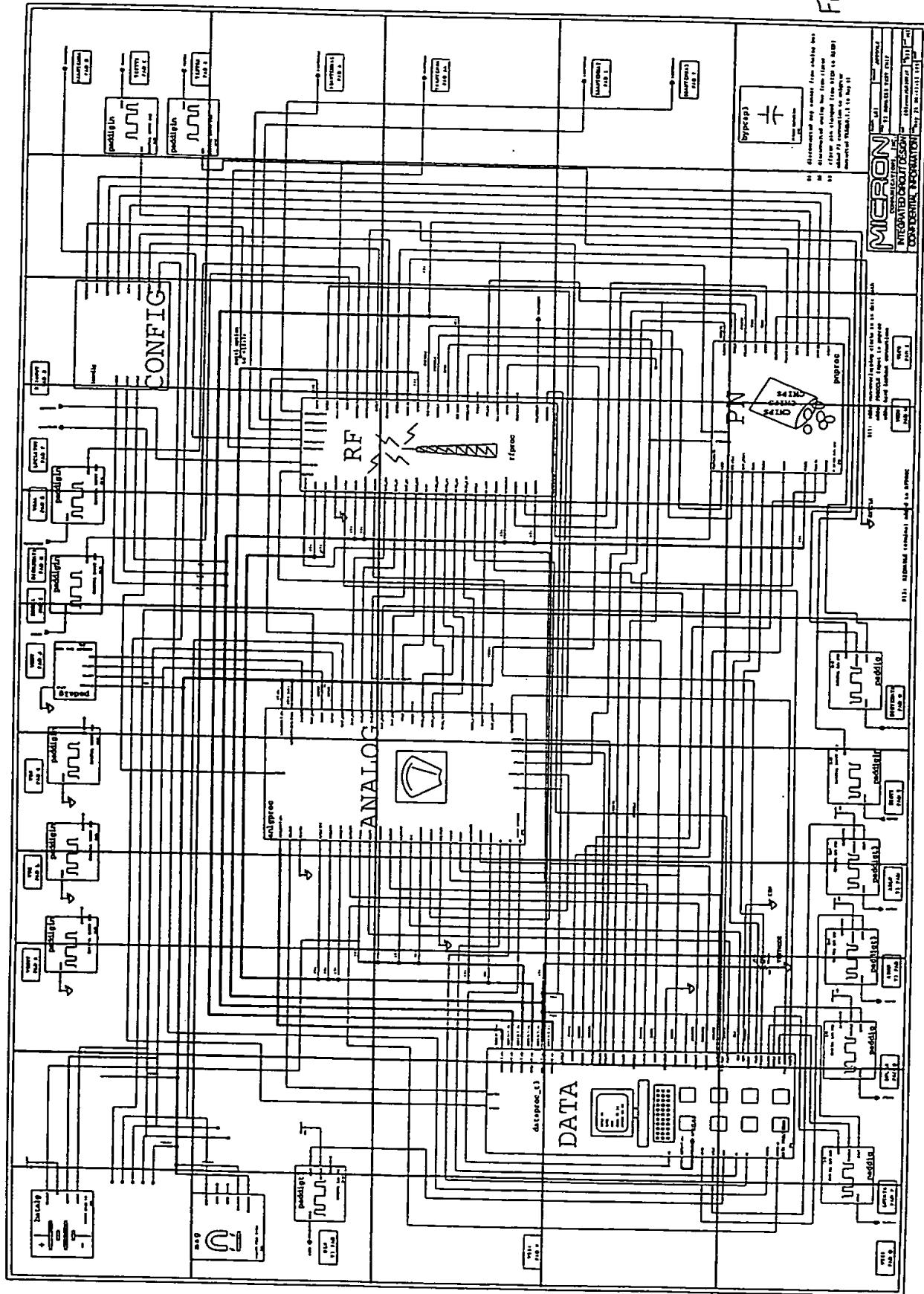


Fig. 19.AA-EK

MICRON
INTEGRATED CIRCUITS
COMMUNICATIONS
CONFIDENTIAL INFORMATION

00000000000000000000

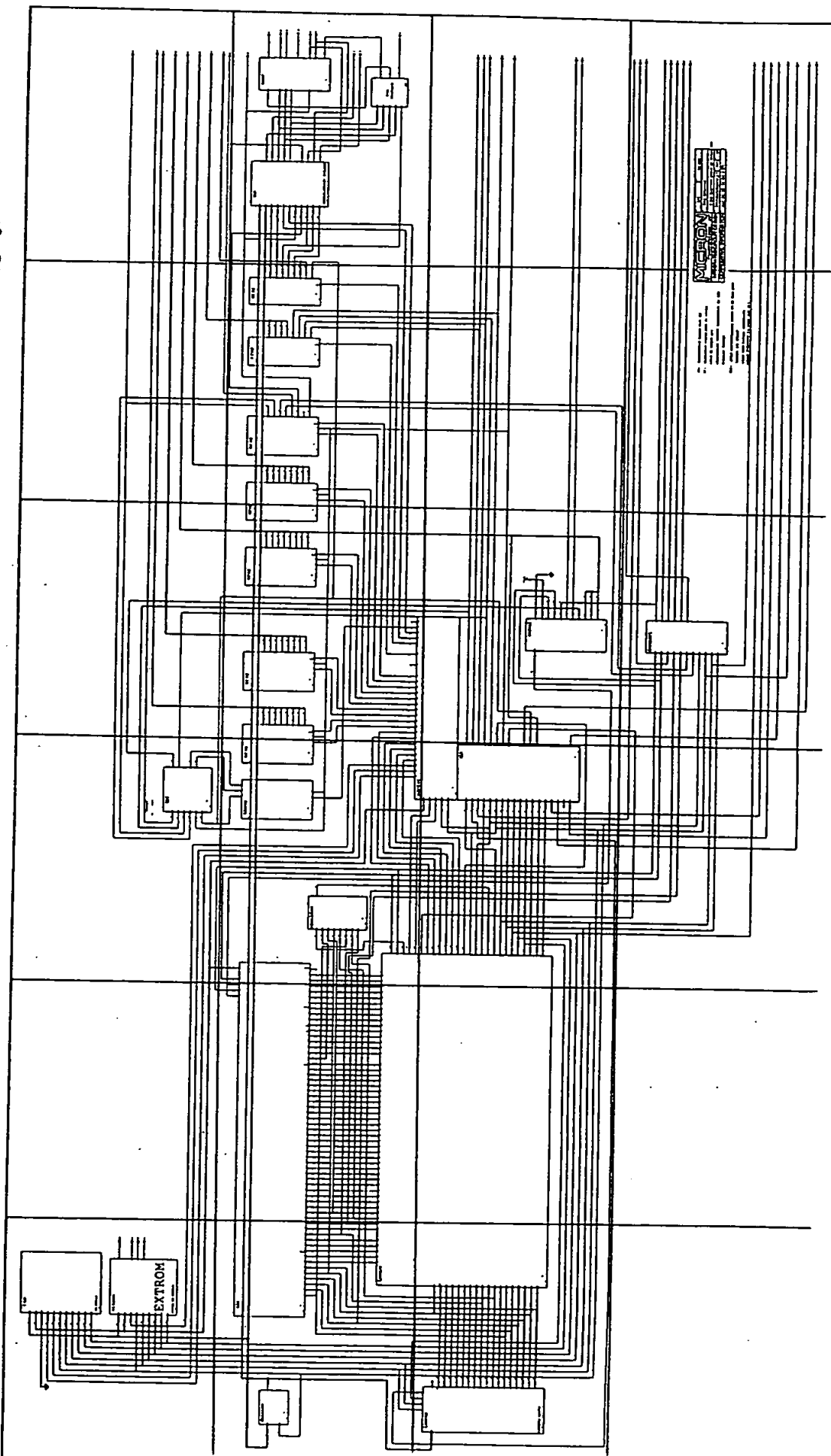
MI40-030

20AA	20AB	20AC	20AD	20AE	20AF
20BA	20BB	20BC	20BD	20BE	20BF
20CA	20CB	20CC	20CD	20CE	20CF
		20DC	20DD	20DE	20DF

11 11 11 11

004720-20000000

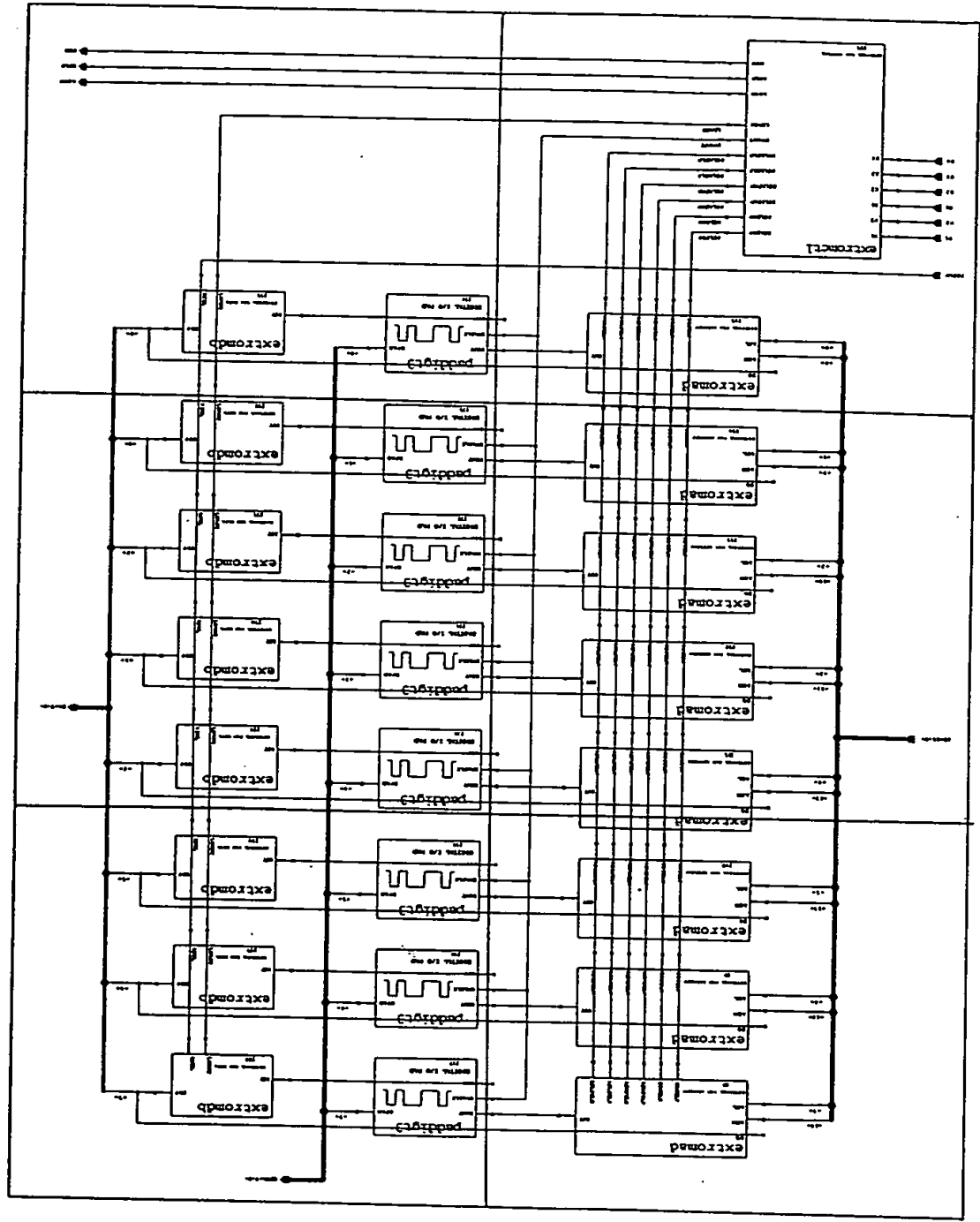
Fig. 20



20.01CB

[illegible]

FIG. 20.01

[illegible]

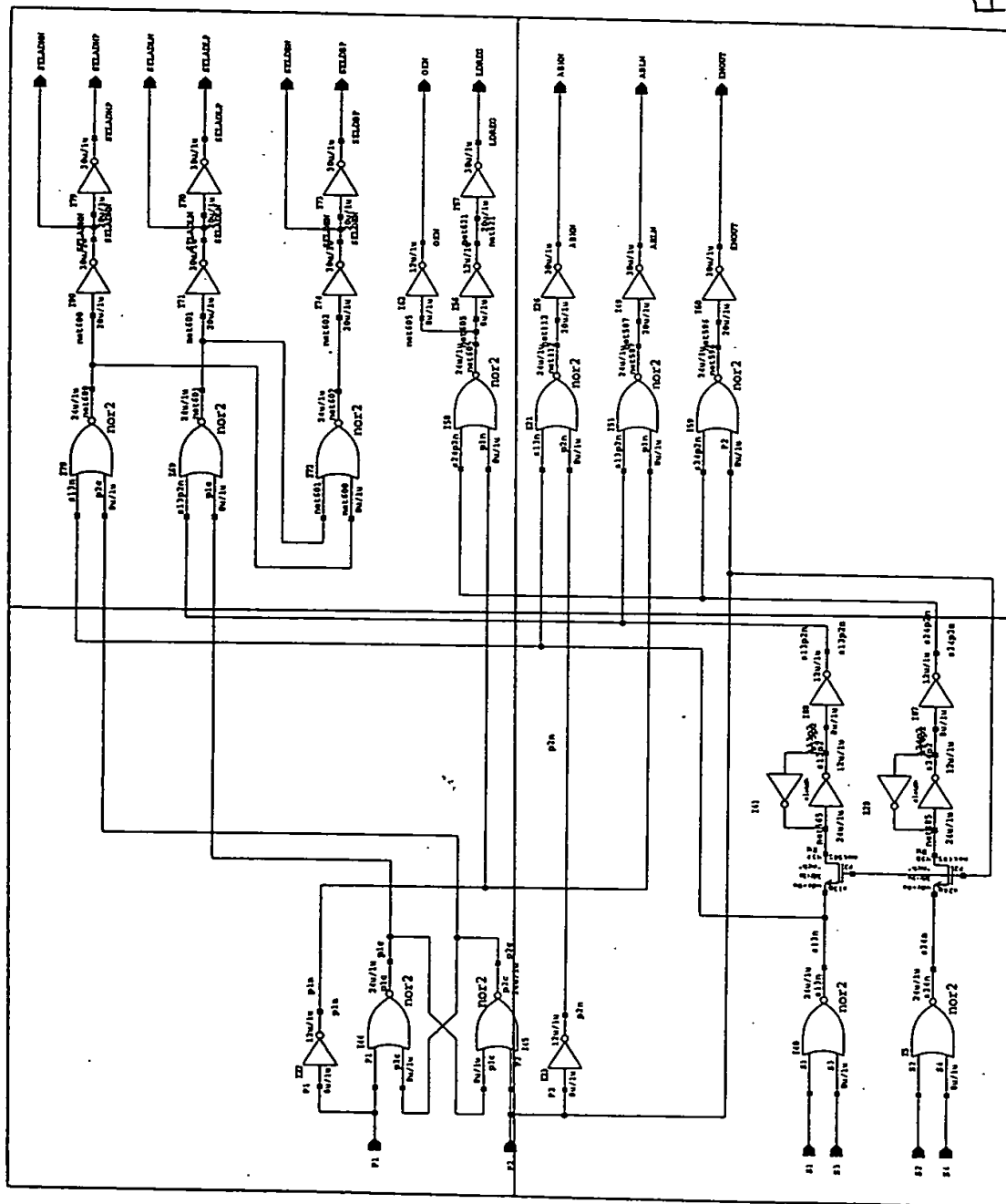
004120" 00000000

MI40-030

20.0101AA	20.0101AB
20.0101BA	20.0101BB

И. П. 00000000

FIG. 20.0101



NOIR

COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

904/827, L03

External ROM Control Logic

103reva/extract	103
-----------------	-----

11 Dec 11 21:56:41 1993

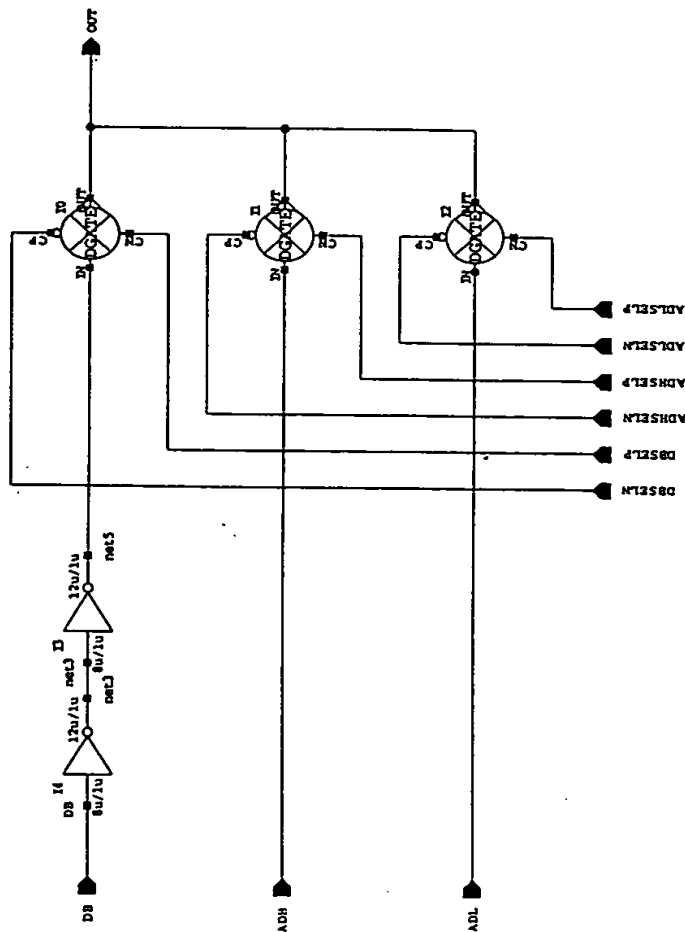


Fig. 20.0102

MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: External ROM Address Interface	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/extromad	REV: -
CONFIDENTIAL INFORMATION		DATE: Dec 11 01:09:14 1993	SIZE: A

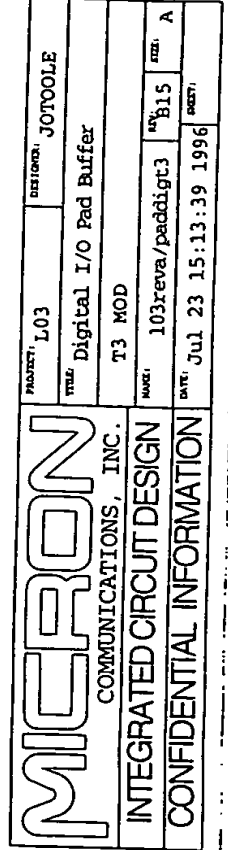
004420 20920555

MI40-030

20.0103AA	20.0103AB	20.0103AC
-----------	-----------	-----------

IL 11 20.01.03

Fig 20.0103M-AC



COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

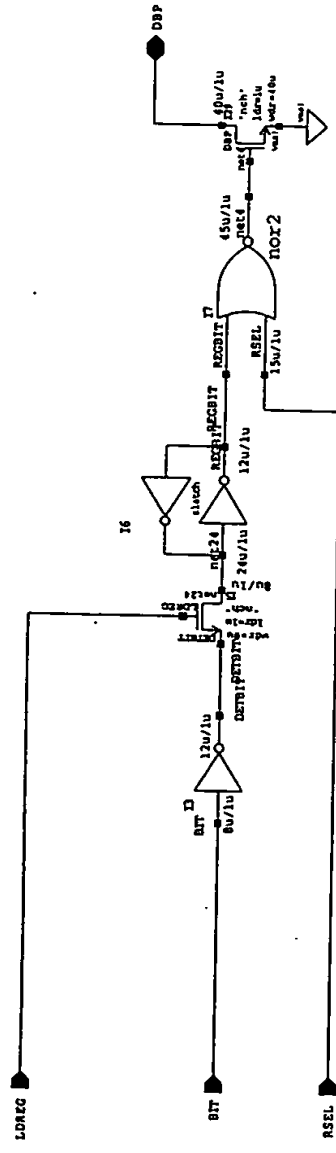
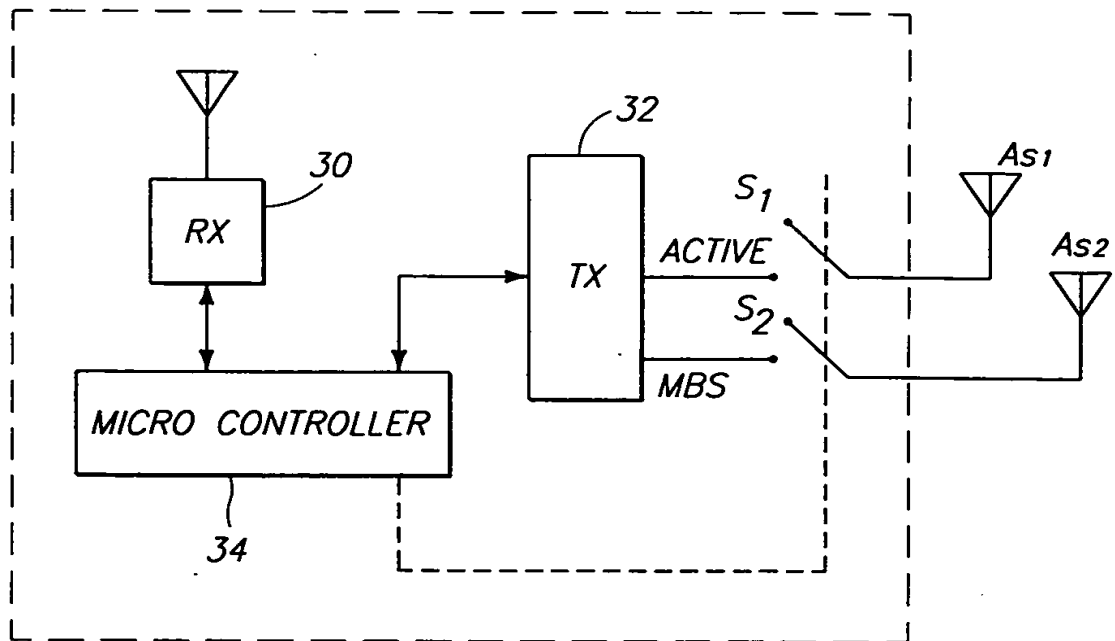
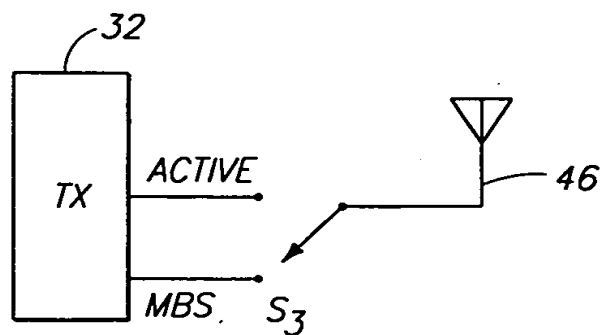


FIG. 20.0104

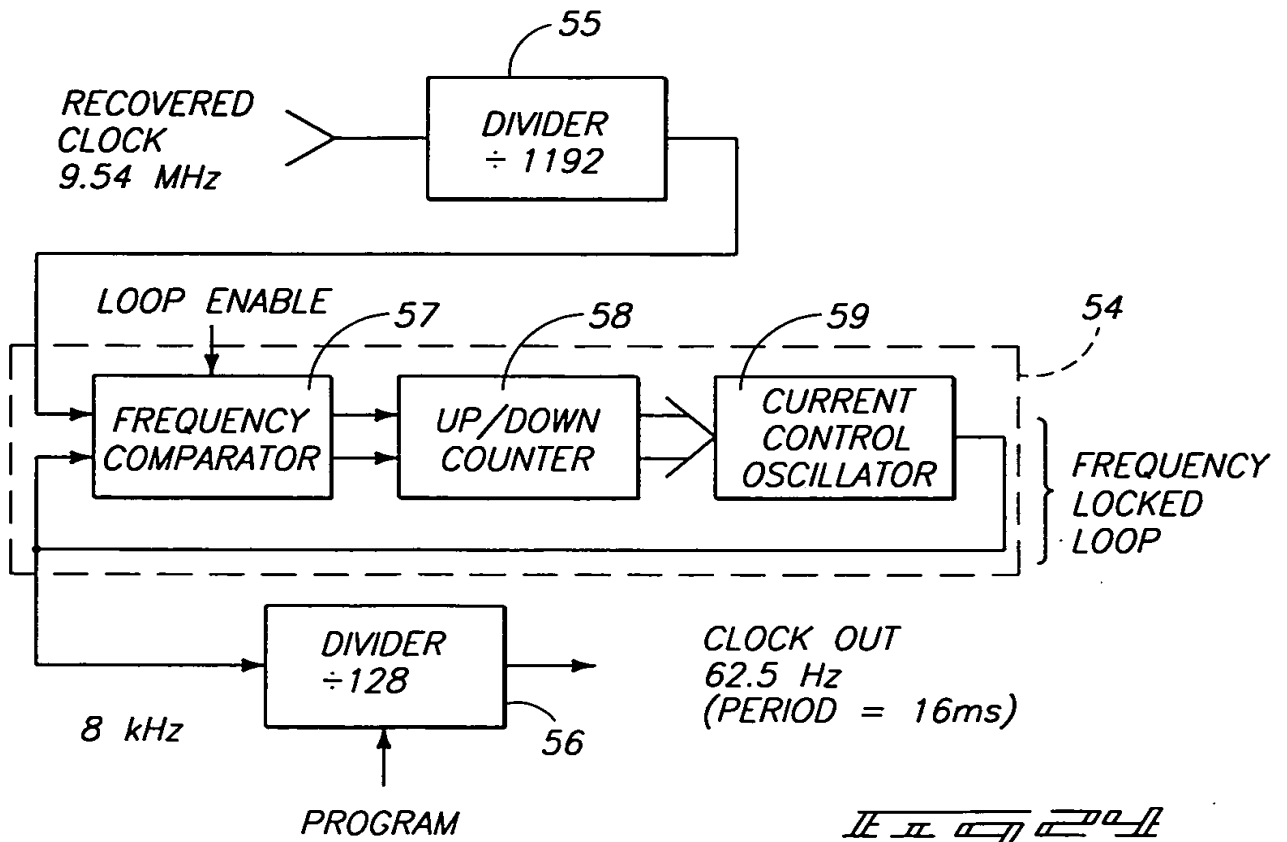
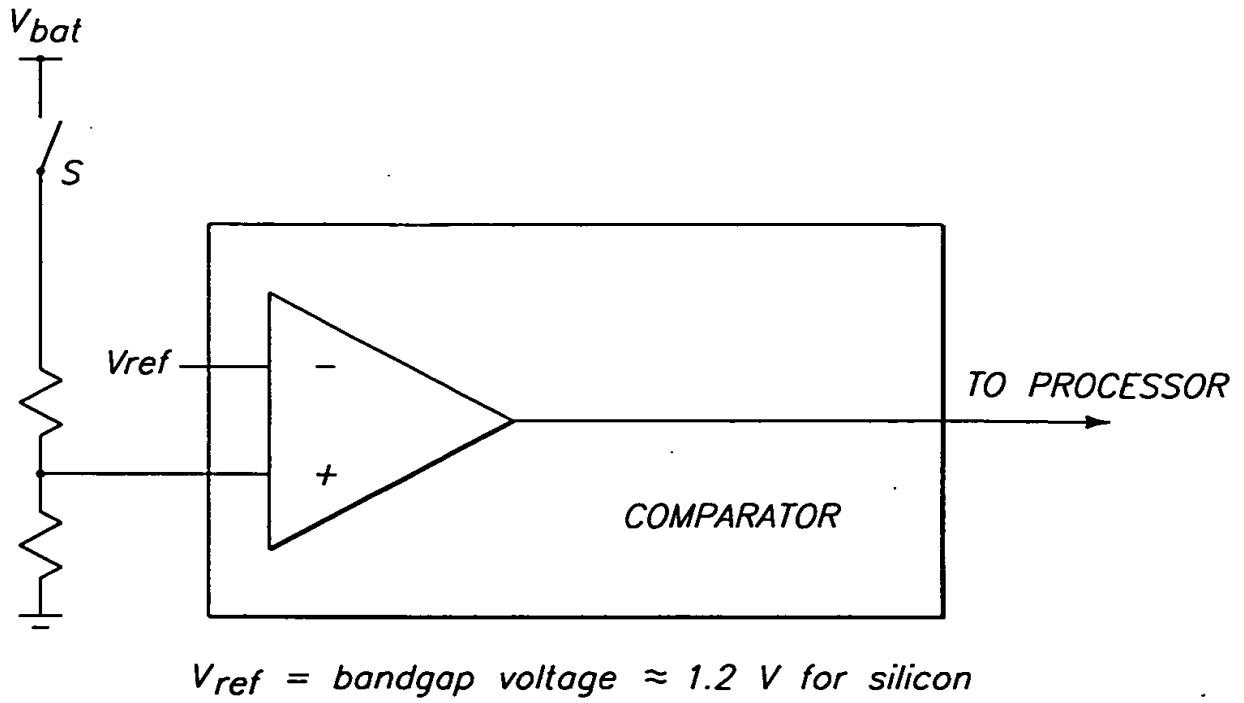
MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: External ROM Database Interface	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/extromdb	REV: A
CONFIDENTIAL INFORMATION		DATE: Dec 11 01:01:13 1993	DATE:

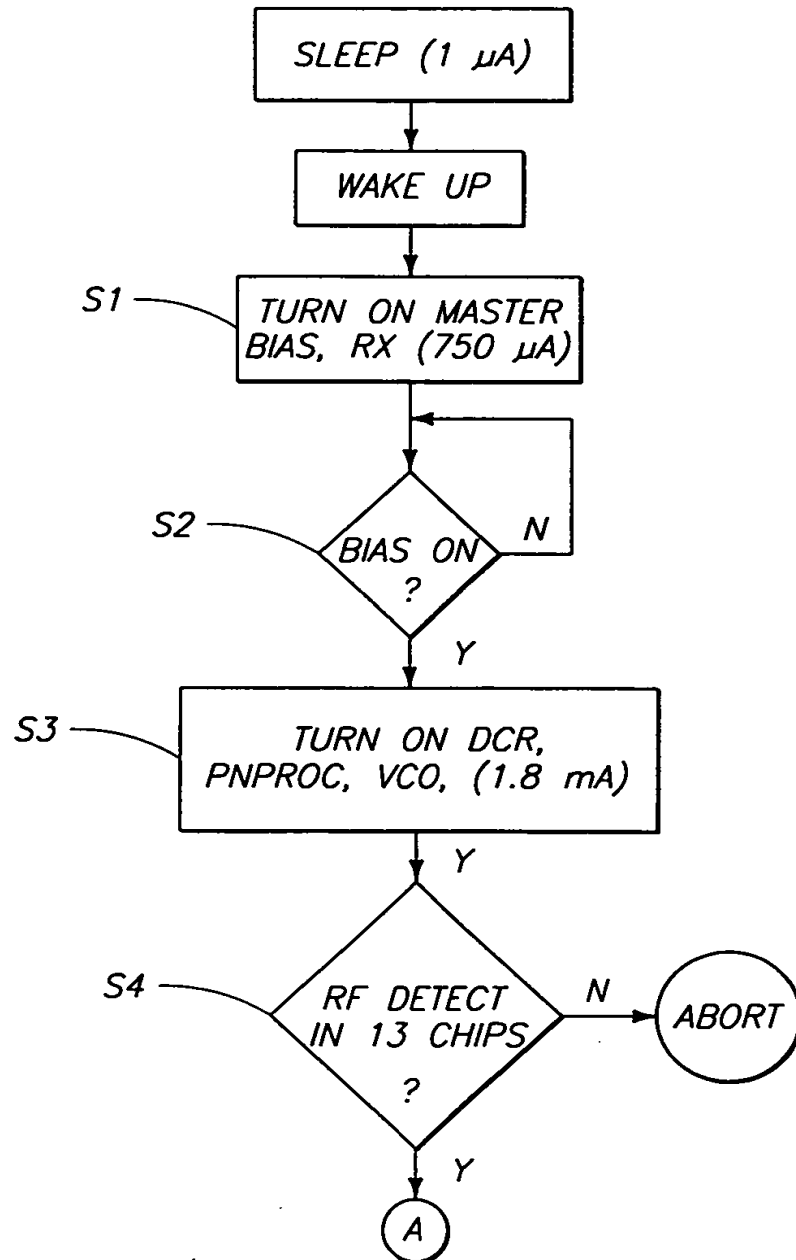


II II II II II

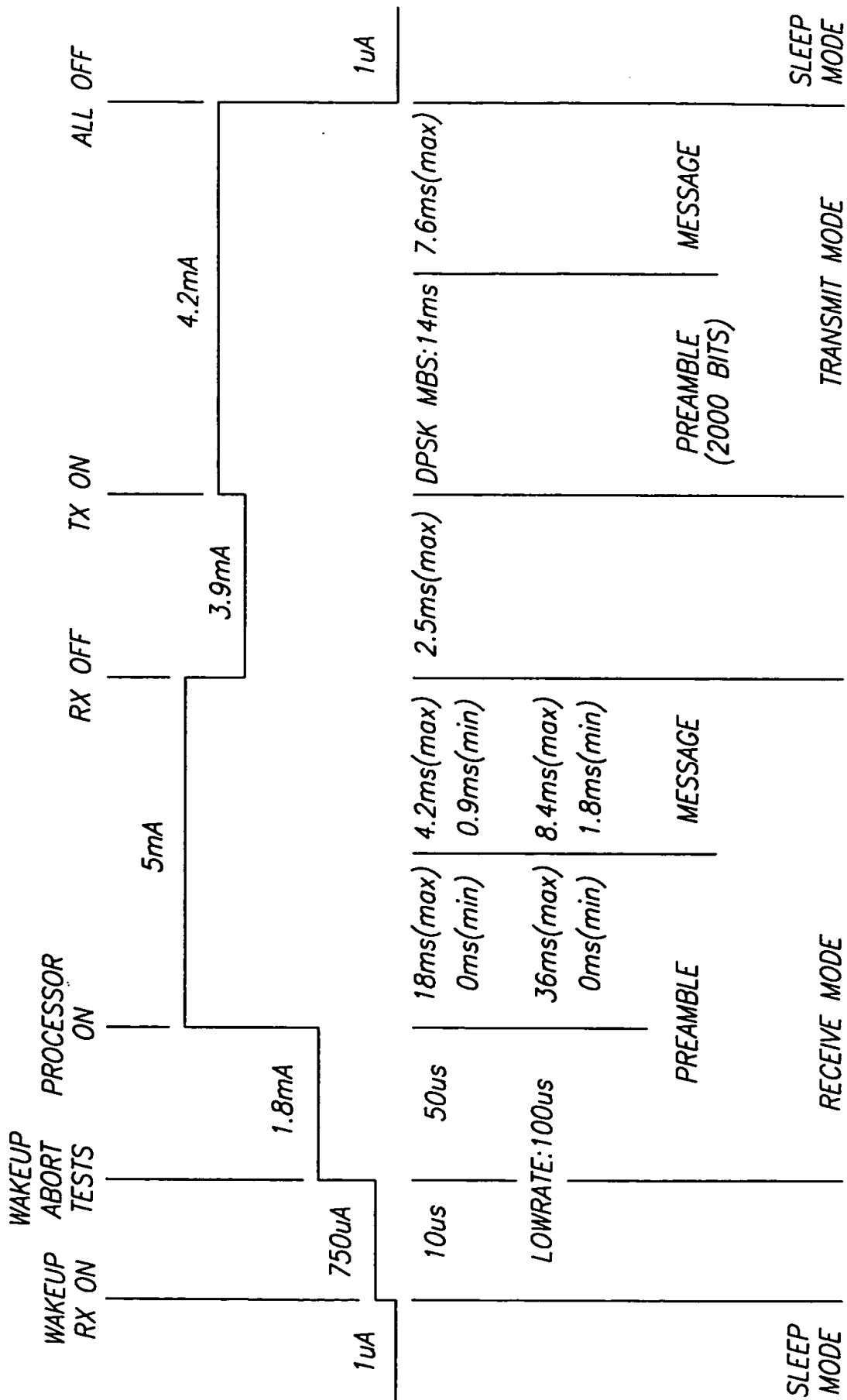


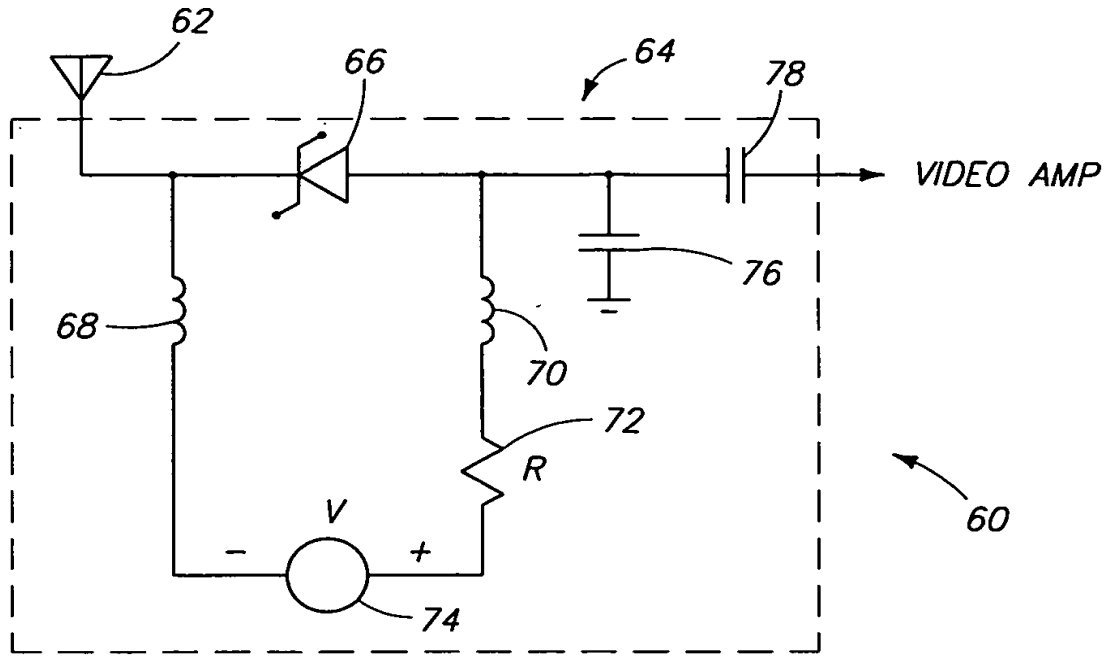
II. 2222



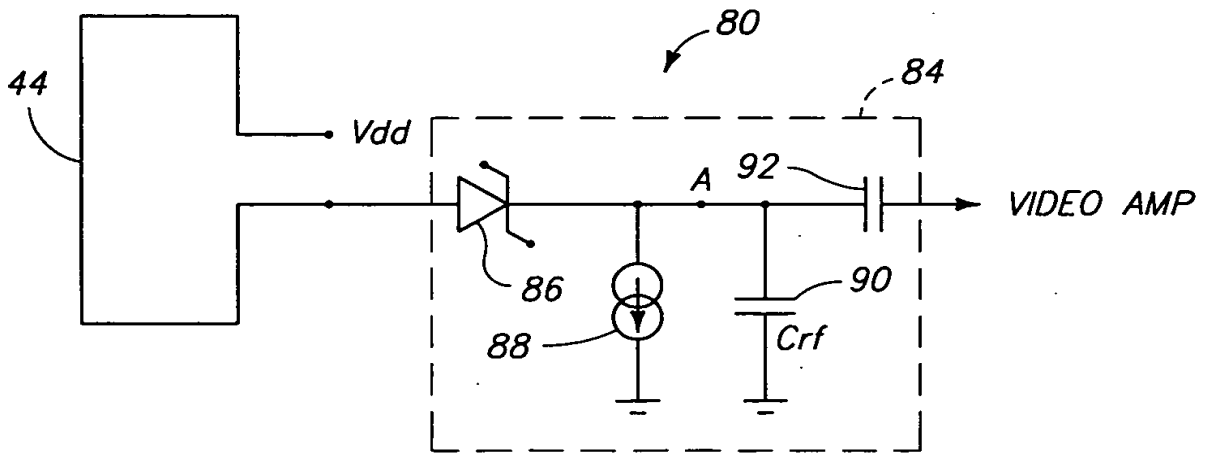
WAKEUP SEQUENCE







II II 28



II II 29

004120-00400



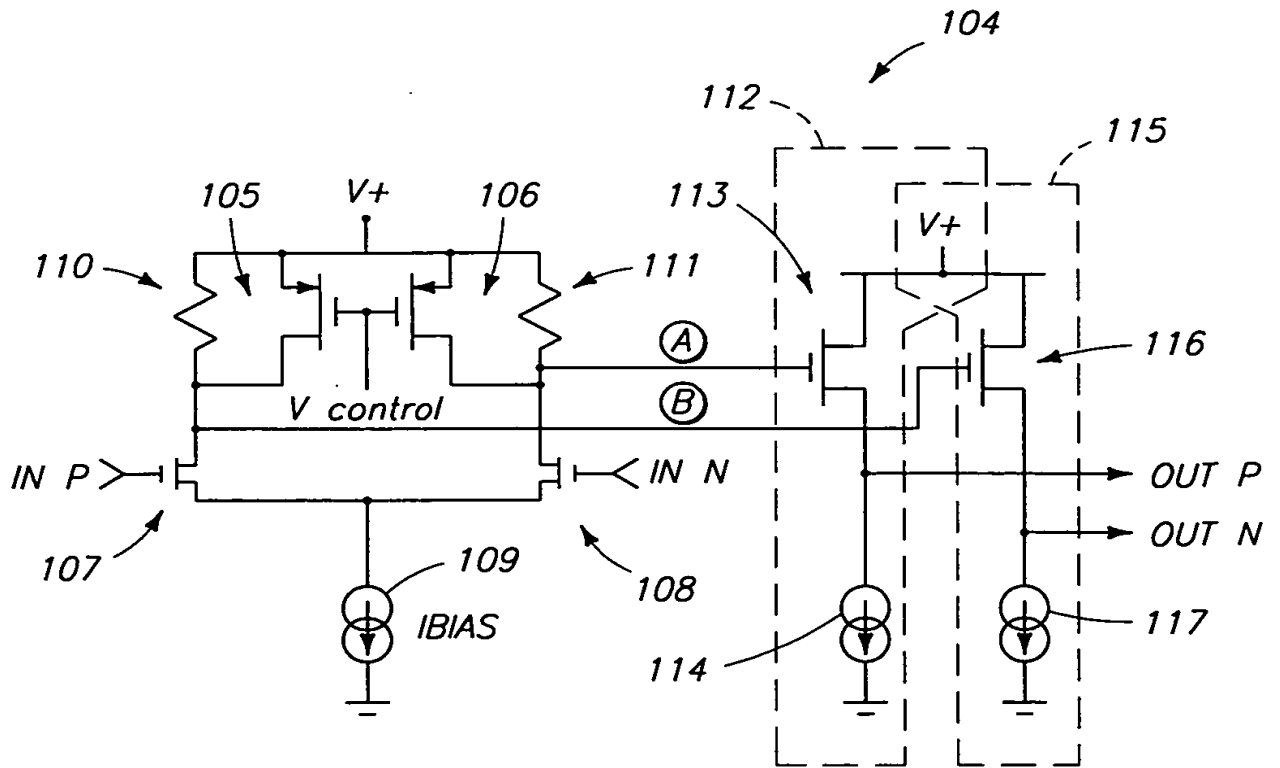


Figure 3

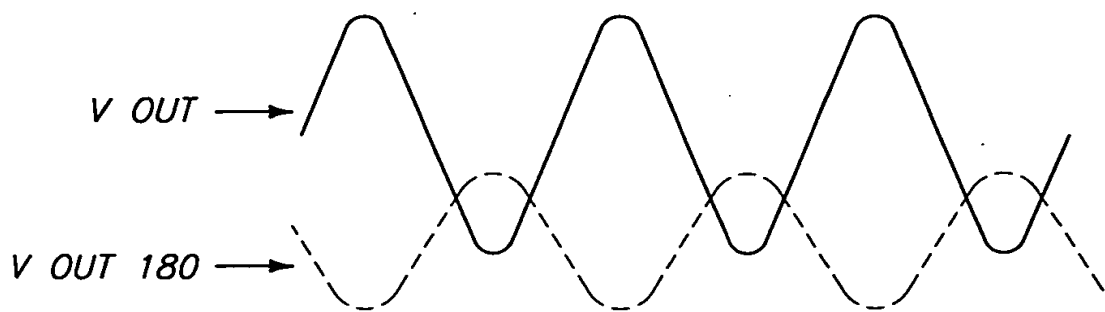
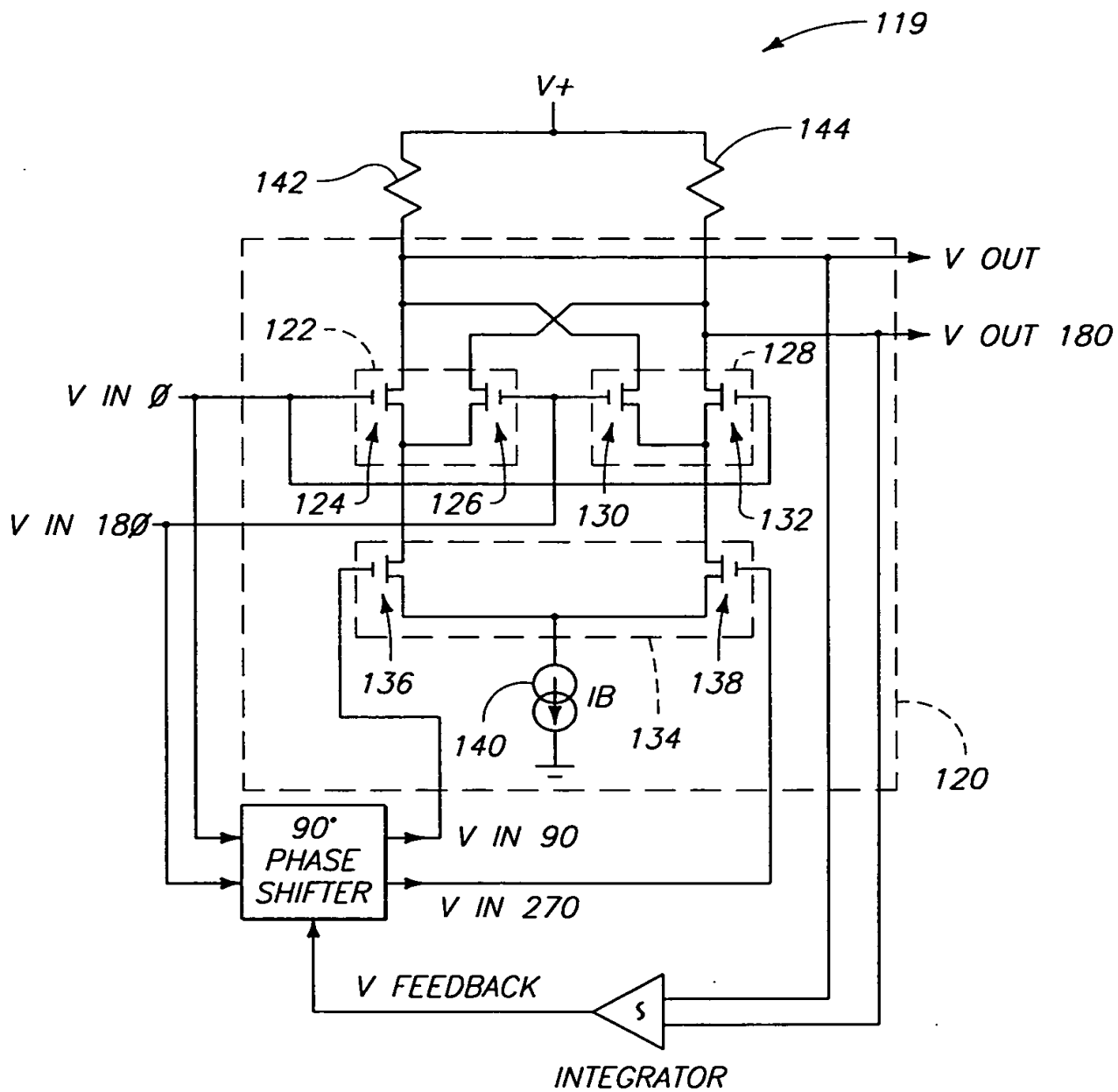


Figure 4



SECRET

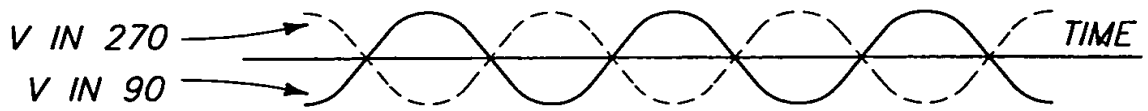
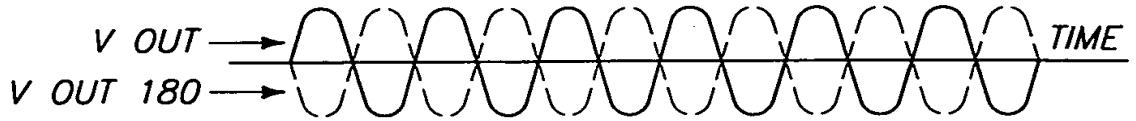
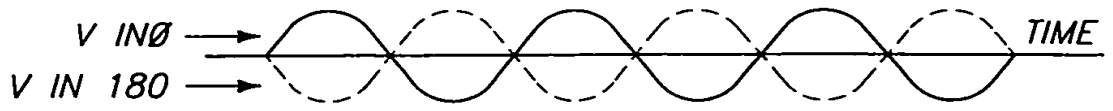
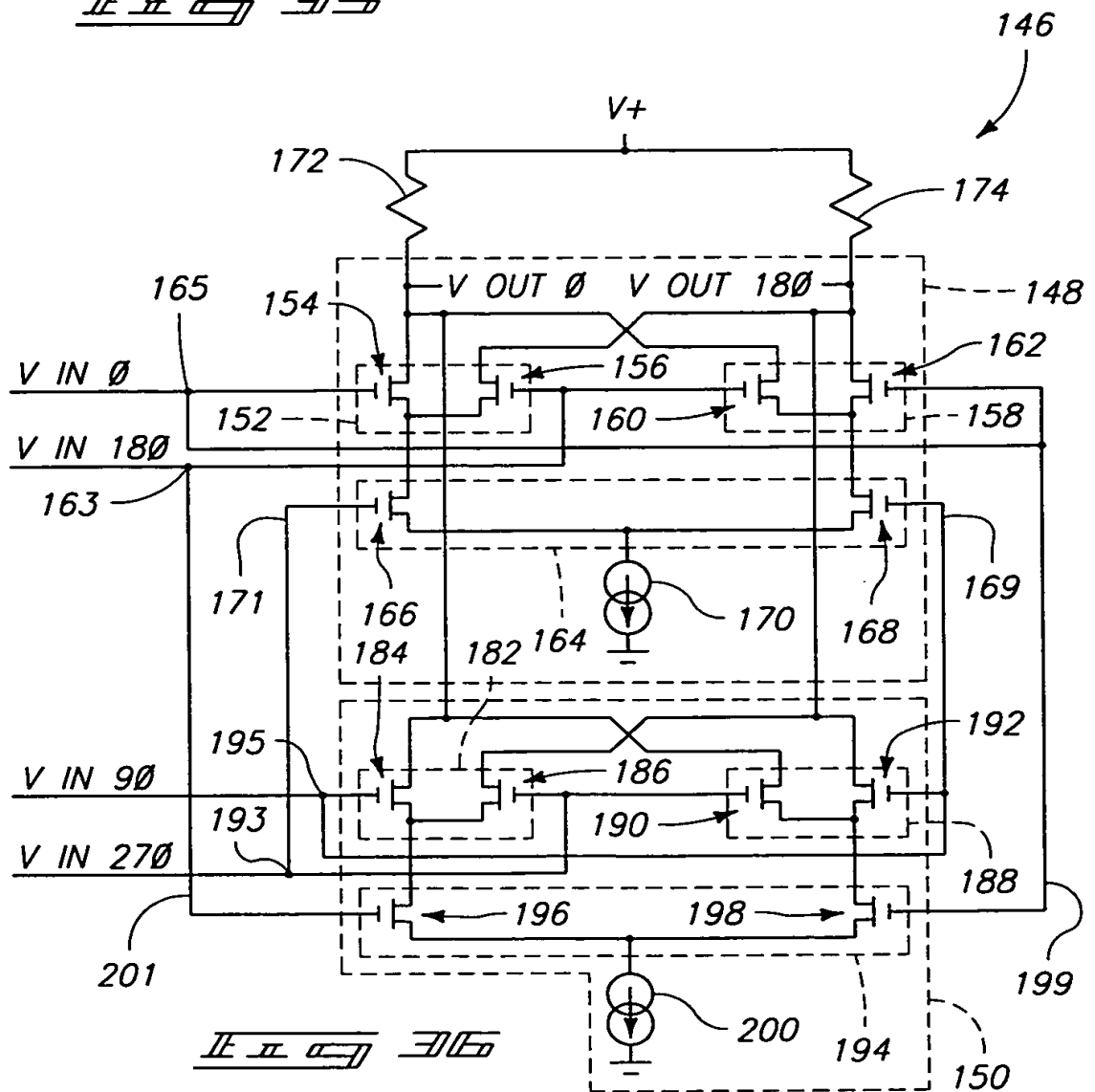
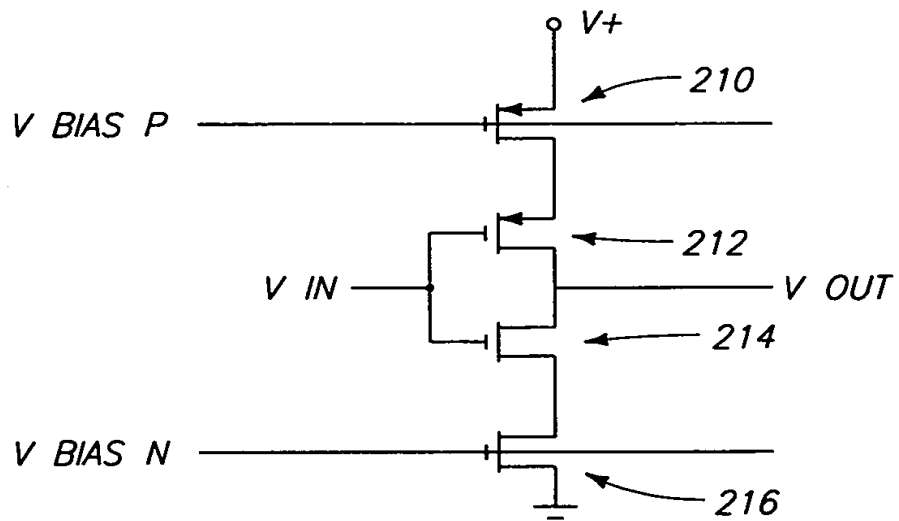


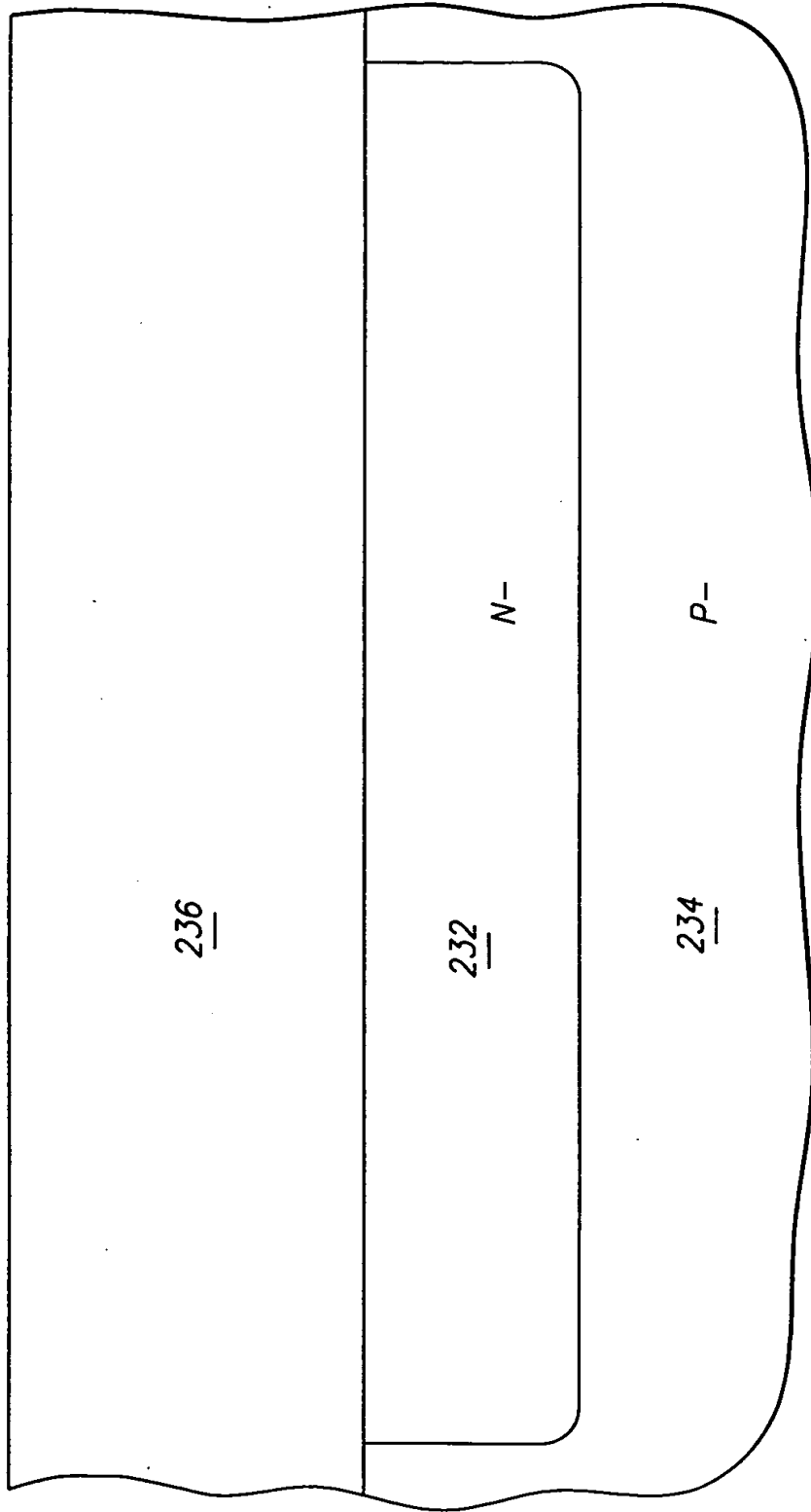
FIG. 3





II II II II II II

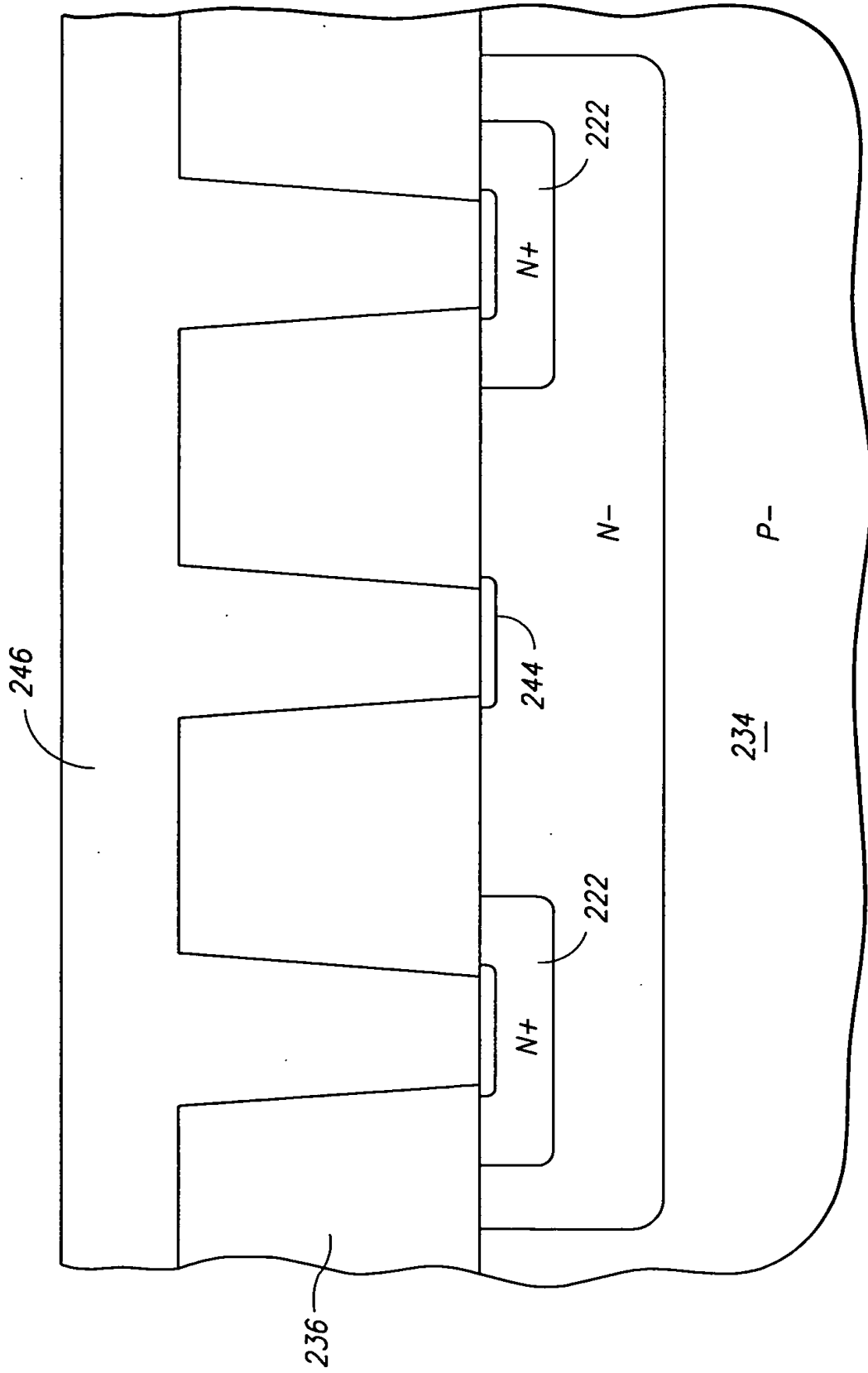
001120 26320546



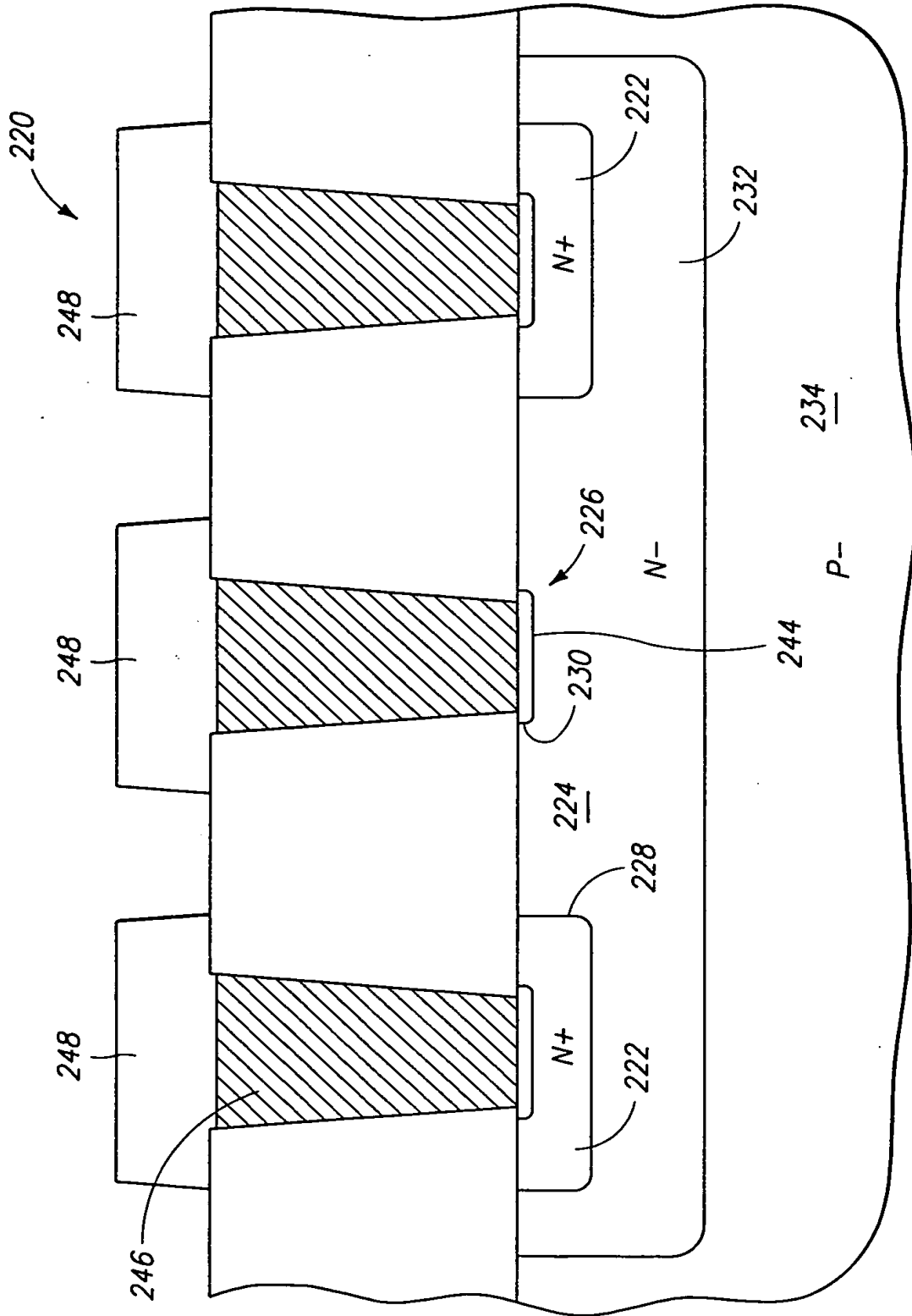
FILE 3188



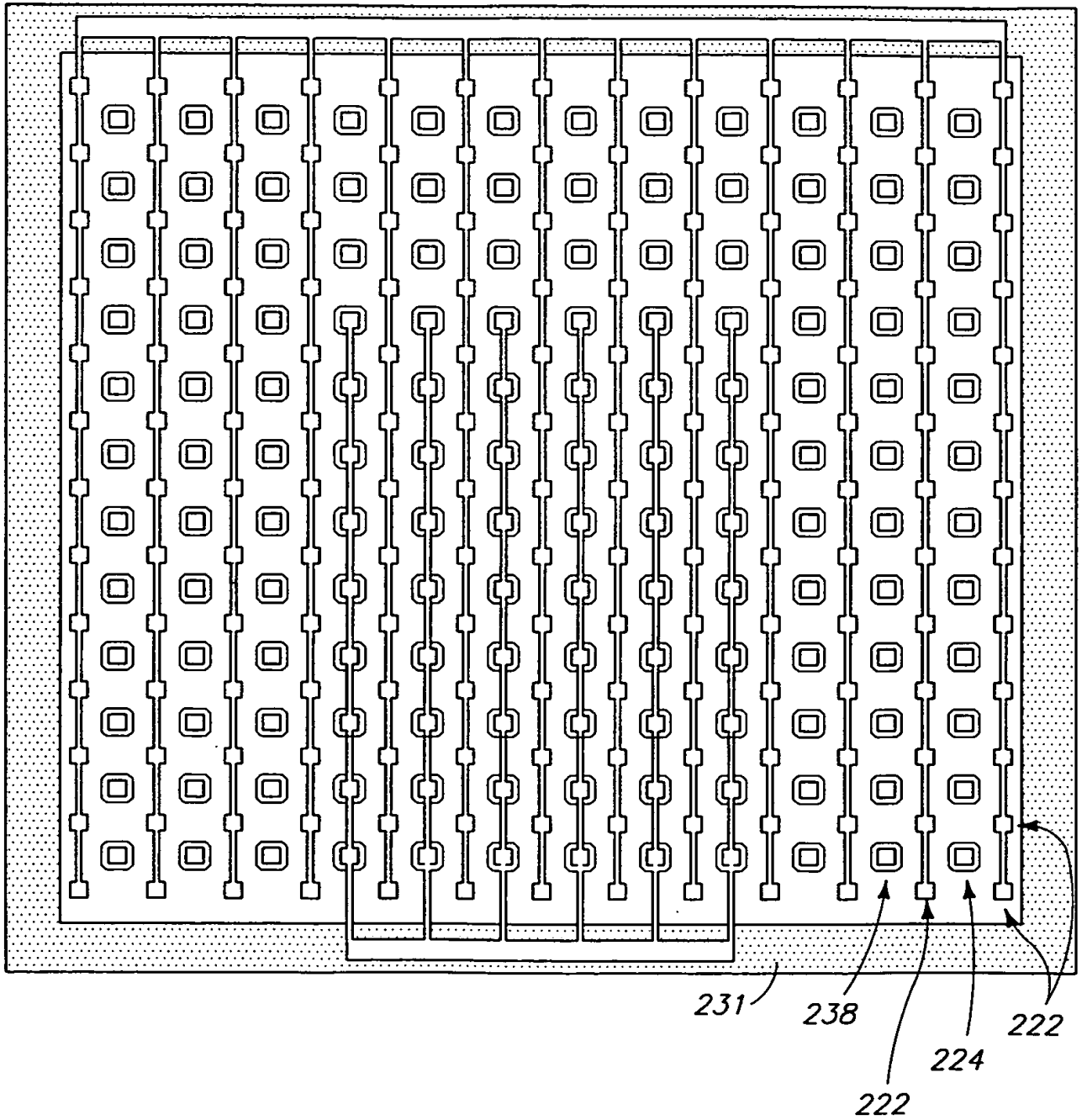
OFFICE OF THE ATTORNEY GENERAL



OFFICE OF THE ATTORNEY GENERAL

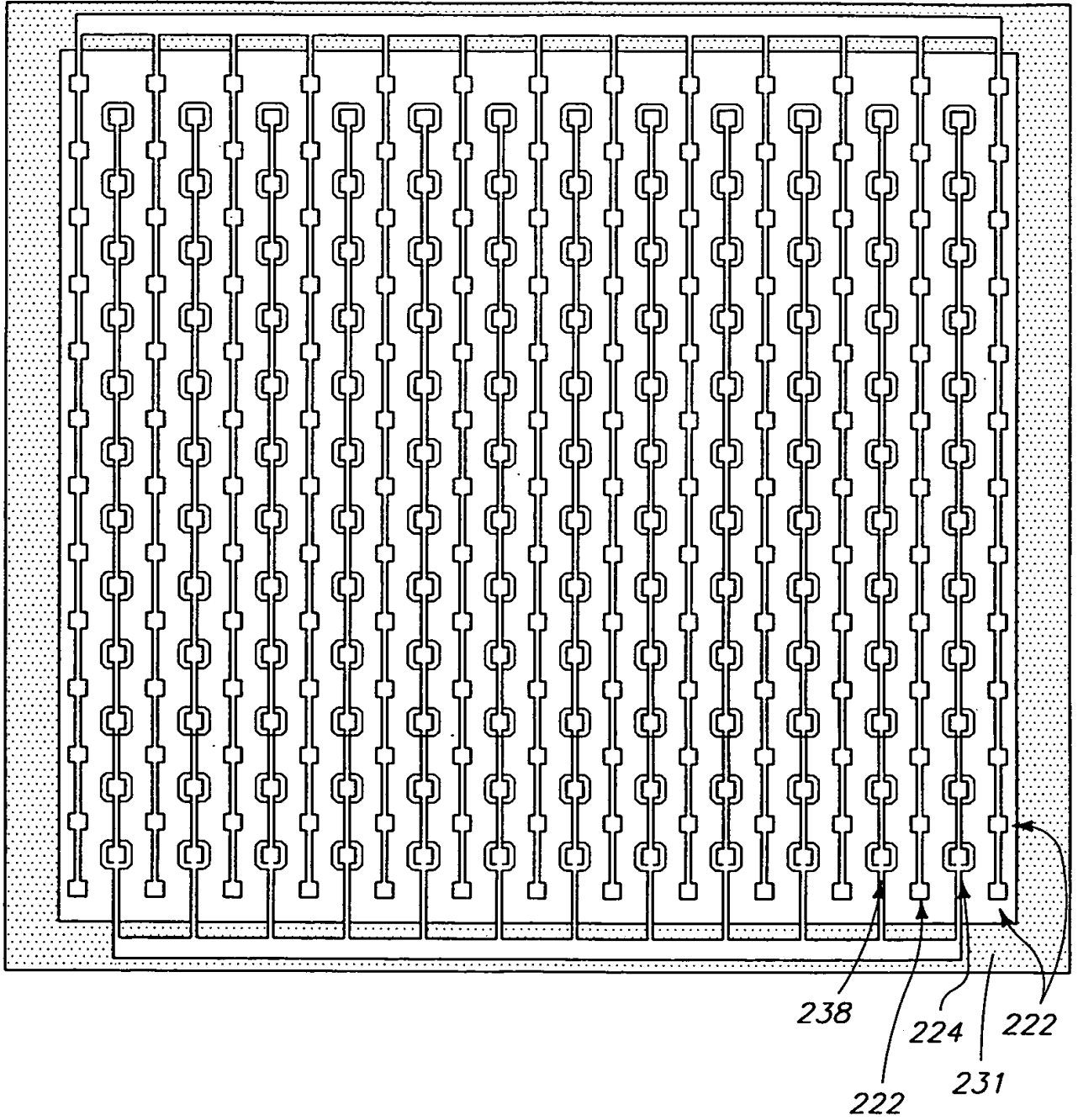


00440-03000000



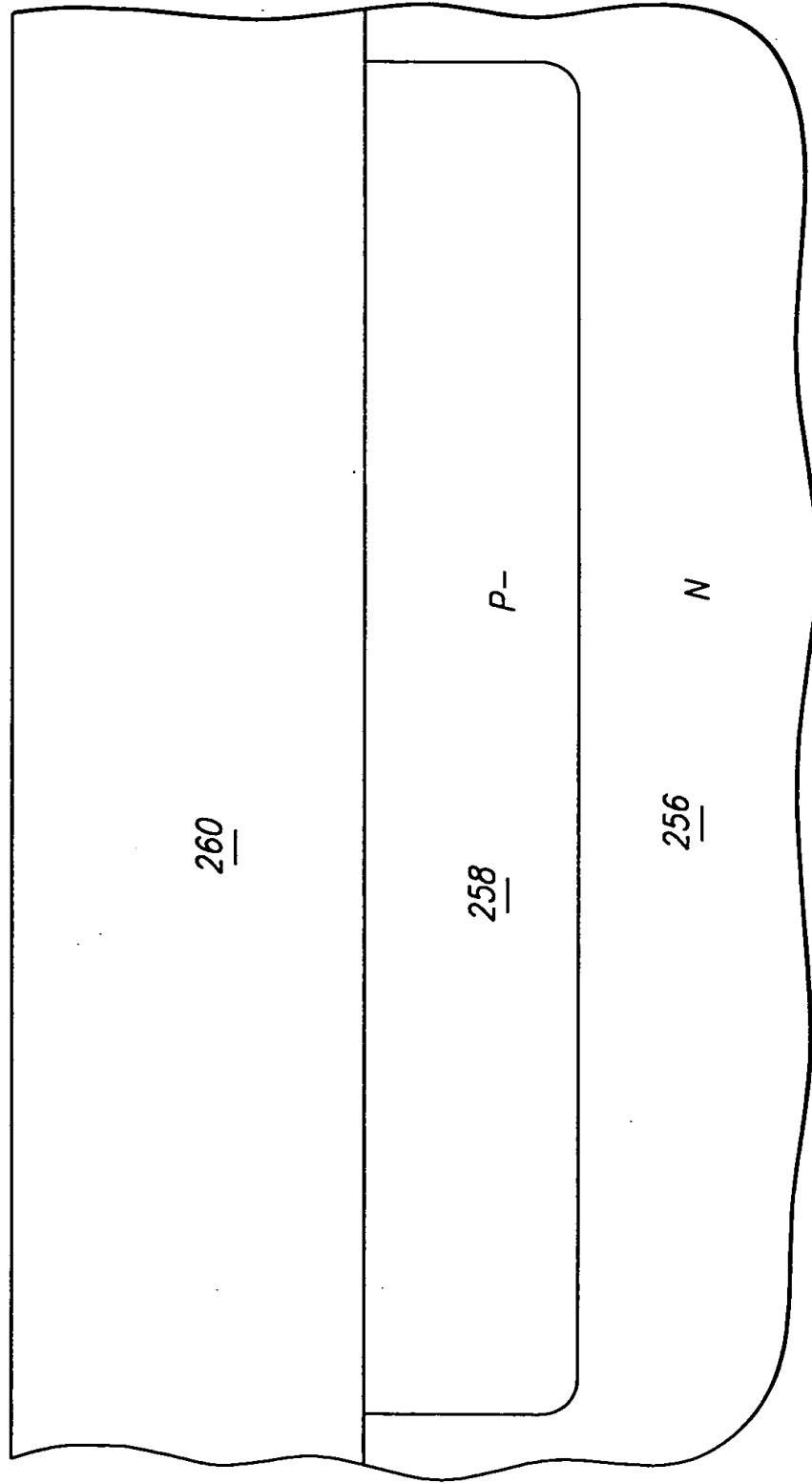
MI40-030

Illegible

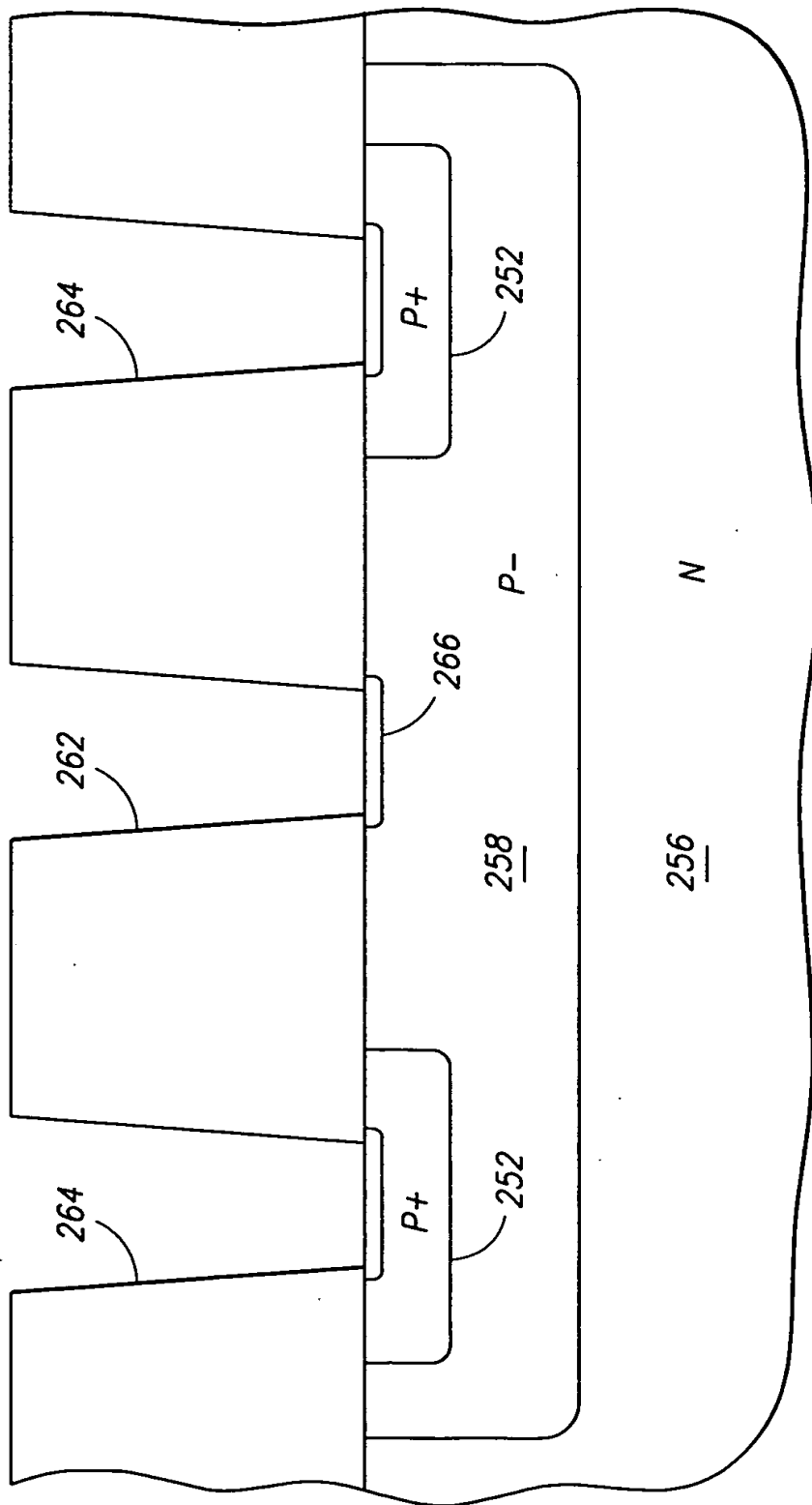


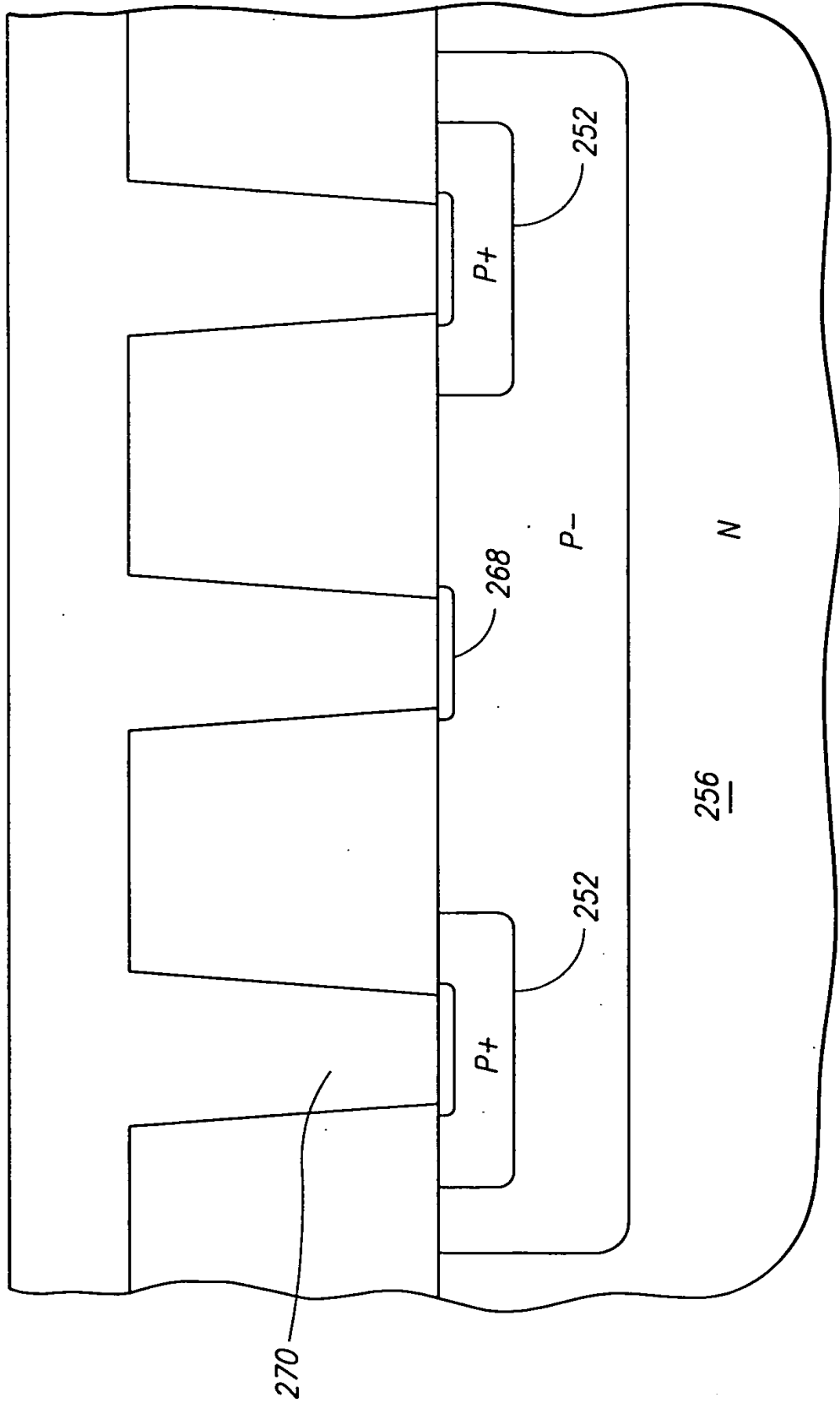
007720-20920900

MI40-030

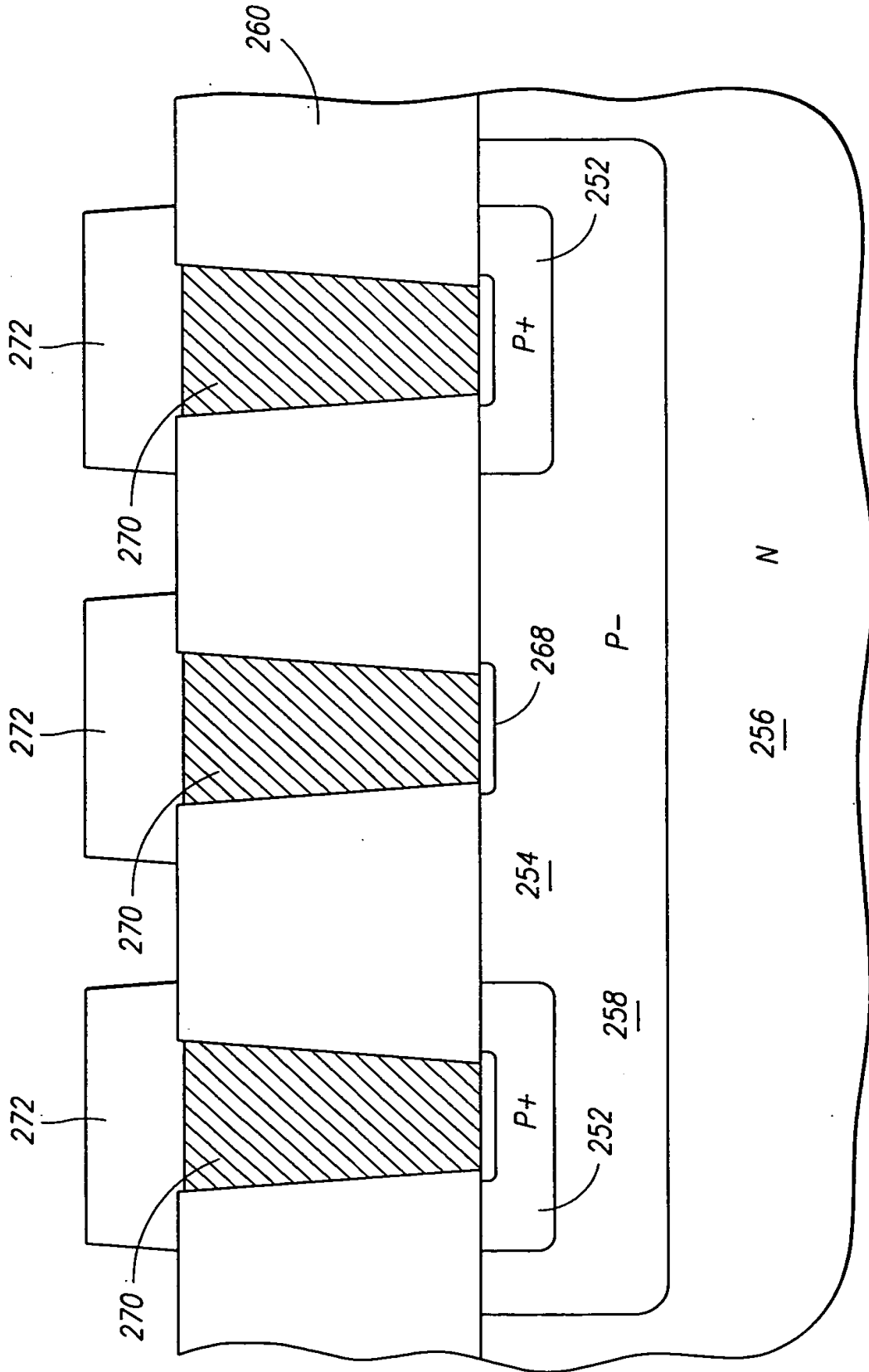


π π π π π π

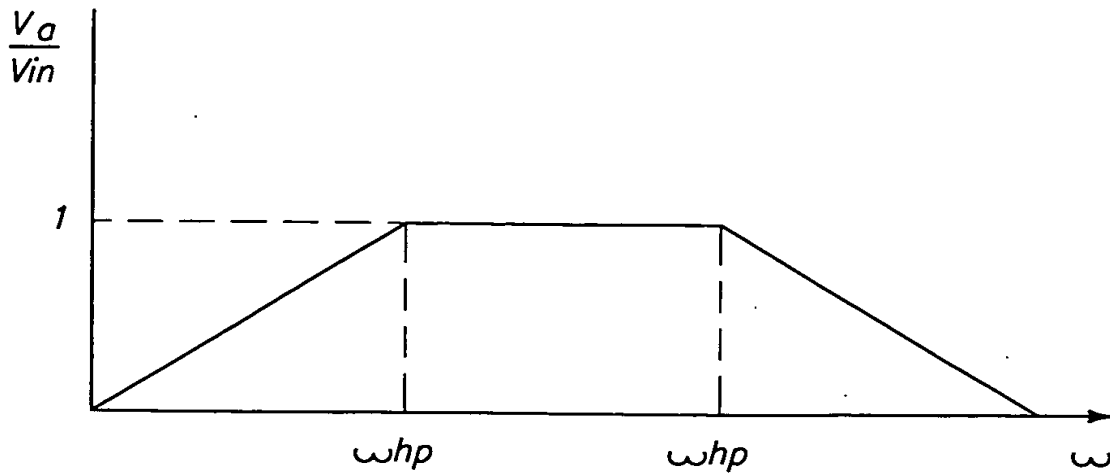
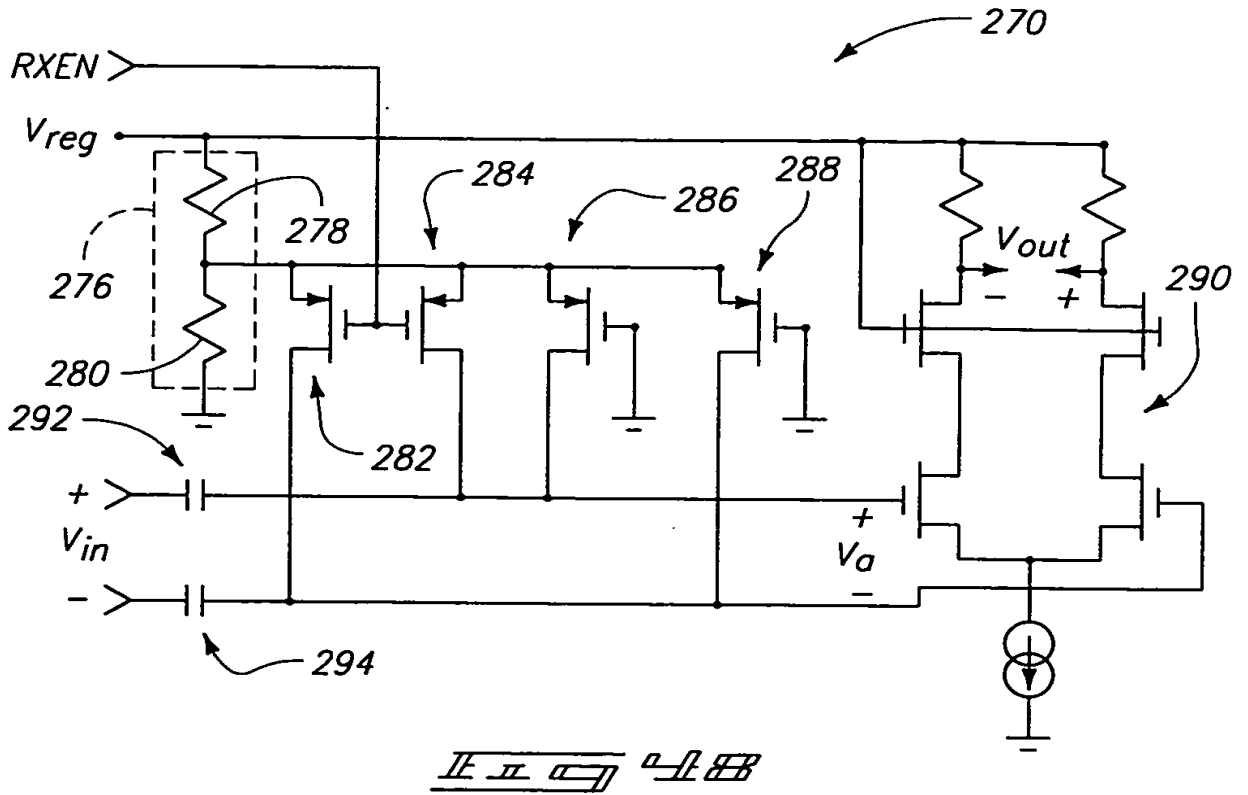




Б. П. Ефремов



MI 40



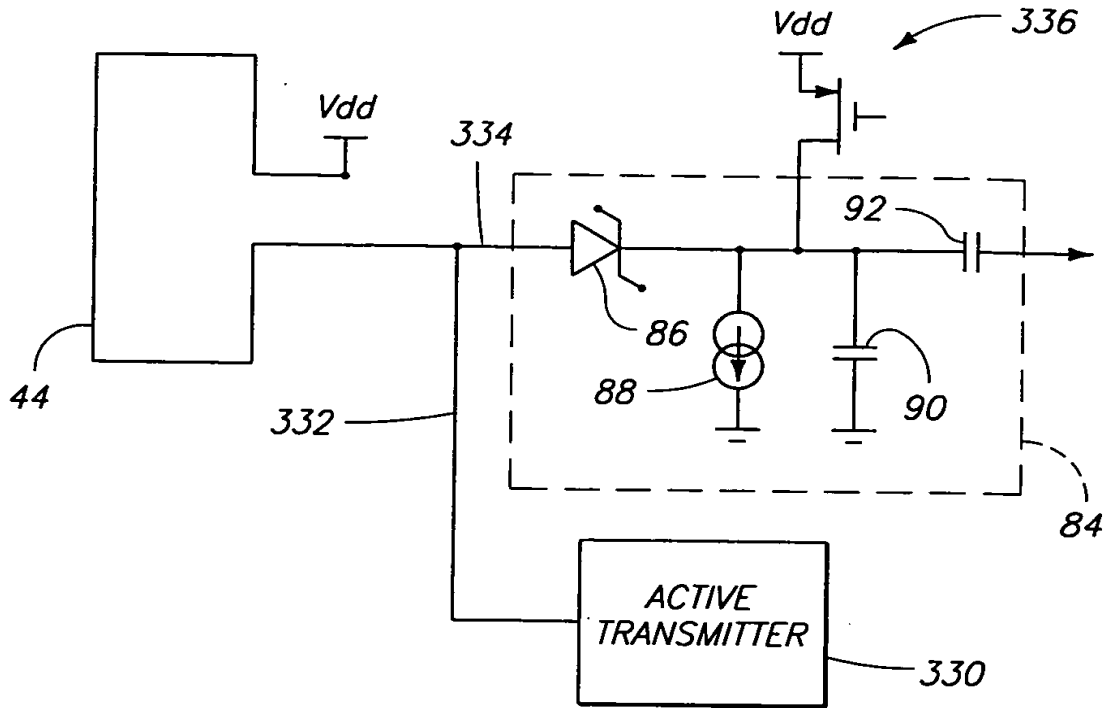


FIG. 50

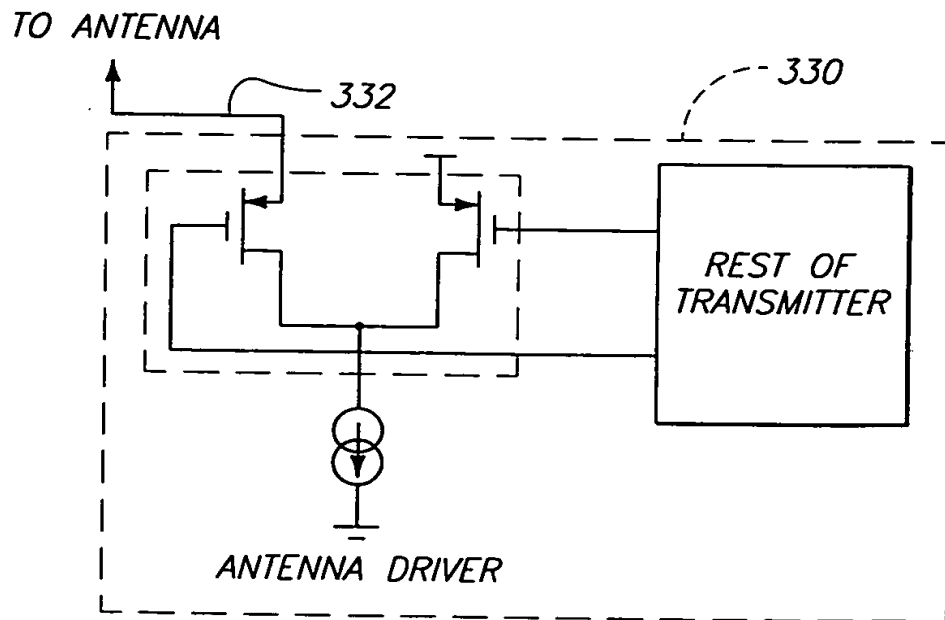


FIG. 51

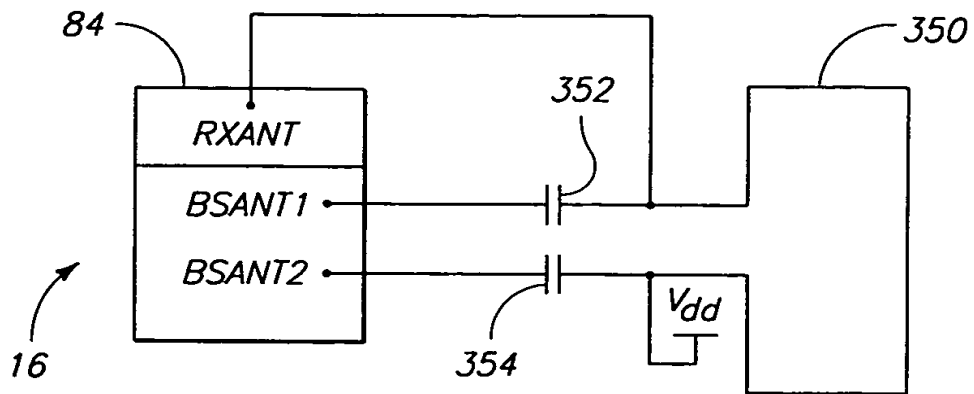


FIG. 5

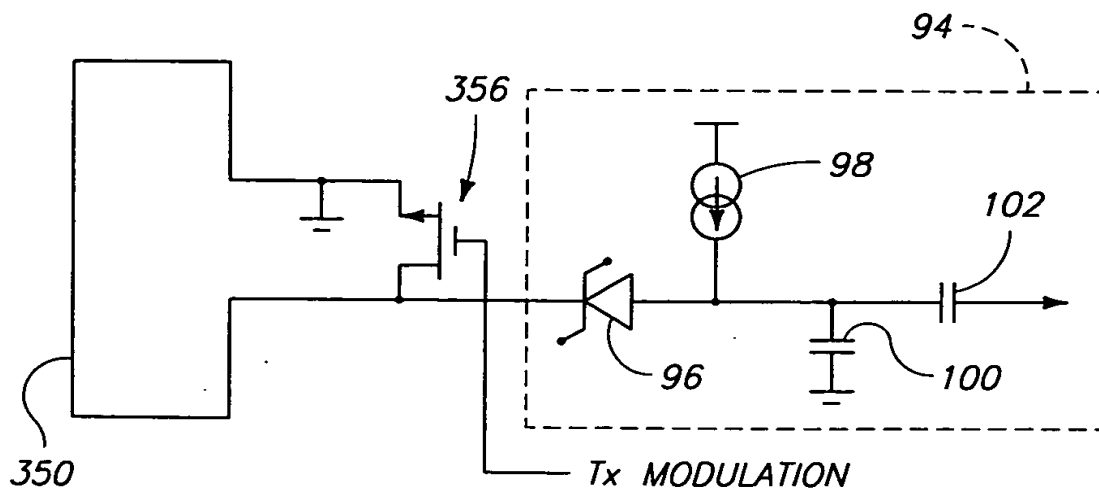
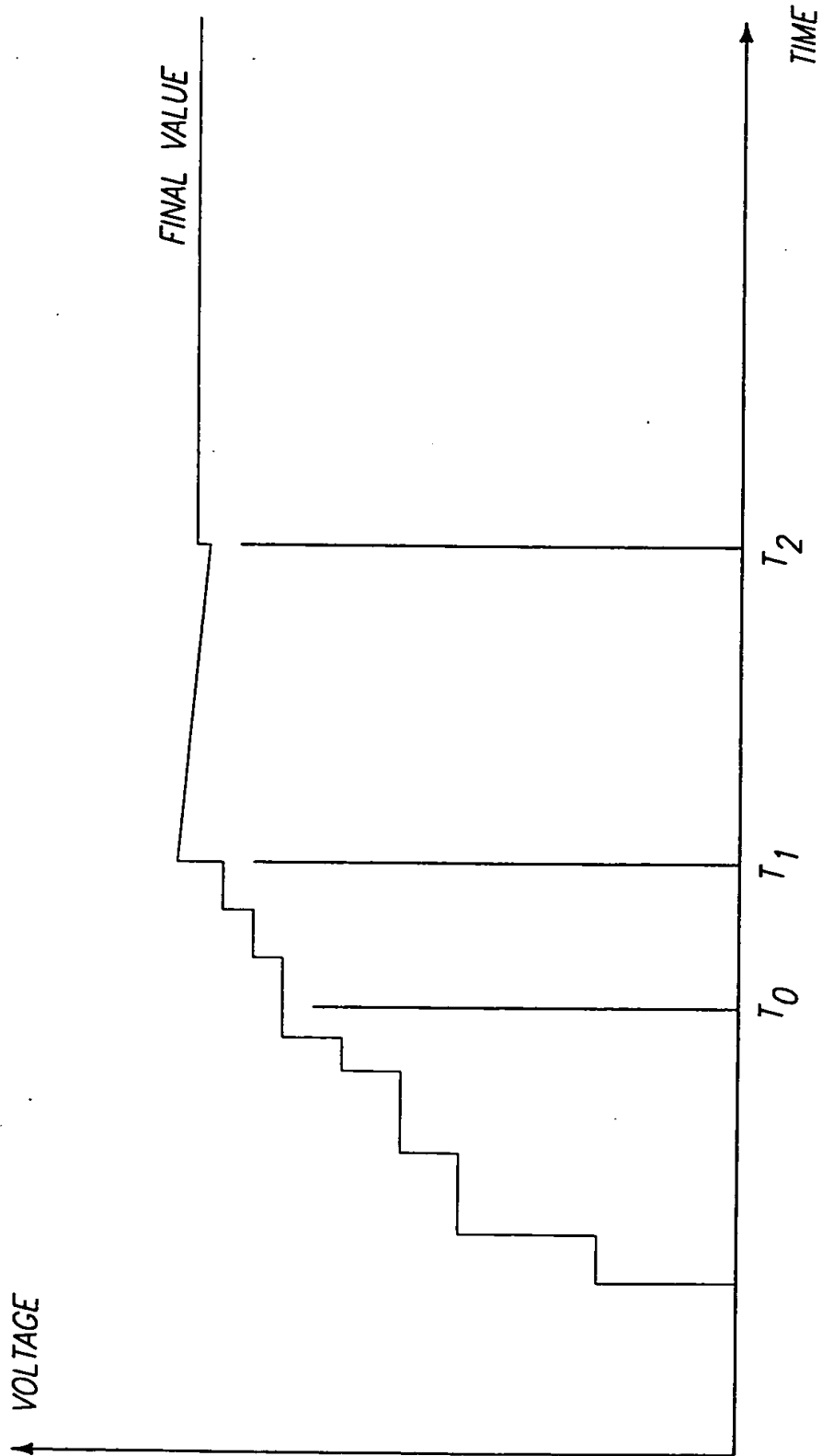


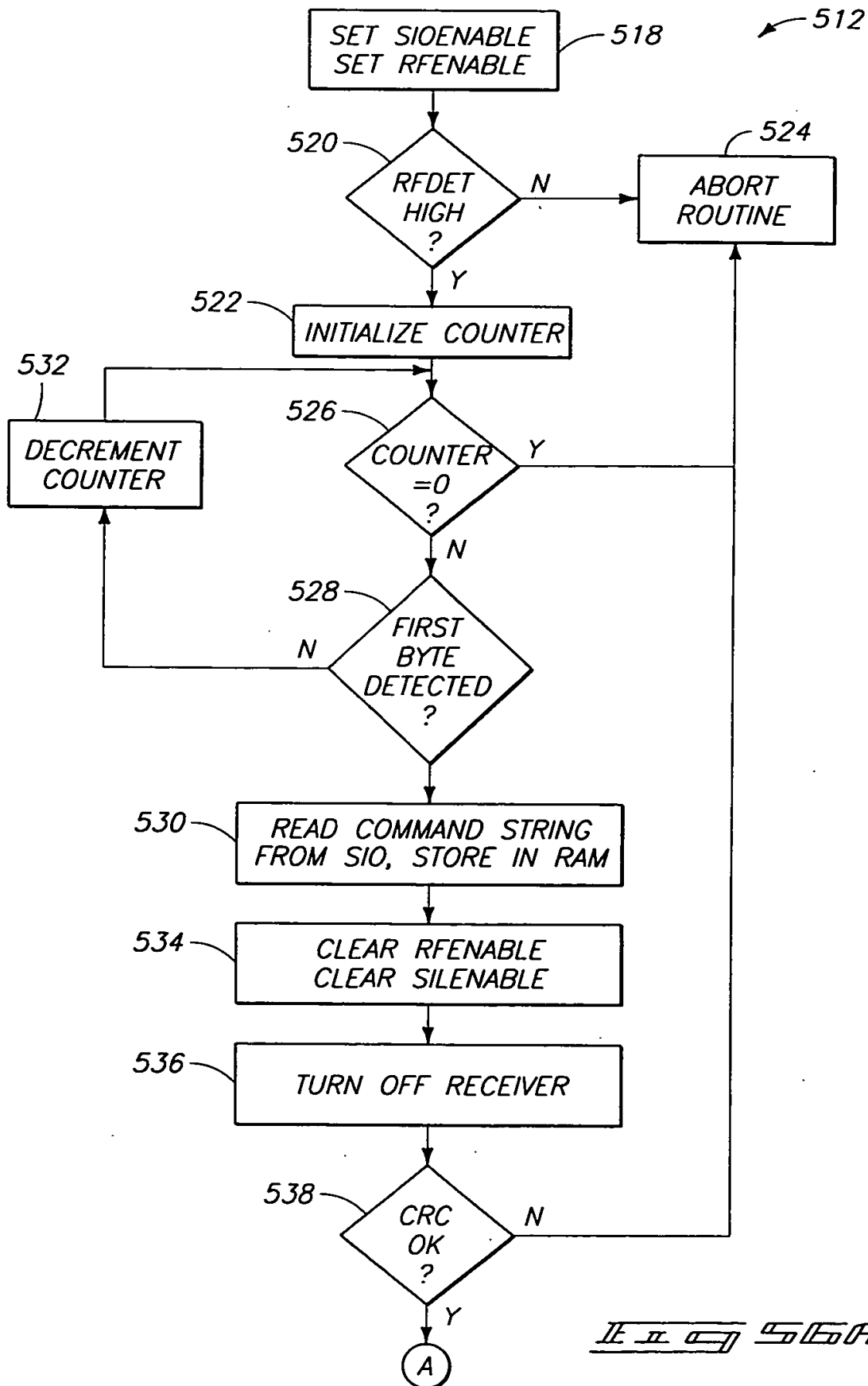
FIG. 6

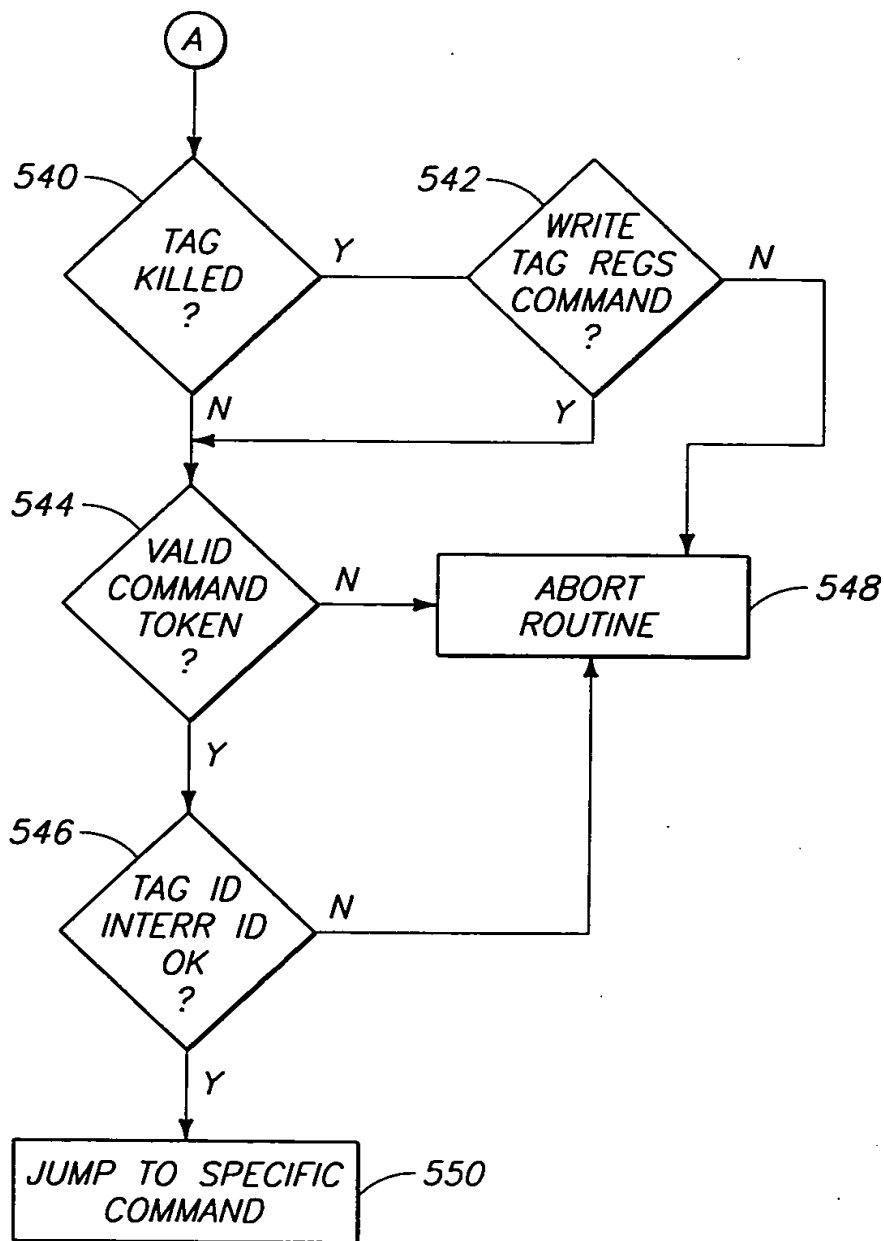


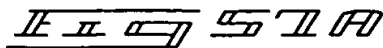
II 11 54

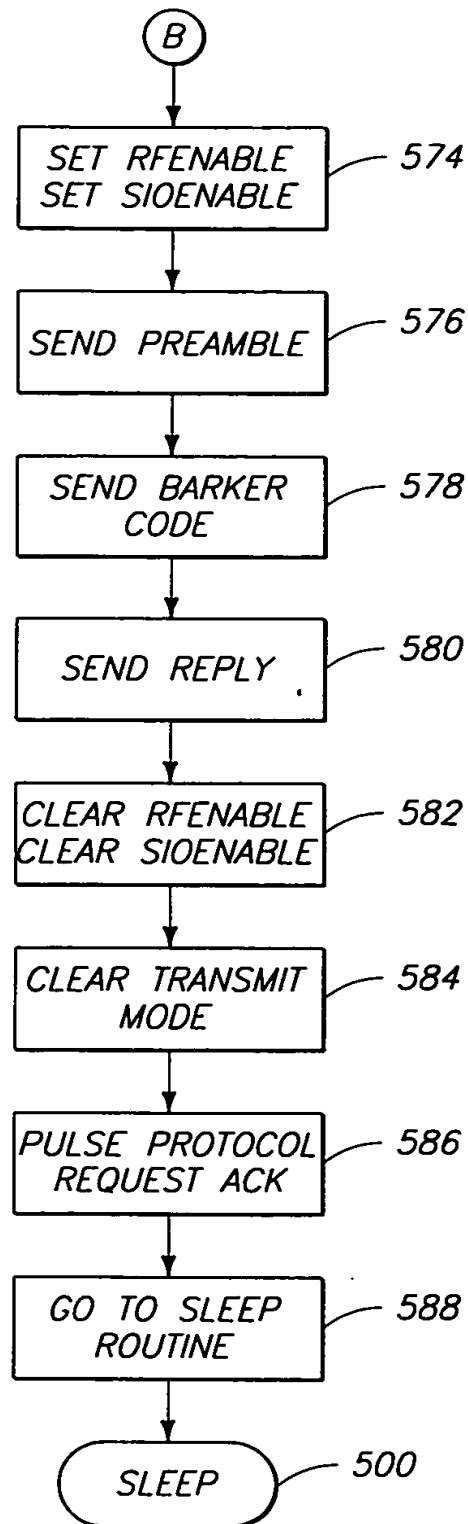
001100 00000000

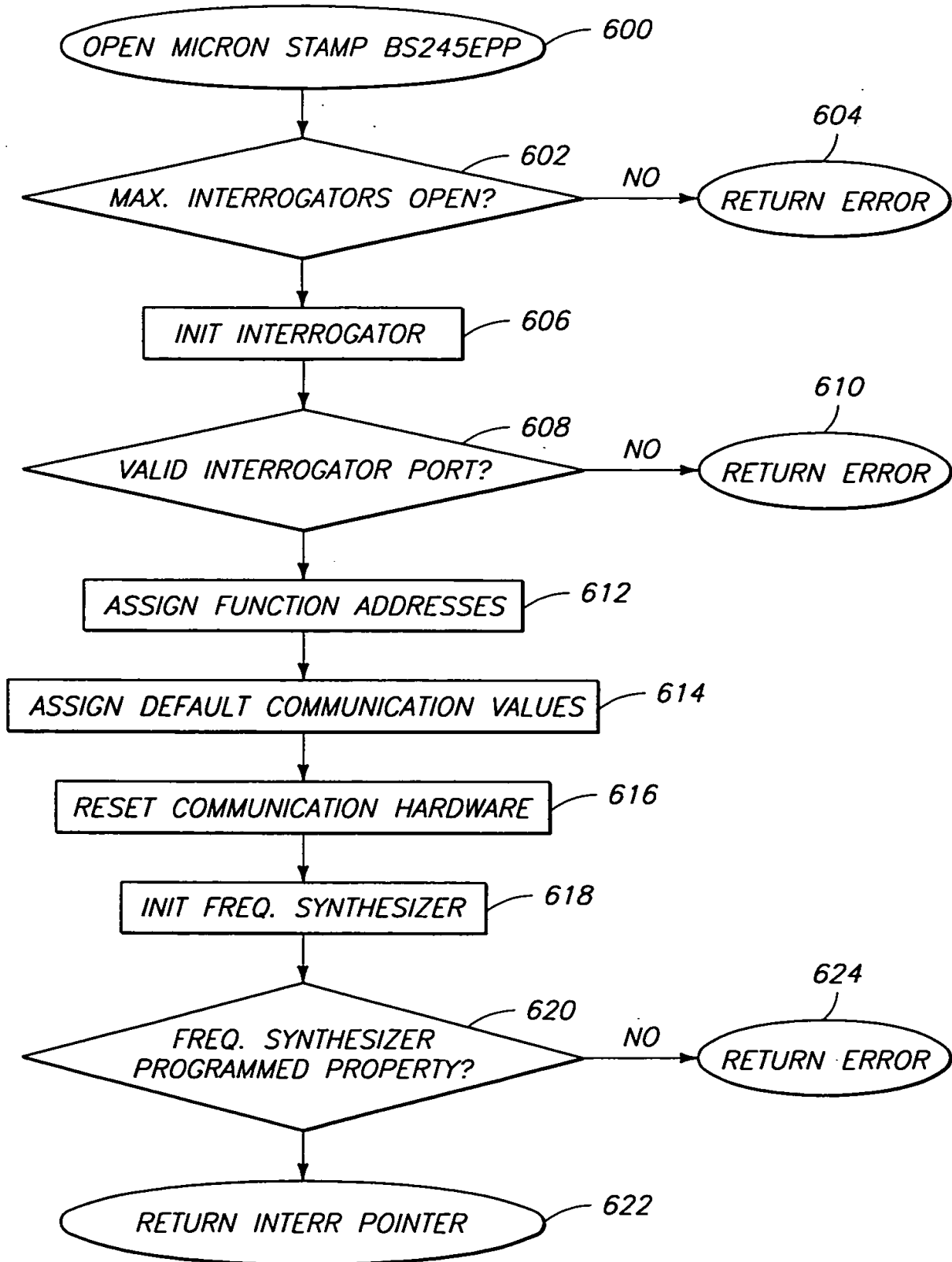


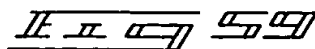


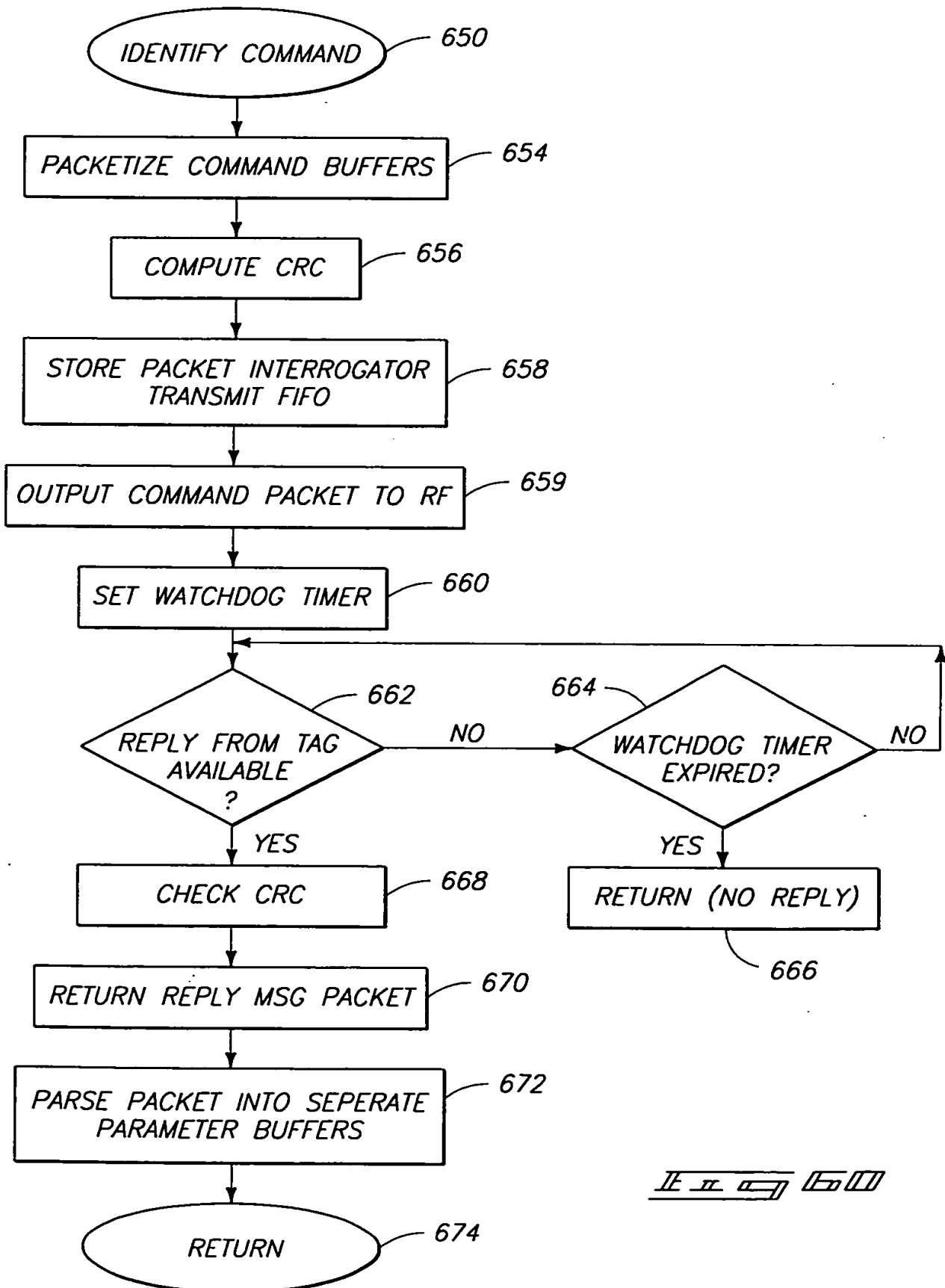




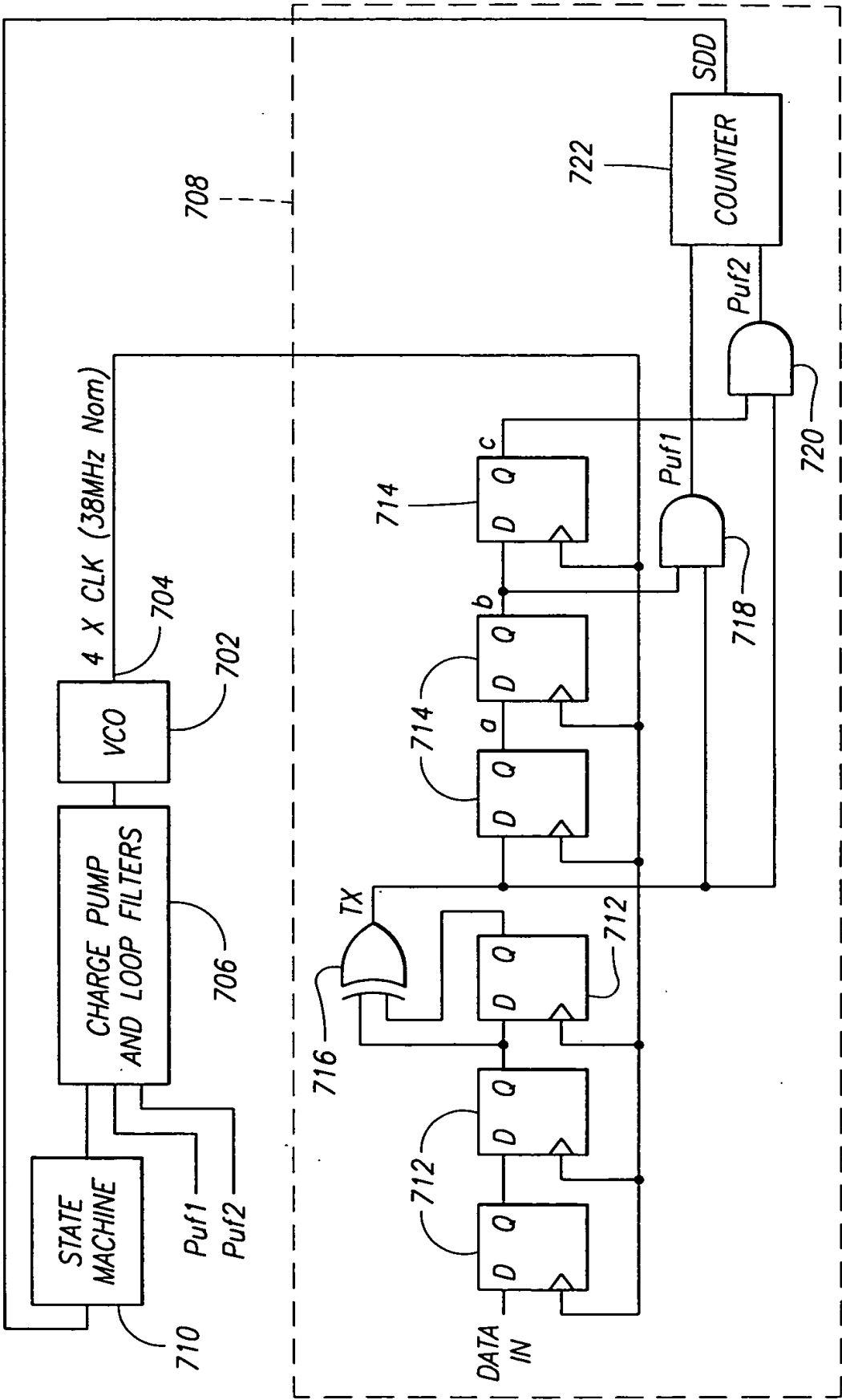




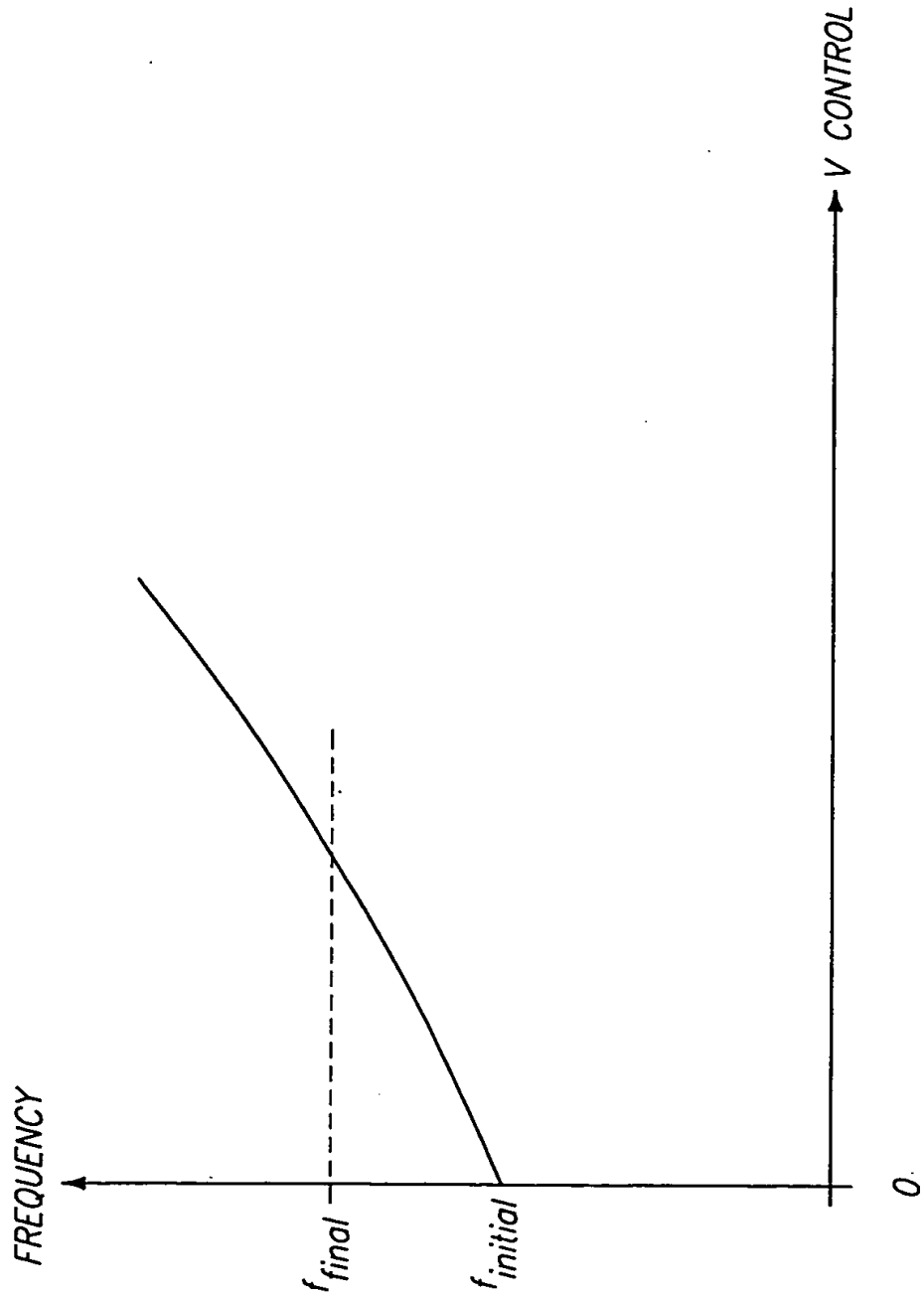


II II II II

700

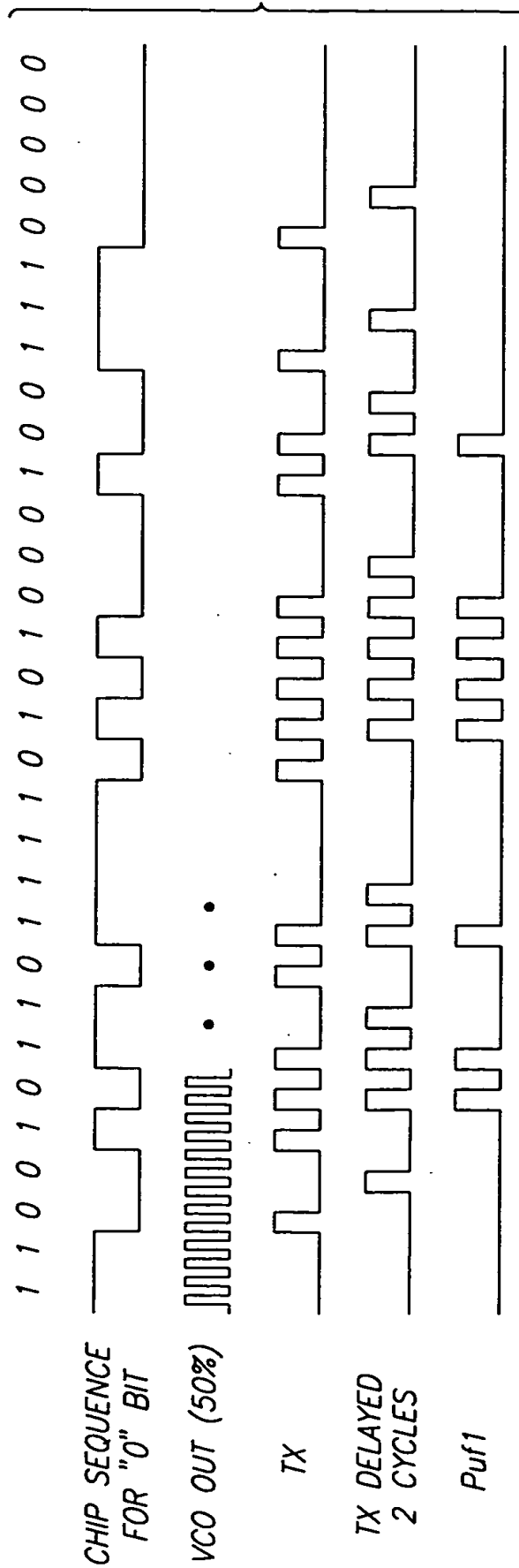


001420 00320500

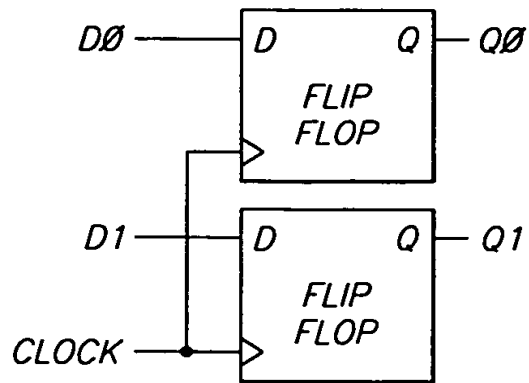
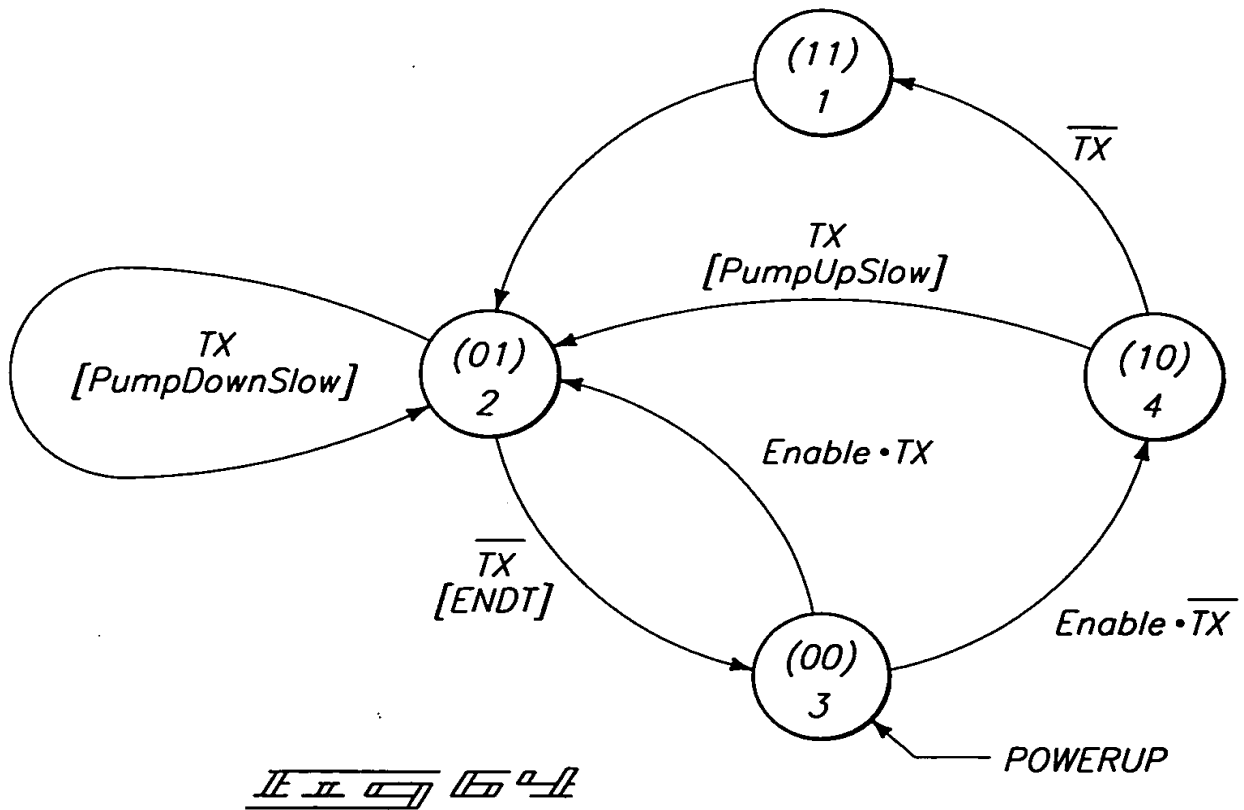


EX 62

004720 20520500



004720 20520500



ENABLE	PRESENT STATE				NEXT STATE	
	TX	Q1	Q0		D1	D0
0	0	0	0		0	0
0	1	0	0		0	0
1	0	0	0		1	0
1	1	0	0		0	1
X	0	0	1		0	0
X	1	0	1		0	1
X	X	1	1		0	1
X	0	1	0		1	1
X	1	1	0		0	1

11 00 00

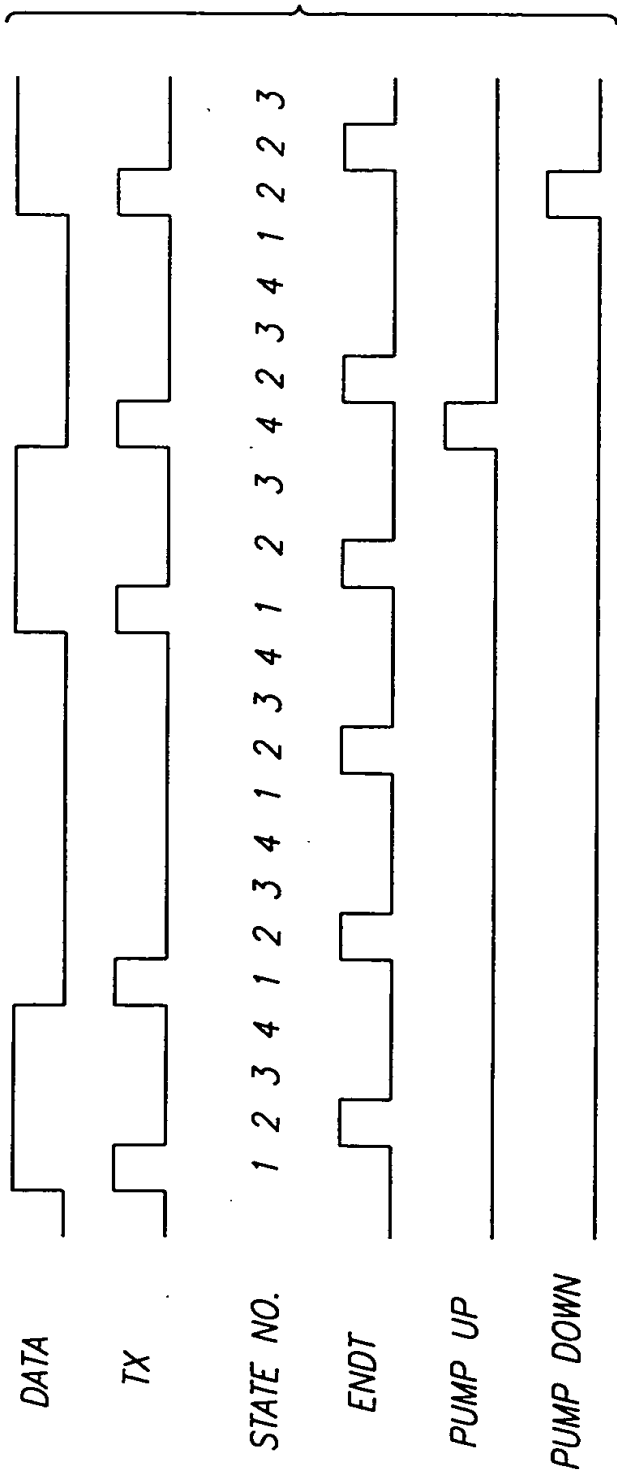
II II II II II

II II II II II



001420 20300000

MI40-030



MI 40 030

MI40-030

$$\frac{\pi}{\pi}$$

NAME	CURRENT (μA)	ΔV (mV)	$\Delta V/V$ CONTROL(NOM) $\times 100$
COARSE	40	160	13.3%
MEDIUM	10	40	3.3
MEDIUM FINE	1	2.6	0.22
FINE	0.1	0.26	0.022